

<IGBT Modules>

# **CM200EXS-34SA**

HIGH POWER SWITCHING USE INSULATED TYPE

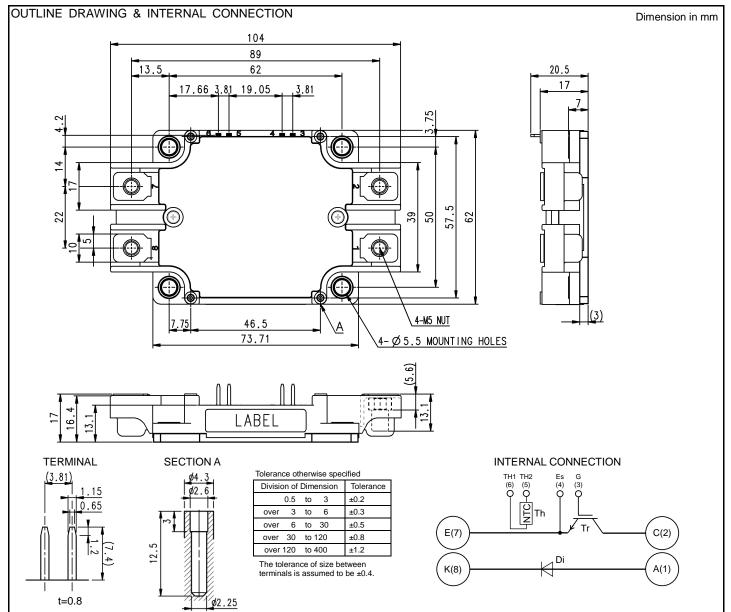


**Brake-chopper** 

- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

### **APPLICATION**

**Brake** 



Ver.1.3

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### MAXIMUM RATINGS (T<sub>j</sub>=25 °C, unless otherwise specified)

### IGBT PART

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1700	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T <sub>C</sub> =125 °C (Note1, 3)	200	۸
I <sub>CRM</sub>		Pulse, Repetitive (Note2)	400	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note1, 3)	2000	W

### DIODE PART

Symbol	Item	Conditions	Rating	Unit
$V_{RRM}$	Repetitive peak reverse voltage	-	1700	V
I <sub>F</sub>	Forward current	DC (Note1)	200	۸
I <sub>FRM</sub>	Forward current	Pulse, Repetitive (Note2)	400	A

### MODULE

Symbol	Item	Conditions	Rating	Unit
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note3)	125	C
T <sub>jop</sub>	Operating junction temperature	Continuous operation (under switching)	-40 ~ <b>+</b> 150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	C

### ELECTRICAL CHARACTERISTICS ( $T_j$ =25 °C, unless otherwise specified)

### **IGBT PART**

Cumbal	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		ī	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I <sub>C</sub> =20 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =200 A, V <sub>GE</sub> =15 V,	T <sub>j</sub> =25 °C	-	2.00	2.50	
V <sub>Cesat</sub>		Refer to the figure of test circuit.	T <sub>j</sub> =125 °C	-	2.20	-	V
(Terminal)	Callacter are the restriction valte as	(Note5)	T <sub>j</sub> =150 °C	-	2.25	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =200 A,	T <sub>j</sub> =25 °C	-	1.90	2.40	
V <sub>Cesat</sub>			T <sub>i</sub> =125 °C	1	2.10	-	V
(Chip)			T <sub>j</sub> =150 °C	ı	2.15	-	1
Cies	Input capacitance			-	-	53	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		1	-	4.3	nF
Cres	Reverse transfer capacitance			ı	-	0.97	1
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =1000 V, I <sub>C</sub> =200 A, V <sub>GE</sub> =15 V		-	1100	-	nC
t <sub>d(on)</sub>	Turn-on delay time	V 4000 V L 200 A V 45 V		-	-	400	
tr	Rise time	$V_{CC}$ =1000 V, $I_{C}$ =200 A, $V_{GE}$ =±15 V,		-	-	100	
t <sub>d(off)</sub>	Turn-off delay time	B. O.O. Industries Is ad		1	-	700	ns
t <sub>f</sub>	Fall time	$R_G$ =0 Ω, Inductive load		1	-	600	1
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =1000 V, I <sub>C</sub> =I <sub>E</sub> =200 A,		1	28	-	1
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=0 \Omega, T_{j}=150 \text{ °C},$		1	52	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25 °C (Note3)		-	-	2.0	mΩ
r <sub>g</sub>	Internal gate resistance	-		-	2.5	-	Ω

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HIGH POWER SWITCHING USE

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### ELECTRICAL CHARACTERISTICS (cont.; $T_j$ =25 °C, unless otherwise specified)

### DIODE PART

C: made al	ltana	Conditions		Limits			l lmit
Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
I <sub>RRM</sub>	Reverse current	$V_R = V_{RRM}$		-	-	1.0	mA
		I <sub>F</sub> =200 A,	T <sub>j</sub> =25 °C	-	4.1	5.3	
V <sub>F</sub>		Refer to the figure of test circuit.	T <sub>j</sub> =125 °C	-	2.9	-	V
(Terminal)	Emitter cellector veltage	(Note5)	T <sub>j</sub> =150 °C	-	2.7	-	
.,	- Emitter-collector voltage	I <sub>F</sub> =200 A,	T <sub>j</sub> =25 °C	-	4.0	5.2	
V <sub>F</sub>			T <sub>j</sub> =125 °C	-	2.8	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	2.6	-	
trr	Reverse recovery time	$V_{CC}$ =1000 V, $I_F$ =200 A, $V_{GE}$ =±15 V,		-	-	300	ns
Qrr	Reverse recovery charge	R <sub>G</sub> =0 Ω, Inductive load		-	8.0	-	μC
Err	Reverse recovery energy per pulse	$V_{CC}$ =1000 V, $I_F$ =200 A, $V_{GE}$ =±15 V, $R_G$ =0 $\Omega$ , $T_i$ =150 °C, Inductive load		-	42	-	mJ

### NTC THERMISTOR PART

Symbol	Itom	Conditions	Limits		Unit	
Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note3)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note3)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note5)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note3)	-	-	10	mW

### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Itom	Conditions	Limits		Unit	
Symbol	ltem	Conditions	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Thermal registance	Junction to case, per IGBT (Note3)	-	-	0.075	K/W
R <sub>th(j-c)D</sub>	Thermal resistance	Junction to case, per DIODE (Note3)	-	-	0.12	N/VV
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note3, 6)	-	25	-	K/kW

### MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
$M_t$	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	NI m
Ms	- Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N⋅m
d	Creepage distance	Terminal to terminal		20.6	-	-	mm
d <sub>s</sub>		Terminal to base plate		17	-	-	
4	Clearance distance	Terminal to terminal		12	-	-	
d <sub>a</sub>	Clearance distance	Terminal to base plate		10.6	-	-	mm
m	mass	-		-	210	-	g
e <sub>c</sub>	Flatness of base plate	On the centerline X, Y (Note7)		-100	-	+100	μm

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### HIGH POWER SWITCHING USE

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This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.

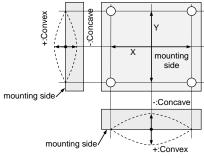
- 2. Pulse width and repetition rate should be such that the device junction temperature (T<sub>j</sub>) dose not exceed T<sub>jmax</sub> rating.
- 3. Case temperature (T<sub>C</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

5. 
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}$ : resistance at absolute temperature  $T_{25}$  [K];  $T_{25}$ =25 [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}$ =50 [°C]+273.15=323.15 [K]

- 6. Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m·K).
- 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



8. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

"φ2.6×10 or φ2.6×12, B1 tapping screw"

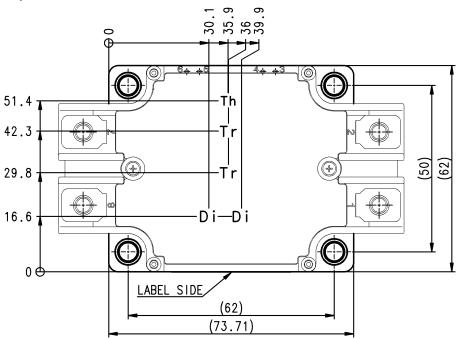
The length of the screw depends on the thickness (t1.6~t2.0) of the PCB.

### RECOMMENDED OPERATING CONDITIONS

Cymphol	ltom	Conditions		Limits		Unit
Symbol	Item	Conditions	Min.	Тур.	Max.	Onit
Vcc	(DC) Supply voltage	Applied across C-E/A-K	-	1000	1200	V
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across G-Es	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	-	0	-	38	Ω

### CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

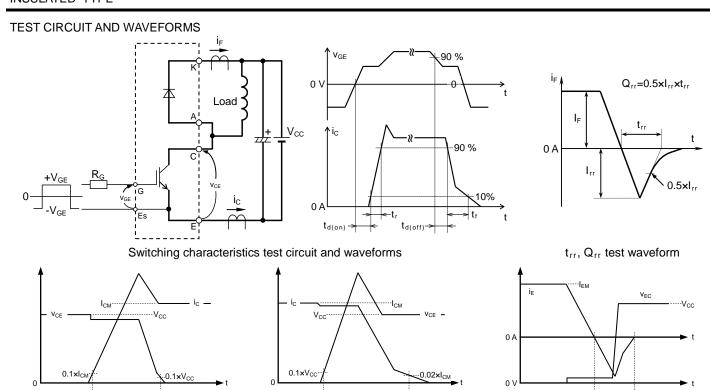


Tr: IGBT, Di: DIODE, Th: NTC thermistor

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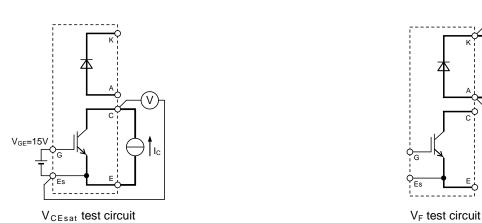
HIGH POWER SWITCHING USE INSULATED TYPE



IGBT Turn-off switching energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

### **TEST CIRCUIT**

IGBT Turn-on switching energy



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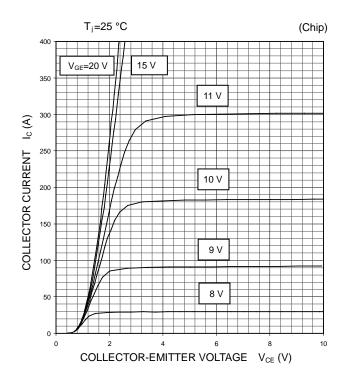
DIODE Reverse recovery energy

HIGH POWER SWITCHING USE INSULATED TYPE

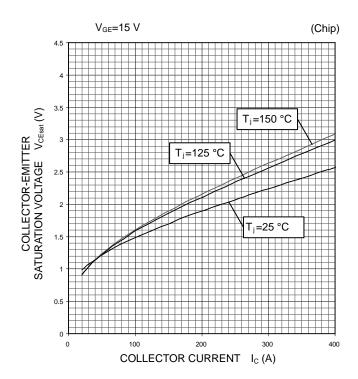
### PERFORMANCE CURVES

IGBT / DIODE PART

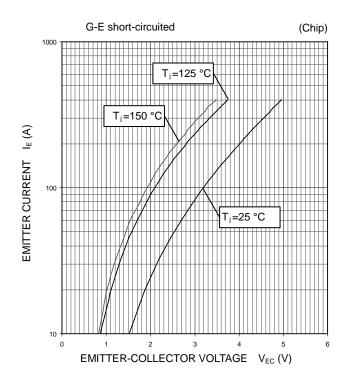
# OUTPUT CHARACTERISTICS (TYPICAL)



#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



DIODE FORWARD CHARACTERISTICS (TYPICAL)



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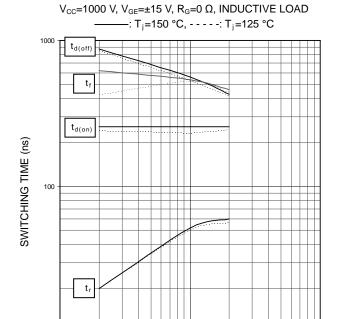
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### PERFORMANCE CURVES

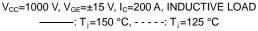
#### IGBT / DIODE PART

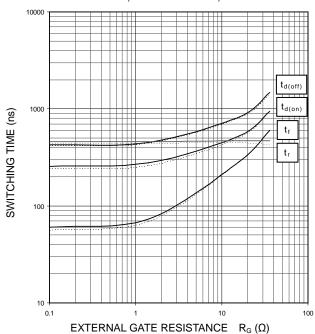
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#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



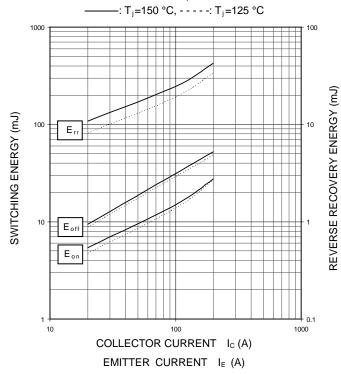
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



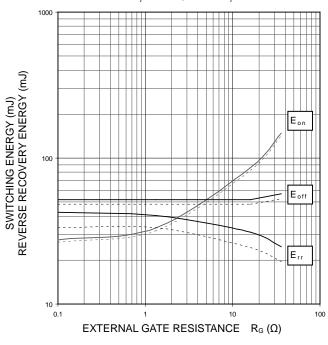


# HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) $V_{\text{CC}}=1000 \text{ V, } V_{\text{GE}}=\pm15 \text{ V, } R_{\text{G}}=0 \text{ } \Omega,$ INDUCTIVE LOAD, PER PULSE

COLLECTOR CURRENT Ic (A)



### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) $V_{CC}=1000 \text{ V}, V_{GE}=\pm15 \text{ V}, I_{C}=200 \text{ A},$ INDUCTIVE LOAD, PER PULSE ———: $T_i=150 \text{ °C}, ----: T_i=125 \text{ °C}$



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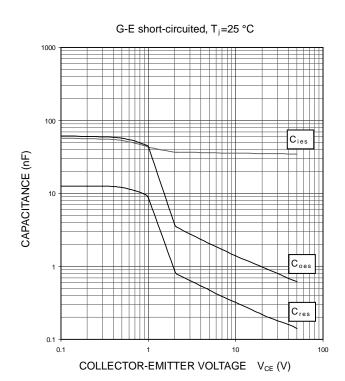
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### PERFORMANCE CURVES

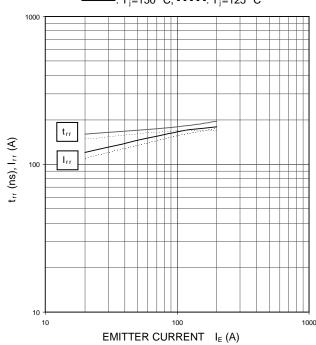
IGBT / DIODE PART

# CAPACITANCE CHARACTERISTICS (TYPICAL)

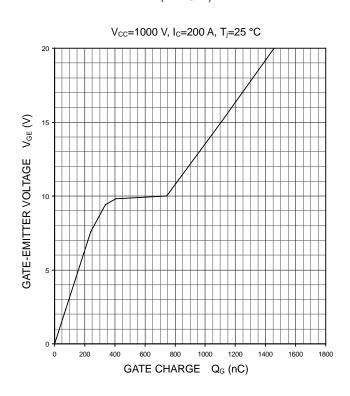


#### DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =1000 V,  $V_{GE}$ =±15 V,  $R_{G}$ =0  $\Omega$ , INDUCTIVE LOAD ———:  $T_{j}$ =150 °C, - - - - -:  $T_{j}$ =125 °C

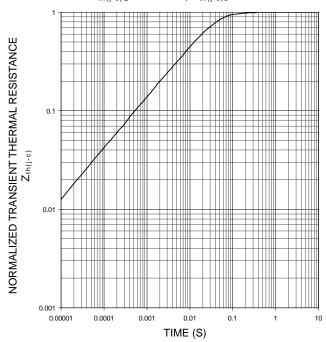


# GATE CHARGE CHARACTERISTICS (TYPICAL)



# TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T<sub>C</sub>=25 °C  $R_{th(j-c)Q}$ =0.075 K/W,  $R_{th(j-c)D}$ =0.12 K/W



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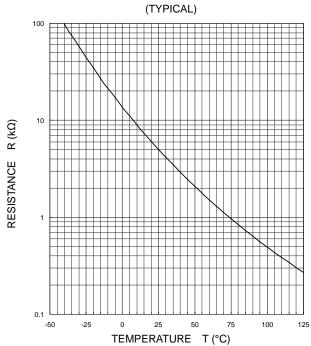
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HIGH POWER SWITCHING USE INSULATED TYPE

### PERFORMANCE CURVES

NTC thermistor part

# TEMPERATURE CHARACTERISTICS



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### Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

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