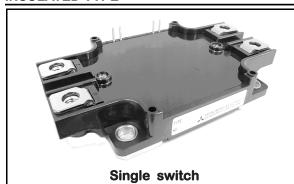


< IGBT MODULES >

CM150EXS-24S

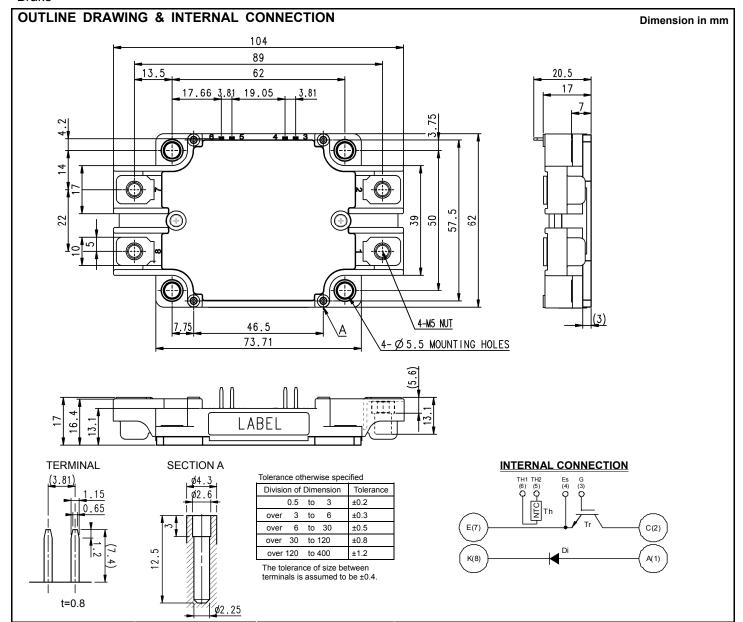
HIGH POWER SWITCHING USE INSULATED TYPE



- Flat base Type
- Copper base plate (non-plating)
- •Tin plating pin terminals
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

Brake



< IGBT MODULES > CM150EXS-24S HIGH POWER SWITCHING USE INSULATED TYPE

ABSOLUTE MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified) IGBT

IODI				
Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =120 °C (Note1, 3)	150	^
I _{CRM}	Collector current	Pulse, Repetitive (Note2)	300	A
P _{tot}	Total power dissipation	T _C =25 °C (Note1, 3)	1150	W

DIODE

Symbol	Item	Conditions	Rating	Unit
V_{RRM}	Repetitive peak reverse voltage	-	1200	V
I _F	Forward current	(Note1)	150	^
I _{FRM}	Forward current	Pulse, Repetitive (Note2)	300	A

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{jmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C
T _{Cmax}	Maximum case temperature	(Note3)	125	
T _{jop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T _{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified)

Cumbal	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =15 mA, V _{CE} =10 V		5.4	6.0	6.6	V
		I _C =150 A (Note4),	T _j =25 °C	-	1.80	2.25	
		V _{GE} =15 V,	T _j =125 °C	-	2.00	-	V
VcEsat	Collector emitter acturation valtage	(Terminal)	T _j =150 °C	-	2.05	-	μA V
	Collector-emitter saturation voltage	I _C =150 A (Note4),	T _j =25 °C	-	1.70	2.15	
		V _{GE} =15 V,	T _j =125 °C	-	1.90	-	V
		(Chip)	T _j =150 °C	-	1.95	-	1
Cies	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	15	nF
Coes	Output capacitance			-	-	3.0	
Cres	Reverse transfer capacitance			-	-	0.25	
Q _G	Gate charge	V _{CC} =600 V, I _C =150 A, V _{GE} =15	V	-	350	-	nC
t _{d(on)}	Turn-on delay time	V -000 V I -450 A V -145	- \/	-	-	800	
tr	Rise time	V_{CC} =600 V, I_{C} =150 A, V_{GE} =±15	o v,	-	-	200	200
t _{d(off)}	Turn-off delay time	D =0.0 Industrial land		-	-	600	118
t _f	Fall time	R_G =0 Ω, Inductive load		-	-	300	
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =150 A, V _{GE} =±15 V,		-	24.2	-	m l
E _{off}	Turn-off switching energy per pulse	R_G =0 Ω, T_j =150 °C, Inductive	load	-	16	-	IIIJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per element, T _C =25 °C (Note3)		-	-	2.0	mΩ
r _g	Internal gate resistance	-		-	13	-	Ω

< IGBT MODULES > CM150EXS-24S HIGH POWER SWITCHING USE INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; T $_{\rm j}$ =25 °C, unless otherwise specified)

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
I _{RRM}	Reverse current	V _R =V _{RRM}	$V_R = V_{RRM}$		-	1.0	mA
		I _F =150 A (Note4),	T _j =25 °C	-	1.8	2.25	
			T _j =125 °C	-	1.8	-	V
V_{F}	Forward voltage	(Terminal)	T _j =150 °C	-	1.8	-	
	Forward voltage	I _F =150 A (Note4),	T _j =25 °C	-	1.7	2.15	
			T _j =125 °C	-	1.7	-	V
		(Chip)	T _j =150 °C	-	1.7	-	
t _{rr}	Reverse recovery time	V _{CC} =600 V, I _F =150 A, V _{GE} =±1	5 V,	-	-	300	ns
Qrr	Reverse recovery charge	R _G =0 Ω, Inductive load		-	8.0	-	μC
_	Poverse receivery energy per pulse	V _{CC} =600 V, I _F =150 A, V _{GE} =±1	5 V,		40.0		mJ
Err	Reverse recovery energy per pulse	$R_G=0 \Omega$, $T_j=150 °C$, Inductive	load	-	12.2	-	IIIJ

NTC THERMISTOR PART

Symbol Item	Itom	Conditions		Limits		- Unit
	Conditions	Min.	Тур.	Max.	Offic	
R ₂₅	Zero-power resistance	T _C =25 °C (Note3)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note3)	-7.3	-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note5)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note3)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itom	Conditions		Limits		Unit
	Conditions	Min.	Тур.	Max.	Utill	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, IGBT (Note3)	-	-	0.13	K/W
$R_{th(j-c)D}$	mermai resistance	Junction to case, DIODE (Note3)	-	-	0.23	r √ v v
В	Contact the amend we sintend	Case to heat sink, per 1 module,		0.5		K/kW
$R_{th(c-s)}$	Contact thermal resistance	Thermal grease applied (Note3, 6)	-	25	-	r./KVV

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit	
Syllibol	item	Conditions		Min.	Тур.	Max.	Offic	
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N·m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m	
d	Creepage distance	Terminal to terminal		20.6	-	-	mm	
d _s		Terminal to base plate		17	-	-	111111	
d	Clearance	Terminal to terminal		12	-	-	mm	
d _a	Clearance	Terminal to base plate		10.6	-	-	mm	
m	mass	-		-	210	-	g	
ес	Flatness of base plate	On the centerline X, Y (Note7)		-100	-	+100	μm	

< IGBT MODULES > CM150EXS-24S

HIGH POWER SWITCHING USE INSULATED TYPE

Note1. Junction temperature (T_i) should not increase beyond T_{imax} rating.

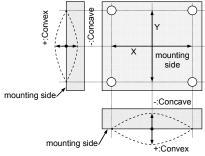
- 2. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed T_{jmax} rating.
- 3. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
 Refer to the figure of test circuit.

5.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$
,

 R_{25} : resistance at absolute temperature T_{25} [K]; T_{25} =25 [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.



8. Use the following screws when mounting the printed circuit board (PCB) on the stand offs.

"φ2.6×10 or φ2.6×12 self tapping screw"

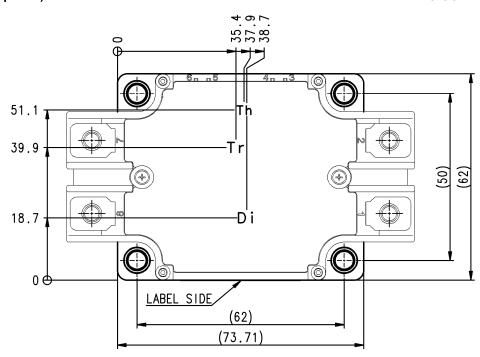
The length of the screw depends on thickness (t1.6~t2.0) of the PCB.

RECOMMENDED OPERATING CONDITIONS

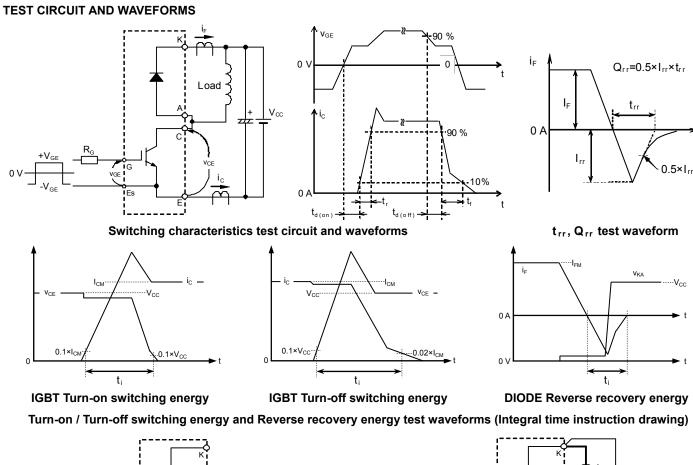
Symbol	Item	Conditions		Limits		Unit
Symbol	item	Conditions	Min.	Тур.	Max.	Offic
Vcc	(DC) Supply voltage	Applied across C-E/A-K terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G-Es terminals	13.5	15.0	16.5	V
R_G	External gate resistance	-	0	-	30	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm



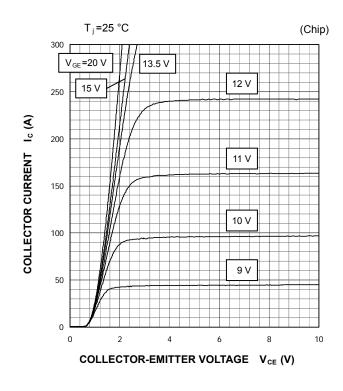
Tr: IGBT, Di: DIODE, Th: NTC thermistor



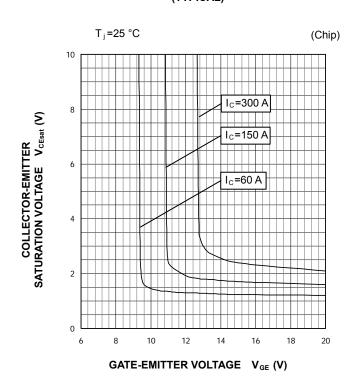


PERFORMANCE CURVES IGBT/DIODE PART

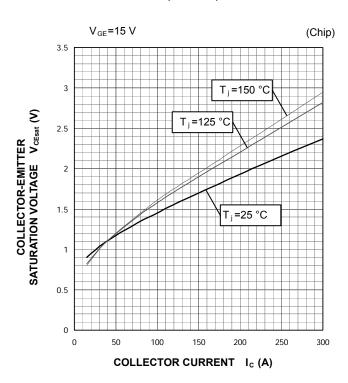
OUTPUT CHARACTERISTICS (TYPICAL)



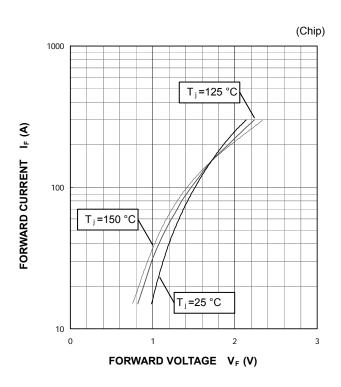
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



CLAMP DIODE FORWARD CHARACTERISTICS (TYPICAL)

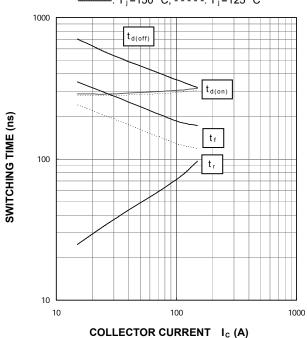


PERFORMANCE CURVES

IGBT/DIODE PART

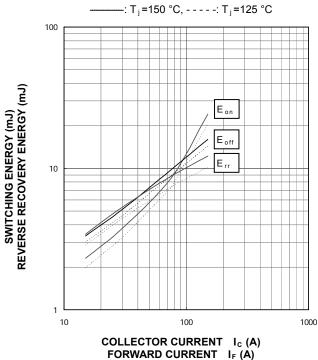
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_{G} =0 Ω , INDUCTIVE LOAD –: T_j=150 °C, - - - - : T_j=125 °C



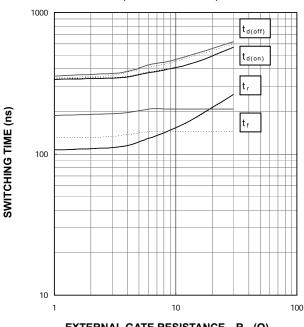
HALF-BRIDGE SWITCHING CHARACTERISTICS

(TYPICAL) V_{CC} =600 V, V_{GE} =±15 V, R_G =0 Ω , INDUCTIVE LOAD, PER PULSE



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

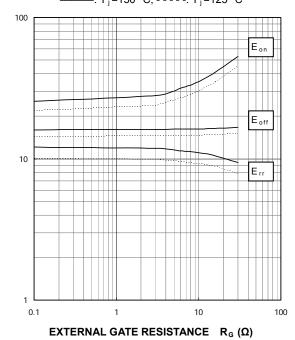
 V_{CC} =600 V, V_{GE} =±15 V, I_{C} =150 A, INDUCTIVE LOAD -: T_j=150 °C, - - - - : T_j=125 °C



EXTERNAL GATE RESISTANCE $R_{G}(\Omega)$

HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

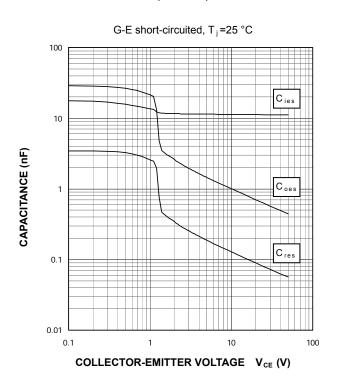
 V_{CC} =600 V, \dot{V}_{GE} =±15 V, I_C/I_F =150 A, INDUCTIVE LOAD, PER PULSE -: T_i=150 °C, - - - - : T_i=125 °C



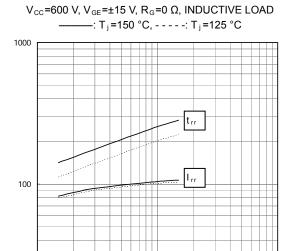
SWITCHING ENERGY (mJ)
REVERSE RECOVERY ENERGY (mJ)

PERFORMANCE CURVES IGBT/DIODE PART

CAPACITANCE CHARACTERISTICS (TYPICAL)



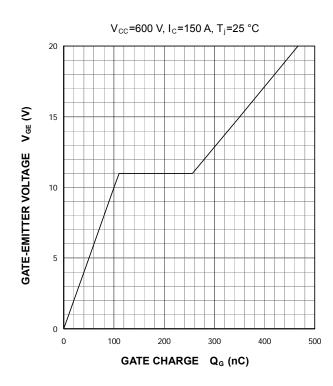
CLAMP DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



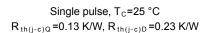
FORWARD CURRENT I_F (A)

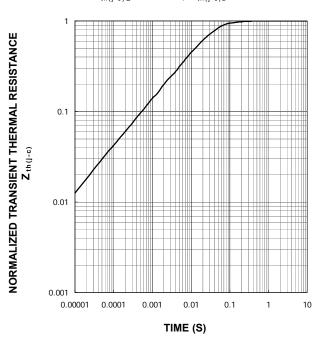
1000

GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)





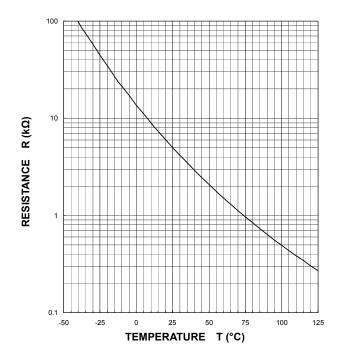
t rr (ns), I rr (A)

10

10

PERFORMANCE CURVES NTC THERMISTOR PART

TEMPERATURE CHARACTERISTICS (TYPICAL)



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