

isc Thyristors

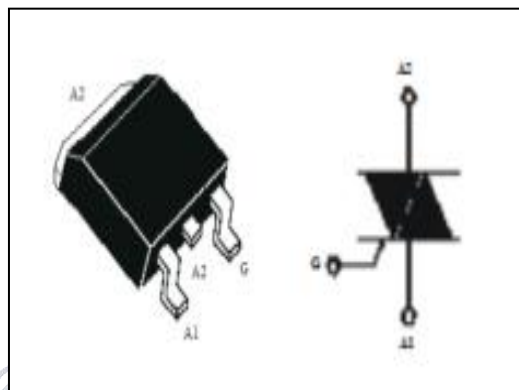
CLA30MT1200NPZ

DESCRIPTION

- With TO-263(D2PAK) packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Switching applications
- Phase control
- Static switching on inductive or resistive load



ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER		MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage		1200	V
V_{RRM}	Repetitive peak reverse voltage		1200	V
$I_{\text{T(RSM)}}$	Average on-state current		15	A
I_{TSM}	Surge non-repetitive on-state current	50HZ 60HZ	170 185	A
$P_{\text{G(AV)}}$	Average gate power dissipation (over any 20 ms period)		0.2	W
T_j	Operating junction temperature		-40~125	$^{\circ}\text{C}$
T_{stg}	Storage temperature		-40~125	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_c=25^{\circ}\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} Rated; V _D =V _{DRM} Rated;	T _j =25℃ T _j =125℃		0.01 1.5	mA
I _{DRM}	Repetitive peak off-state current					
V _{TM}	On-state voltage	I _T =15A			1.38	V
I _{GT}	Gate-trigger current	V _D =6V	I		40	mA
			II		40	
			III		40	
V _{GT}	Gate-trigger voltage	V _D =6V			1.3	V
R _{th} (j-mb)	Junction to mounting base	Half cycle			0.95	℃/W

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