

# RF SWITCH CG2409M2

### **High Power SPDT RF Switch**

#### **DESCRIPTION**

 The CG2409M2 is a GaAs MMIC high power SPDT (Single Pole Double Throw) switch which was designed for WiMAX and Wireless LAN applications

#### **FEATURES**

- Control voltage:
   VC(H) = 1.8 to 5.0 V (3.0V TYP.)
   VC(L) = -0.2 to 0.2 V (0V TYP.)
- Low insersion loss:

 $L_{ins}1 = 0.35 \text{ dB TYP.} @ f = 1.0 \text{ GHz}$   $L_{ins}2 = 0.45 \text{ dB TYP.} @ f = 2.5 \text{ GHz}$  $L_{ins}3 = 0.50 \text{ dB TYP.} @ f = 3.0 \text{ GHz}$ 

High isolation:

ISL1 = 30 dB TYP. @ f = 1.0 GHz ISL2 = 27 dB TYP. @ f = 2.5 GHz ISL3 = 23 dB TYP. @ f = 3.0 GHz

Power Handling

 $P_{in(0.1dB)} = +37.5 \text{ dBm TYP.} @ f = 2.5 \text{ GHz}, VC(H) = 3.0 \text{ V, VC(L)} = 0 \text{ V}$ 

#### **PACKAGE**

 6-pin mini mold Package (2.0mm x 1.25mm x 0.9mm)



#### **APPLICATIONS**

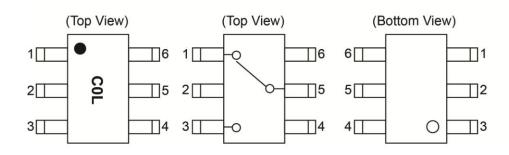
 WiMAX and wireless LAN (IEEE802.11a/b/g/n/ac)

#### ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Description
CG2409M2	CG2409M2-C4	6-pin mini mold (Pb-Free)	COL	<ul> <li>Embossed Tape 8 mm wide</li> <li>Pin 4, 5, 6 face the perforation side of the tape</li> <li>MOQ 10 kpcs/reel</li> </ul>
CG2409M2-EVAL	CG2409M2-EVAL			Evaluation Board with DC block capacitors, power supply bypass capacitors, and RF and DC connectors     MOQ 1



## PIN CONFIGURATION AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name		
1	RF1		
2	GND		
3	RF2		
4	VC2		
5	RFC		
6	VC1		

#### **TRUTH TABLE**

VC1	VC2	RFC-RF1	RFC-RF2
High	Low	ON	OFF
Low	High	OFF	ON

#### **ABSOLUTE MAXIMUM RATINGS**

(TA = +25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 <sup>Note 1</sup>	V
Input Power	Pin	+38.0 <sup>Note 2</sup>	dBm
Operating Ambient Temperature	T <sub>A</sub>	-45~+85	°C
Storage Temperature	$T_{stg}$	-55~+150	°C

Note

1. |VC1 - VC2|≦6.0V

2. 3.0V≦|VC1 - VC2|≦5.0V

#### RECOMMENDED OPERATING RANGE

(TA = +25 °C, unless otherwise specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.05	-	3.8	GHz
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.0	V
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V



#### **ELECTRICAL CHARACTERISTICS**

(TA=+25 °C, VC(H)=3.0V, VC(L)=0V, Zo=50Ω, DC Block Capacitance=8pF, unless otherwise specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	Lins1	f = 0.05 to 0.5 GHz Note 1	-	0.30	0.50	dB
	Lins2	f = 0.5 to 1.0 GHz Note 2	-	0.35	0.55	dB
	Lins3	f = 1.0 to 2.0 GHz Note 2	-	0.40	0.60	dB
	Lins4	f = 2.0 to 2.5 GHz	-	0.45	0.65	dB
	Lins5	f = 2.5 to 3.0 GHz	-	0.50	0.70	dB
	Lins6	f = 3.0 to 3.8 GHz	-	0.60	0.85	dB
Isolation	ISL1	f = 0.05 to 0.5 GHz Note 1	27	30	-	dB
	ISL2	f = 0.5 to 1.0 GHz Note 2	27	30	-	dB
	ISL3	f = 1.0 to 2.0 GHz Note 2	24	27	-	dB
	ISL4	f = 2.0 to 2.5 GHz	24	27	-	dB
	ISL5	f = 2.5 to 3.0 GHz	20	23	-	dB
	ISL6	f = 3.0 to 3.8 GHz	16	19	-	dB
Return Loss	RL1	f = 0.05 to 0.5 GHz Note 1	15	20	-	dB
	RL2	f = 0.5 to 2.0 GHz Note 2	15	20	-	dB
	RL3	f = 2.0 to 3.8 GHz	15	20	-	dB
0.1 dB Loss Compression Input Power Note 3	P <sub>in(0.1dB)</sub>	f = 2.5 GHz	-	+37.5	-	dBm
2nd Harmonics	2f0	f = 2.5 GHz, P <sub>in</sub> =+30dBm	-	75	-	dBc
3rd Harmonics	3f0	f = 2.5 GHz, P <sub>in</sub> =+30dBm	-	75	-	dBc
Input 3rd Order Intercept Point	IIP3	f = 2.5GHz 2-tone 1MHz Spacing		+60		dBm
Switch Control Speed	tsw	50% CTL to 90/10% RF	-	100	-	ns
Switch Control Current	Icont	Non RF	_	5	_	μA

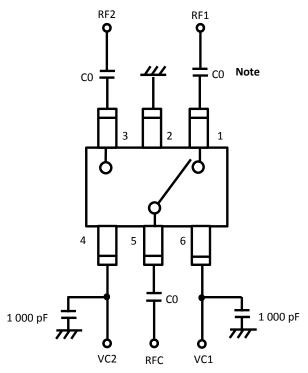
Note 1 DC block capacitance = 1,000pF at f=0.05 to 0.5 GHz

Note 2 DC block capacitance = 56pF at f=0.5 to 2.0 GHz

**Note 3**  $P_{in}(0.1dB)$  is the measured input power level when the insertion loss increases 0.1dB more than that of the linear range.



#### **EVALUATION CIRCUIT**



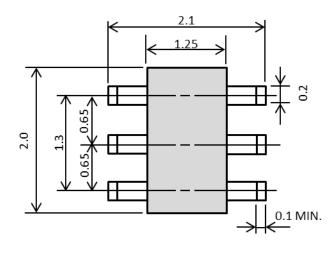
**Note** C0: 0.05 to 0.5 GHz 1,000pF

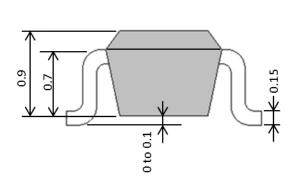
: 0.5 to 2.0 GHz 56pF : 2.0 to 3.8 GHz 8pF

The application circuits and their parameters are for reference only and are not intended for use in actual designs. DC Block Capacitors are required at all RF ports.

#### **PACKAGE DIMENSIONS**

6-pin mini mold package (Unit: mm)







#### **REVISION HISTORY**

Version	Change to current version	Page(s)
CDS-0032-01 (Issue A)	Preliminary Datasheet	N/A
September 14, 2016		



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