

## OVERVIEW

The CF5035 series are 1.8V operation, high-frequency crystal oscillator module ICs. They support 70MHz to 165MHz (1.8V) and 70MHz to 220MHz (2.5 to 3.3V) 3rd overtone oscillation and fundamental oscillation modes. The crystal oscillator circuit is a Colpitts oscillator with the necessary oscillator capacitors and feedback resistor built-in. The oscillation characteristics feature little variation with supply voltage, providing stable oscillation over a wide supply voltage range. The output circuit comprises a CMOS buffer that can operate at high frequencies and drive a 15pF capacitance load. The devices are fabricated using a proprietary BiCMOS process with the oscillator circuit and output buffer on a single chip, making them ideal as SMD-type high-frequency crystal oscillators.

## FEATURES

- 1.6 to 3.6V operating supply voltage range
- Oscillation frequency range (varies with version)
  - 1.6 to 3.6V: 70MHz to 165MHz
  - 2.25 to 3.6V: 70MHz to 220MHz
- –40 to 85°C operating temperature range
- Oscillation capacitors built-in
- Inverter amplifier feedback resistor built-in
- Oscillation detector function built-in
- Standby function
  - High impedance in standby mode, oscillator stops
- Low standby current
  - Power-saving pull-up resistor built-in
- CMOS output duty level
- 15pF output load
- 8mA output drive capability ( $V_{DD} = 1.6V$ )
- BiCMOS process
- Chip form (CF5035AL×)

## APPLICATIONS

- 1.8V, high-frequency crystal oscillator modules

## SERIES CONFIGURATION

Version	Operating supply voltage range [V]	Recommended operating frequency range <sup>1</sup> [MHz]	Built-in capacitance [pF]	
			C <sub>IN</sub>	C <sub>OUT</sub>
CF5035ALA	1.6 to 3.6	70 to 95	4	4
CF5035ALB		95 to 125	4	4
CF5035ALC		125 to 165	2	2
CF5035ALD	2.25 to 3.6	165 to 220	1	1

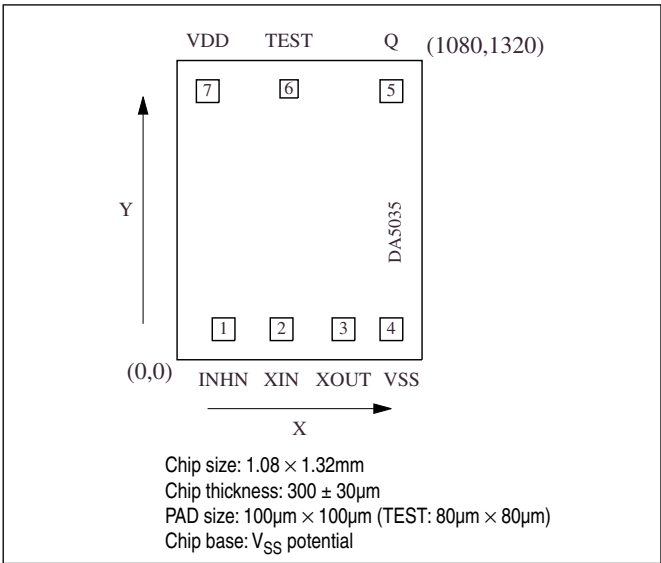
1. The recommended operating frequency is a yardstick value derived from the crystal used for NPC characteristics authentication. However, the oscillator frequency band is not guaranteed. Specifically, the characteristics can vary greatly due to crystal characteristics and mounting conditions, so the oscillation characteristics of components must be carefully evaluated.

## ORDERING INFORMATION

Device	Package
CF5035AL×–1	Chip form

PAD LAYOUT

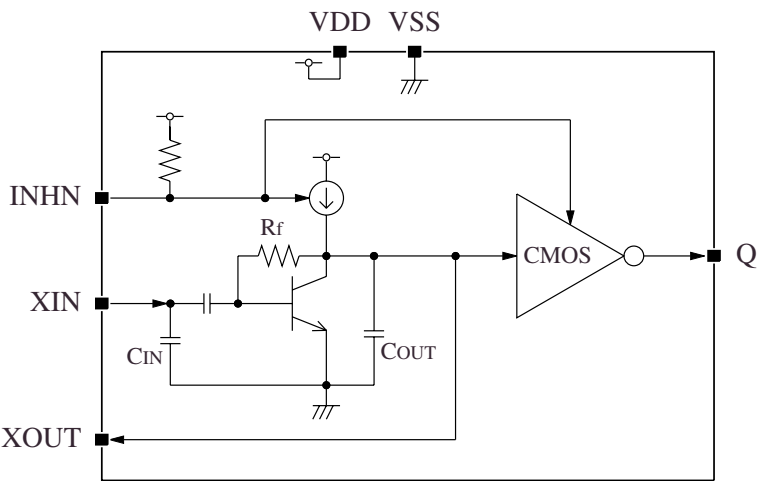
(Unit:  $\mu\text{m}$ )



PIN DESCRIPTION and PAD DIMENSIONS

No.	Name	I/O	Description	Pad dimensions [ $\mu\text{m}$ ]	
				X	Y
1	INH N	I	Output state control input. Oscillator stops when LOW. Power-saving pull-up resistor built-in.	205	135
2	XIN	I	Oscillator input	461	135
3	XOUT	O	Oscillator output	734	135
4	VSS	–	(–) ground	945	135
5	Q	O	Output. Output frequency. High impedance in standby mode	945	1185
6	TEST	I	Test (leave open)	494	1195
7	VDD	–	(+) supply voltage	135	1185

BLOCK DIAGRAM



**ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Condition	Rating	Unit
Supply voltage range	$V_{DD}$	Between VDD and VSS	-0.5 to +5.0	V
Input voltage range	$V_{IN}$	Input pins	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output voltage range	$V_{OUT}$	Output pins	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output current	$I_{OUT}$	Q pin	25	mA
Storage temperature range	$T_{STG}$	Chip form	-65 to +150	°C

**RECOMMENDED OPERATING CONDITIONS** $C_L \leq 15\text{pF}$ 

Parameter	Symbol	Condition		Rating			Unit
				min	typ	max	
Supply voltage	$V_{DD}$	$70\text{MHz} \leq f \leq 95\text{MHz}$	5035ALA	1.6	—	3.6	V
		$95\text{MHz} \leq f \leq 125\text{MHz}$	5035ALB	1.6	—	3.6	V
		$125\text{MHz} \leq f \leq 165\text{MHz}$	5035ALC	1.6	—	3.6	V
		$165\text{MHz} \leq f \leq 220\text{MHz}$	5035ALD	2.25	—	3.6	V
Input voltage	$V_{IN}$	Input pins		$V_{SS}$	—	$V_{DD}$	V
Operating temperature	$T_{OPR}$	Chip temperature		-40	+25	+85	°C

## ELECTRICAL CHARACTERISTICS

## DC Characteristics

Recommended operating conditions unless otherwise noted.

Parameter	Symbol	Condition			Rating			Unit
					min	typ	max	
Operating current consumption <sup>1</sup>	I <sub>DD</sub>	Measurement cct 1, INHN = open or HIGH, C <sub>L</sub> = 15pF	5035ALA f = 95MHz	V <sub>DD</sub> = 1.6 to 2.0V	–	11	19	mA
				V <sub>DD</sub> = 2.25 to 2.75V	–	16	26	mA
				V <sub>DD</sub> = 2.75 to 3.6V	–	20	33	mA
			5035ALB f = 125MHz	V <sub>DD</sub> = 1.6 to 2.0V	–	17	27	mA
				V <sub>DD</sub> = 2.25 to 2.75V	–	23	36	mA
				V <sub>DD</sub> = 2.75 to 3.6V	–	29	46	mA
			5035ALC f = 165MHz	V <sub>DD</sub> = 1.6 to 2.0V	–	23	37	mA
				V <sub>DD</sub> = 2.25 to 2.75V	–	31	51	mA
				V <sub>DD</sub> = 2.75 to 3.6V	–	39	65	mA
			5035ALD f = 220MHz	V <sub>DD</sub> = 2.25 to 2.75V	–	38	58	mA
V <sub>DD</sub> = 2.75 to 3.6V	–	48		73	mA			
Standby current	I <sub>ST</sub>	Measurement cct 1, INHN = LOW			–	–	10	μA
HIGH-level output voltage	V <sub>OH</sub>	Q: Measurement cct 3, V <sub>DD</sub> = 1.6 to 3.6V, I <sub>OH</sub> = 8mA			V <sub>DD</sub> – 0.4	V <sub>DD</sub> – 0.3	–	V
LOW-level output voltage	V <sub>OL</sub>	Q: Measurement cct 3, V <sub>DD</sub> = 1.6 to 3.6V, I <sub>OL</sub> = 8mA			–	0.3	0.4	V
Output leakage current	I <sub>Z</sub>	Q: Measurement cct 5, INHN = LOW, V <sub>DD</sub> = 3.6V		V <sub>OH</sub> = V <sub>DD</sub>	–	–	10	μA
				V <sub>OL</sub> = V <sub>SS</sub>	–	–	10	μA
HIGH-level input voltage	V <sub>IH</sub>	INHN, Measurement cct 4			0.7V <sub>DD</sub>	–	–	V
LOW-level input voltage	V <sub>IL</sub>	INHN, Measurement cct 4			–	–	0.3V <sub>DD</sub>	V
INHN pull-up resistance	R <sub>PU1</sub>	Measurement cct 6		INHN = V <sub>SS</sub>	0.4	–	4	MΩ
	R <sub>PU2</sub>			INHN = 0.7V <sub>DD</sub>	30	–	150	kΩ

1. The operating current consumption includes the  $C_L = 15\text{pF}$  capacitance load charging current.

## AC Characteristics

Recommended operating conditions unless otherwise noted.

Parameter	Symbol	Condition	Rating			Unit
			min	typ	max	
Output duty cycle	Duty	Measurement cct 1, $T_a = 25^\circ\text{C}$ , $C_L = 15\text{pF}$				
		$V_{DD} = 2.25 \text{ to } 3.6\text{V}$	45	50	55	%
		$V_{DD} = 1.6 \text{ to } 2.25\text{V}$	40	50	60	%
Rise time	$t_{r1}$	Measurement cct 1, $C_L = 15\text{pF}$ , $V_{DD} = 2.25 \text{ to } 3.6\text{V}$ , $0.1V_{DD}$ to $0.9V_{DD}$	–	1	2	ns
	$t_{r2}$	Measurement cct 1, $C_L = 15\text{pF}$ , $V_{DD} = 1.6 \text{ to } 2.25\text{V}$ , $0.2V_{DD}$ to $0.8V_{DD}$	–	1.5	2.5	ns
Fall time	$t_{f1}$	Measurement cct 1, $C_L = 15\text{pF}$ , $V_{DD} = 2.25 \text{ to } 3.6\text{V}$ , $0.9V_{DD}$ to $0.1V_{DD}$	–	1	2	ns
	$t_{f2}$	Measurement cct 1, $C_L = 15\text{pF}$ , $V_{DD} = 1.6 \text{ to } 2.25\text{V}$ , $0.8V_{DD}$ to $0.2V_{DD}$	–	1.5	2.5	ns
Output enable delay time <sup>1</sup>	$t_{OE}$	Measurement cct 2, $T_a = 25^\circ\text{C}$ , $C_L \leq 15\text{pF}$ , $\text{INH} = \text{LOW} \rightarrow \text{HIGH}$	–	–	2	ms
Output disable delay time	$t_{OD}$	Measurement cct 2, $T_a = 25^\circ\text{C}$ , $C_L \leq 15\text{pF}$ , $\text{INH} = \text{HIGH} \rightarrow \text{LOW}$	–	–	200	ns

1. Oscillator stop function is built-in. When INHN goes LOW, normal output stops. When INHN goes HIGH, normal output is not resumed until after the oscillator start-up time has elapsed.

## Timing chart

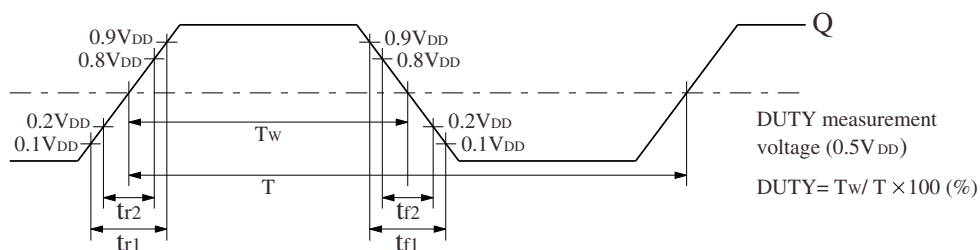


Figure 1. Output switching waveform

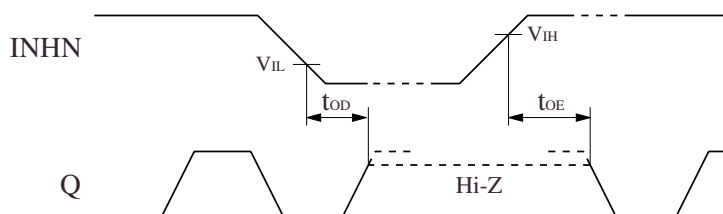


Figure 2. Output disable/enable timing chart

## FUNCTIONAL DESCRIPTION

### Standby Function

When INHN goes LOW, the device is in standby mode. The Q output becomes high impedance and the oscillator circuit stops.

INHN	Q	Oscillator
HIGH (or open)	$f_O$ output frequency	Normal operation
LOW	High impedance	Stopped

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### Power-saving Pull-up Resistor

The INHN pin pull-up resistance changes in response to the input level (HIGH or LOW). When INHN is tied LOW, the pull-up resistance becomes large, reducing the current consumed by the resistance. When INHN is left open, the pull-up resistance becomes small, such that even if the input is affected by external noise the outputs are stable due to INHN being tied HIGH by the pull-up resistor.

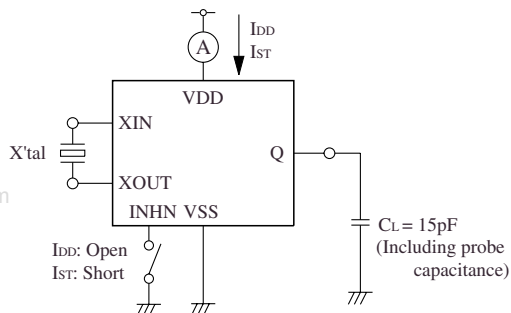
### Oscillation Detector Function

The devices also feature an oscillation detector circuit. This circuit functions to disable the outputs until the oscillator circuit starts. This prevents abnormal oscillator output at oscillator start-up when power is applied or when INHN is switched.

## MEASUREMENT CIRCUITS

### Measurement cct 1

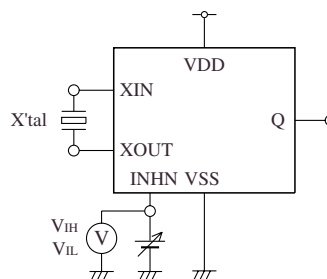
Measurement parameter:  $I_{DD}$ ,  $I_{ST}$ , Duty,  $t_r$ ,  $t_f$



Note: The AC characteristics are observed using an oscilloscope on pin Q.

### Measurement cct 4

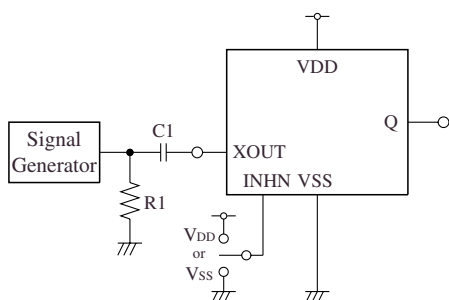
Measurement parameter:  $V_{IH}$ ,  $V_{IL}$



$V_{IH}$ : Voltage in  $V_{SS}$  to  $V_{DD}$  transition that changes the output state.  
 $V_{IL}$ : Voltage in  $V_{DD}$  to  $V_{SS}$  transition that changes the output state.  
 INHN has an oscillation stop function.

### Measurement cct 2

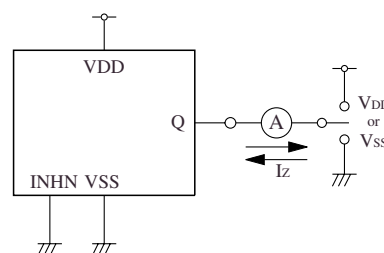
Measurement parameter:  $t_{OE}$ ,  $t_{OD}$



XIN input signal: 1Vp-p, sine wave  
 C1: 0.001μF  
 R1: 50Ω

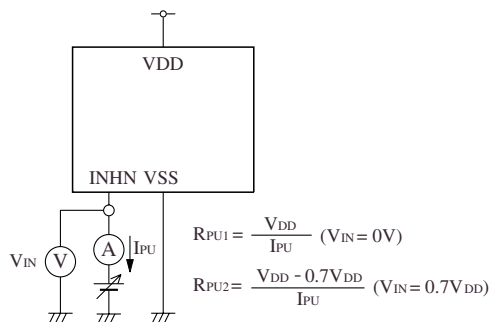
### Measurement cct 5

Measurement parameter:  $I_Z$



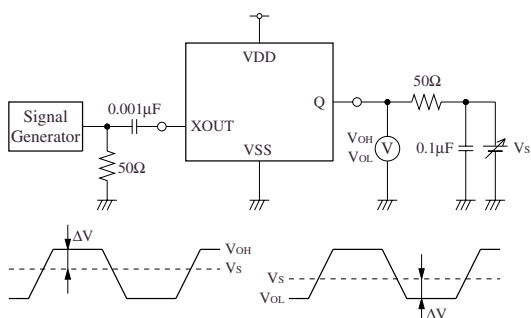
### Measurement cct 6

Measurement parameter:  $R_{PU1}$ ,  $R_{PU2}$



### Measurement cct 3

Measurement parameter:  $V_{OH}$ ,  $V_{OL}$



$V_S$  adjusted such that  $\Delta V = 50 \times I_{OH}$ .  
 $V_S$  adjusted such that  $\Delta V = 50 \times I_{OL}$ .

XOUT input signal: 1Vp-p, sine wave

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