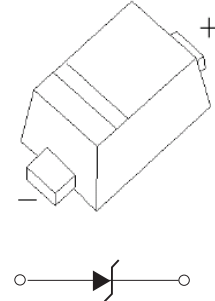




## SOD-723 Plastic-Encapsulate Diodes

### **CESD5V0D7** ESD Protection Diode

**SOD-723**



#### **DESCRIPTION**

The CESD5V0D7 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

#### **FEATURES**

- Stand-off Voltage: 3.3 V-12 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb-Free Devices

#### **Maximum Ratings @Ta=25°C**

Parameter	Symbol	Limit	Unit
IEC61000-4-2(ESD) Air Contact		$\pm 30$ $\pm 30$	KV
ESD Voltage per human body model		16	KV
per machine model		400	V
Total Power Dissipation on FR-5 Board (Note 1)	$P_D$	100	mW
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	1250	°C/W
Lead Solder Temperature – Maximum (10 Second Duration)	$T_L$	260	°C
Junction and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150	°C

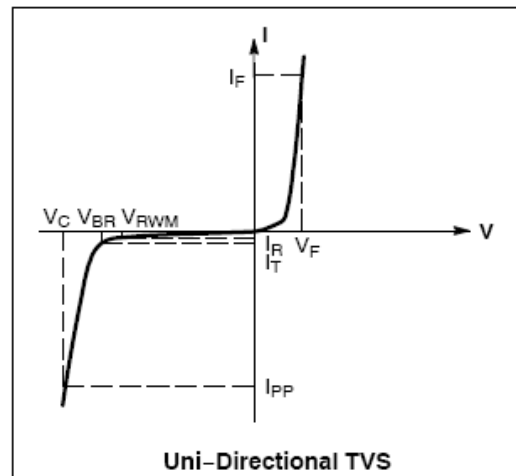
Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only.

Functional operation above the Recommended. Operating Conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

Note 1. FR-5 = 1.0 x 0.75 x 0.62 in.

# **ELECTRICAL CHARACTERISTICS** (Ta = 25°C unless otherwise noted)

Symbol	Parameter
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Working Peak Reverse Voltage
$I_R$	Maximum Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$
$P_{pk}$	Peak Power Dissipation
C	Max. Capacitance @ $V_R=0$ and $f=1\text{MHz}$



## **ELECTRICAL CHARACTERISTICS** (Ta = 25°C unless otherwise noted, $V_F = 0.9\text{ V Max. @ } I_F = 10\text{mA}$ for all types)

Device*	Device Marking	$V_{RWM}$ (V)	$I_R$ ( $\mu\text{A}$ ) @ $V_{RWM}$	$V_{BR}$ (V) @ $I_T$ (Note 2)		$I_T$	$I_{PP}$ (A) *	$V_C$ (V) @Max $I_{PP}$ *	$P_{pk}$ * (W)	C (pF)
		Max	Max	Min	Max	mA	Max	Max	Max	Typ
CESD3V3D7	E0	3.3	2.5	5.0	5.9	1.0	10.4	11.9	113	80
CESD5V0D7	E2	5.0	1.0	6.2	7.3	1.0	8.8	13.3	117	65
CESD12VD7	E3	12	1.0	13.5	15.6	1.0	5.4	23.7	128	30

\*Other voltages available upon request.

+Surge current waveform per Figure 1.

Note 2.  $V_{BR}$  is measured with a pulse test current  $I_T$  at an ambient temperature of 25°C.