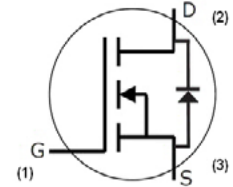


C2M0280120D

Silicon Carbide Power MOSFET C2M™ MOSFET Technology N-Channel Enhancement Mode

Features

- C2M™ Silicon Carbide (SiC) MOSFET technology
- High blocking voltage with low On-resistance
- High speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Halogen free, RoHS compliant



1 2 3

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Part Number	Package
C2M0280120D	TO 247-3

Typical Applications

- Renewable energy
- High voltage DC/DC converters
- Switch Mode Power Supplies
- UPS

Benefits

- Higher system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency

Key Parameters

Parameter	Symbol	Min.	Typ.	Max	Unit	Conditions	Note
Drain - Source Voltage	V_{DS}			1200	V	$T_c = 25^\circ\text{C}$	
Maximum Gate - Source Voltage	$V_{GS(max)}$	-10		+25		Transient	
Operational Gate-Source Voltage	$V_{GS op}$		-5/20			Static	Note 1
DC Continuous Drain Current	I_D			11	A	$V_{GS} = 20\text{ V}, T_c = 25^\circ\text{C}, T_J \leq 150^\circ\text{C}$	Fig. 19
				7.5		$V_{GS} = 20\text{ V}, T_c = 100^\circ\text{C}, T_J \leq 150^\circ\text{C}$	Note 2
Pulsed Drain Current	I_{DM}			20		t_{Pmax} limited by T_{Jmax} $V_{GS} = 20\text{ V}, T_c = 25^\circ\text{C}$	Fig. 22
Power Dissipation	P_D			69.4	W	$T_c = 25^\circ\text{C}, T_J = 150^\circ\text{C}$	Fig. 20
Operating Junction and Storage Temperature	T_J, T_{stg}			-55 to +150	$^\circ\text{C}$		
Solder Temperature	T_L			260		According to JEDEC J-STD-020	
Mounting Torque	M_D			1 8.8	Nm lbf-in	M3 or 6-32 screw	

Note (1): Recommended turn-on gate voltage is 20V with $\pm 5\%$ regulation tolerance, see Application Note PRD-04814 for additional details

Note (2): Verified by design


Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	Note
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	1200	—	—	V	$V_{GS} = 0\text{ V}, I_D = 100\text{ }\mu\text{A}$	Fig. 11
Gate Threshold Voltage	$V_{GS(th)}$	2.0	3.1	4		$V_{DS} = V_{GS}, I_D = 1.25\text{ mA}$	
		—	2.7	—		$V_{DS} = V_{GS}, I_D = 1.25\text{ mA}, T_J = 150^\circ\text{C}$	
Zero Gate Voltage Drain Current	I_{DSS}	—	1	100	μA	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$	
Gate-Source Leakage Current	I_{GSS}	—		250	nA	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	
Drain-Source On-State Resistance	$R_{DS(on)}$	—	320	370	m Ω	$V_{GS} = 20\text{ V}, I_D = 6\text{ A}$	Fig. 4, 5, 6
		—	540	—		$V_{GS} = 20\text{ V}, I_D = 6\text{ A}, T_J = 150^\circ\text{C}$	
Transconductance	g_{fs}	—	2.6	—	S	$V_{DS} = 20\text{ V}, I_{DS} = 6\text{ A}$	Fig. 7
			2.5			$V_{DS} = 20\text{ V}, I_{DS} = 6\text{ A}, T_J = 150^\circ\text{C}$	
Input Capacitance	C_{iss}	—	267	—	pF	$V_{GS} = 0\text{ V},$ $V_{DS} = 1000\text{ V}$ $f = 1\text{ MHz}$ $V_{AC} = 25\text{ mV}$	Fig. 17, 18
Output Capacitance	C_{oss}	—	31	—			
Reverse Transfer Capacitance	C_{rss}	—	4	—			
Output Capacitance Stored Energy	E_{oss}	—	17	—	μJ	$V_{DS} = 800\text{ V}, V_{GS} = -5/20\text{ V}, I_D = 6\text{ A},$ $R_{G(ext)} = 2.5\text{ }\Omega, L = 404\text{ }\mu\text{H}$ FWD = Internal Body Diode of MOSFET	Fig. 25
Turn-On Switching Energy (Body Diode)	E_{on}	—	111	—			
Turn Off Switching Energy (Body Diode)	E_{off}	—	10	—			
Turn-On Switching Energy (External Diode)	E_{on}	—	95	—			
Turn Off Switching Energy (External Diode)	E_{off}	—	9.8	—	ns	$V_{DS} = 800\text{ V}, V_{GS} = -5/20\text{ V}, I_D = 6\text{ A},$ $R_{G(ext)} = 2.5\text{ }\Omega, L = 404\text{ }\mu\text{H}$ FWD = External SiC Diode	Fig. 27
Turn-On Delay Time	$t_{d(on)}$	—	6	—			
Rise Time	t_r	—	19	—			
Turn-Off Delay Time	$t_{d(off)}$	—	10	—			
Fall Time	t_f	—	16	—	Ω	$f = 1\text{ MHz}, V_{AC} = 25\text{ mV}, \text{ESR of } C_{ISS}$	Fig. 12
Internal Gate Resistance	$R_{G(int)}$	—	10	—			
Gate to Source Charge	Q_{gs}	—	6	—			
Gate to Drain Charge	Q_{gd}	—	7	—			
Total Gate Charge	Q_g	—	19	—	nC	$V_{DS} = 800\text{ V}, V_{GS} = -5/20\text{ V}$ $I_D = 6\text{ A}$ Per IEC60747-8-4 pg 21	

Reverse Diode Characteristics (T_c = 25°C unless otherwise specified)

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Note
Diode Forward Voltage	V _{SD}	4.3	—	V	V _{GS} = -5 V, I _{SD} = 3 A	Fig. 8, 9, 10
		3.8	—		V _{GS} = -5 V, I _{SD} = 3 A, T _J = 150°C	
Continuous Diode Forward Current	I _S	—	12	A	V _{GS} = -5 V, T _C = 25°C	Note 1
Diode Pulse Current	I _{SM}	—	20	—	V _{GS} = -5 V, pulse width t _p limited by T _{jmax}	
Reverse Recover Time	t _{rr}	17	—	nS	V _{GS} = -5 V, I _{SD} = 6 A, V _R = 800 V dif/dt = 2985 A/μs	Note 1
Reverse Recovery Charge	Q _{rr}	48	—	nC		
Peak Reverse Recovery Current	I _{rrm}	5	—	A		
Reverse Recovery time	t _{rr}	25	—	nS	V _{GS} = -5 V, I _{SD} = 6 A, V _R = 800 V dif/dt = 1000 A/μs	Note 1
Reverse Recovery Charge	Q _{rr}	45	—	nC		
Peak Reverse Recovery Current	I _{rrm}	4	—	A		

Note:
¹ When using SiC Body Diode the maximum recommended V_{GS} = -5V

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Thermal Resistance from Junction to Case	R _{θJC}	1.53	1.8	°C/W	Fig. 21
Thermal Resistance from Junction to Ambient			40		

Typical Performance

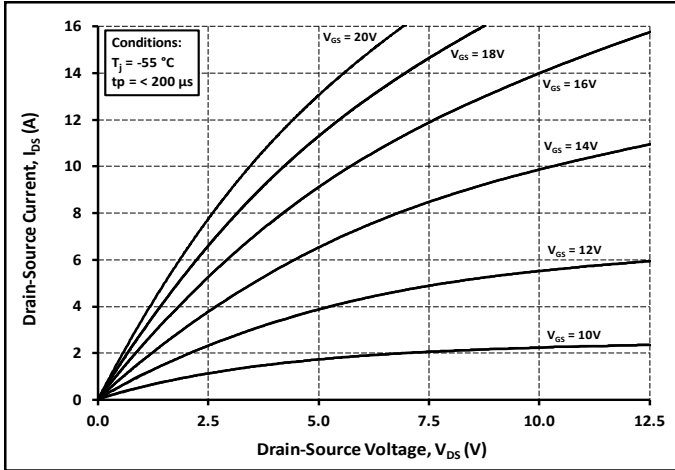


Figure 1. Output Characteristics $T_j = -55^\circ\text{C}$

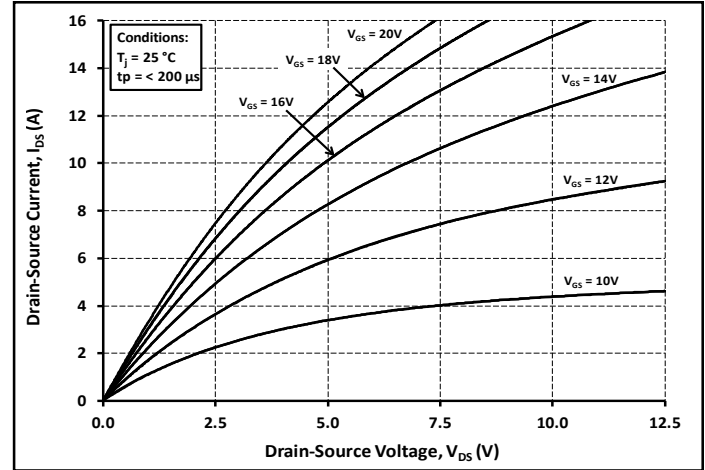


Figure 2. Output Characteristics $T_j = 25^\circ\text{C}$

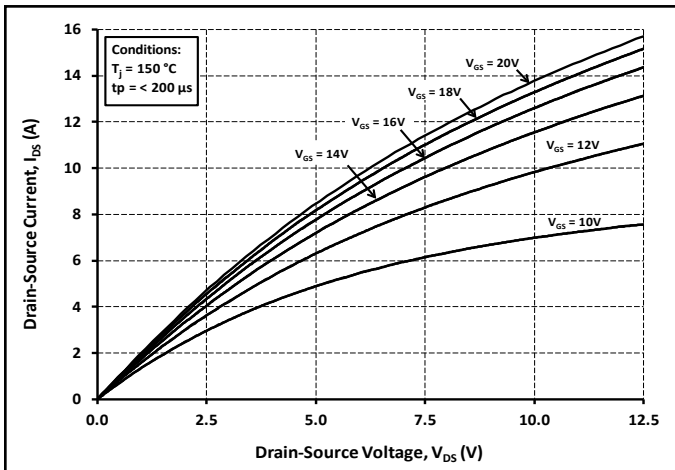


Figure 3. Output Characteristics $T_j = 150^\circ\text{C}$

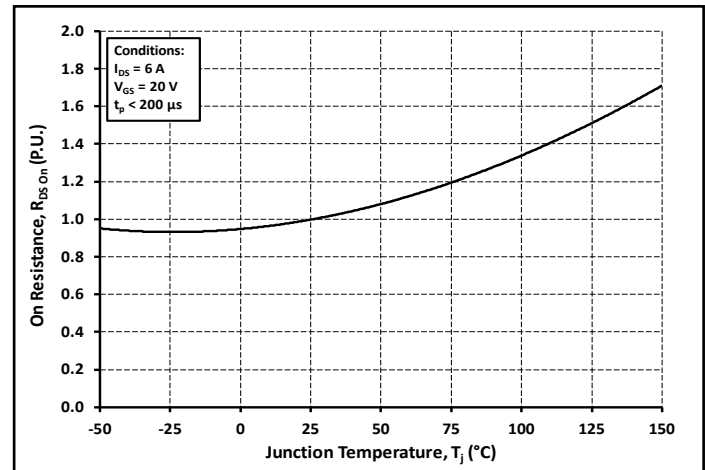


Figure 4. Normalized On-Resistance vs. Temperature

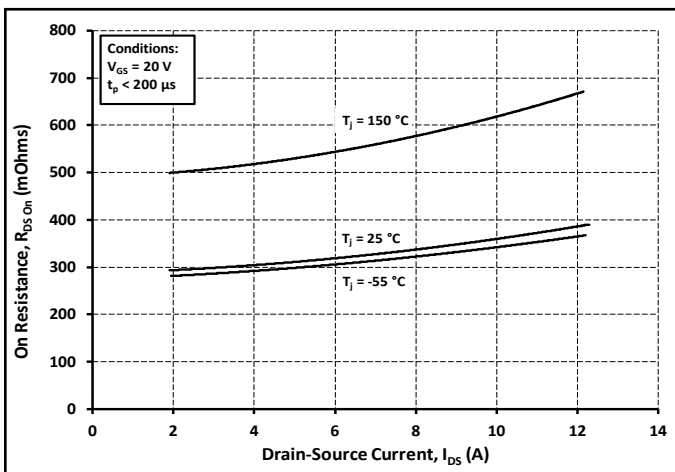


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

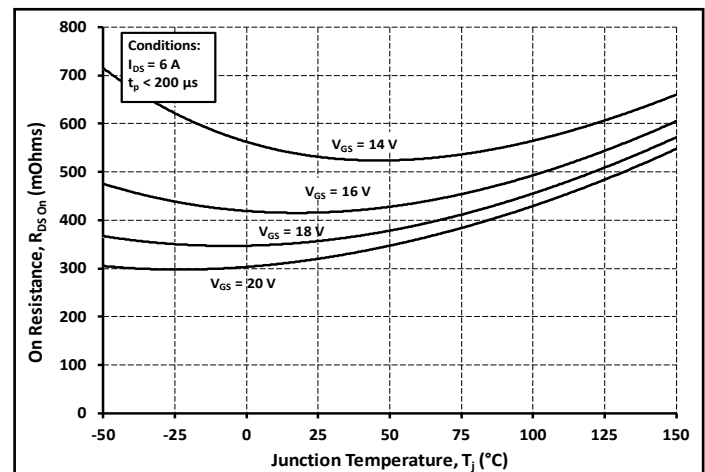


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

Typical Performance

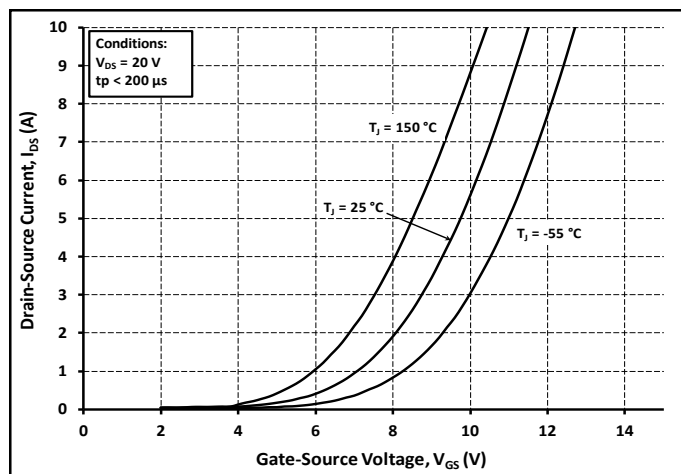


Figure 7. Transfer Characteristic for Various Junction Temperatures

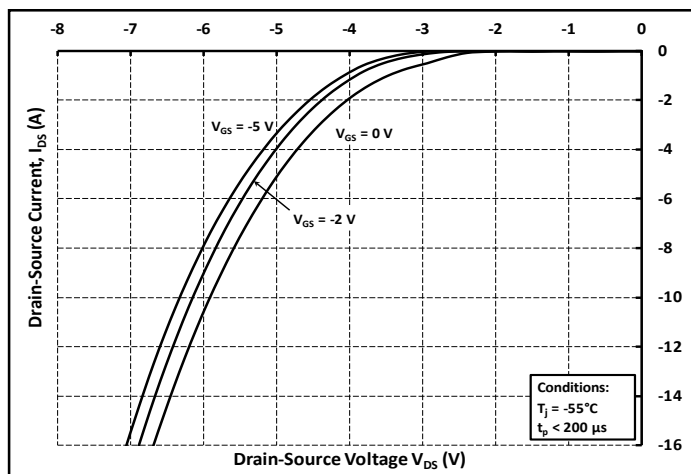


Figure 8. Body Diode Characteristic at -40°C

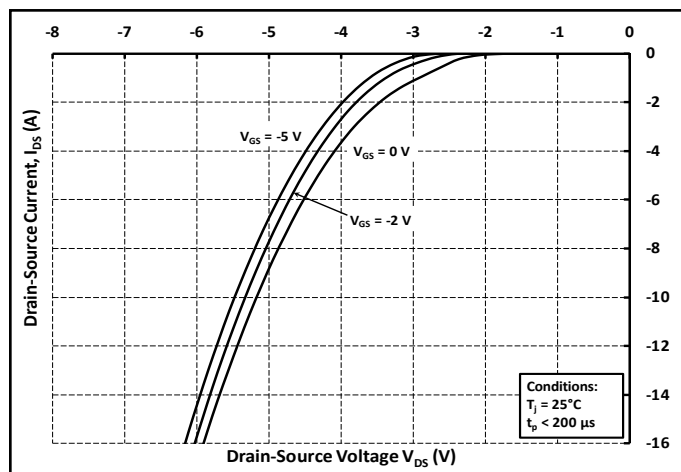


Figure 9. Body Diode Characteristic at 25°C

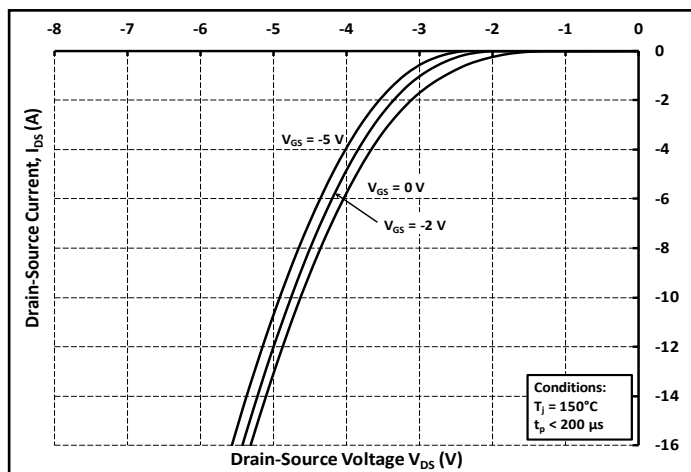


Figure 10. Body Diode Characteristic at 150°C

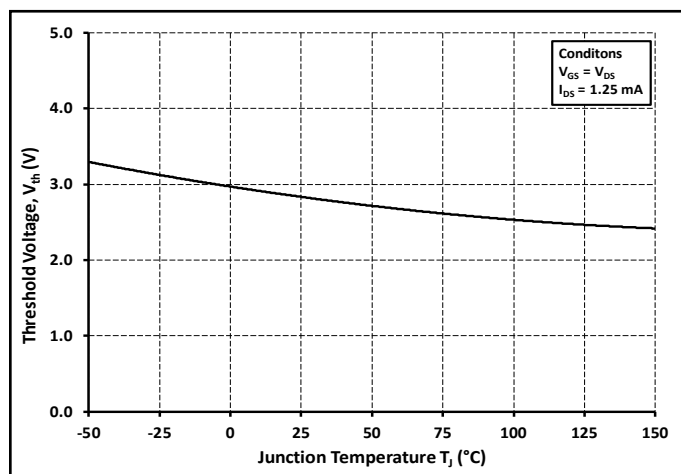


Figure 11. Threshold Voltage vs. Temperature

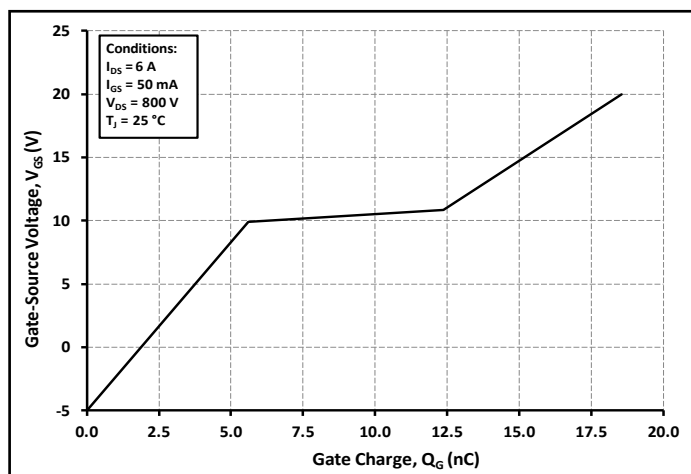


Figure 12. Gate Charge Characteristics

Typical Performance

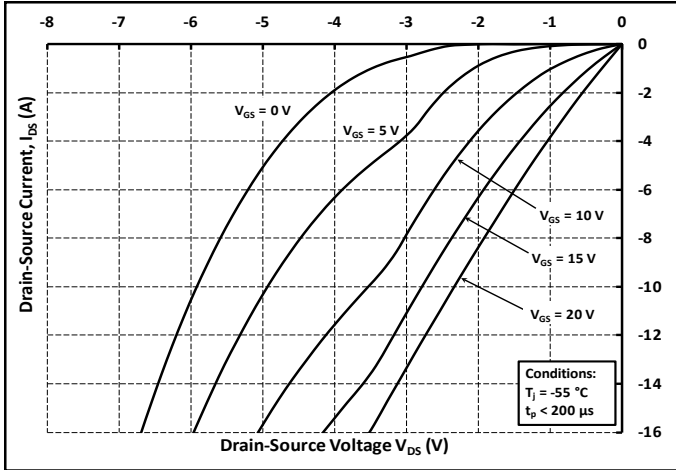


Figure 13. 3rd Quadrant Characteristic at -55°C

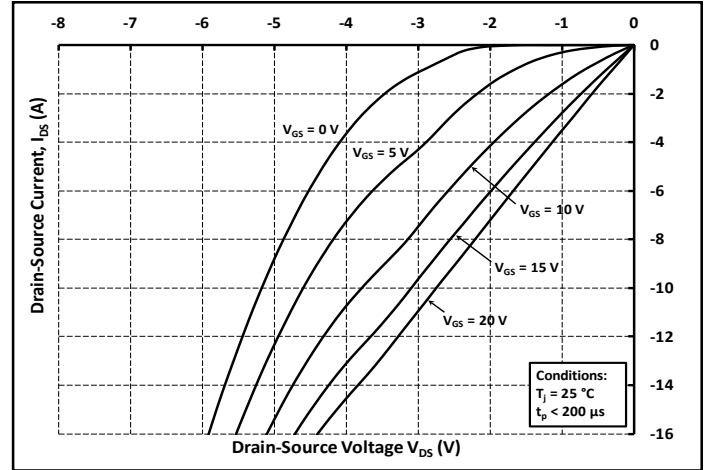


Figure 14. 3rd Quadrant Characteristic at 25°C

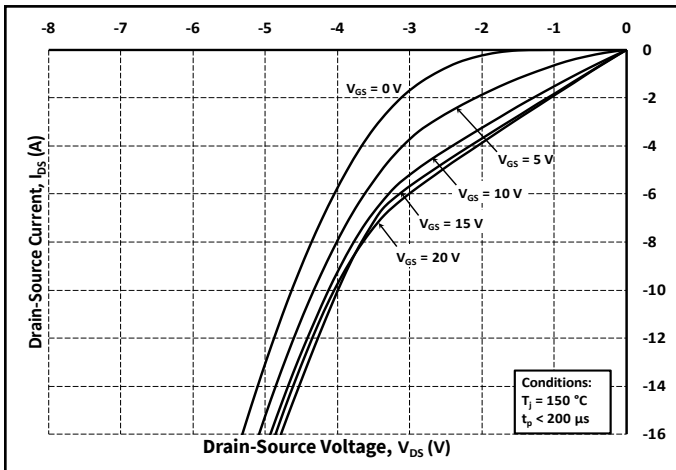


Figure 15. 3rd Quadrant Characteristic at 150°C

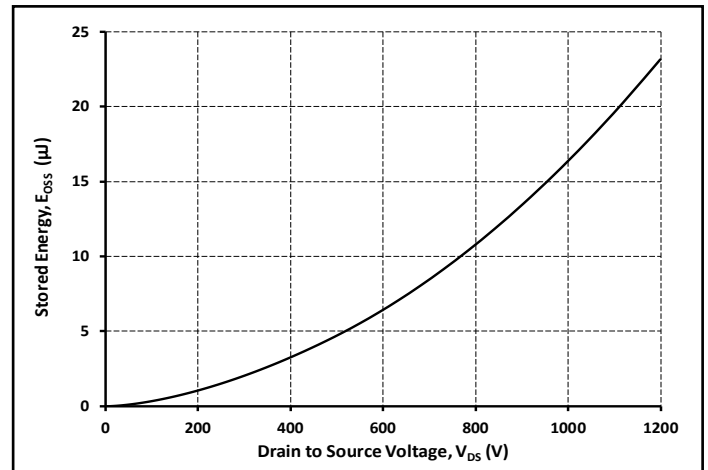


Figure 16. Output Capacitor Stored Energy

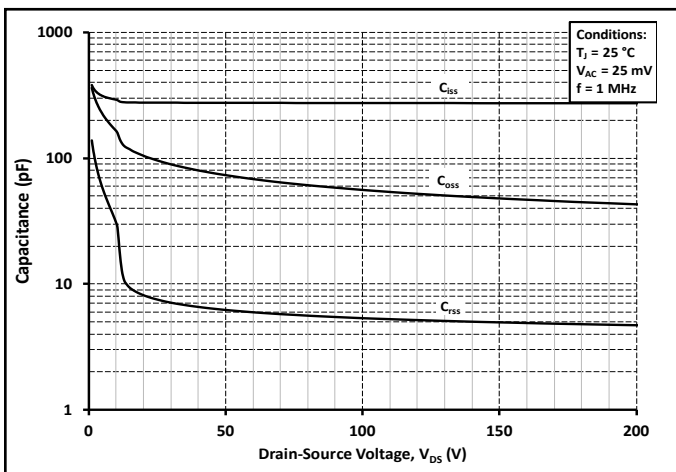


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200 V)

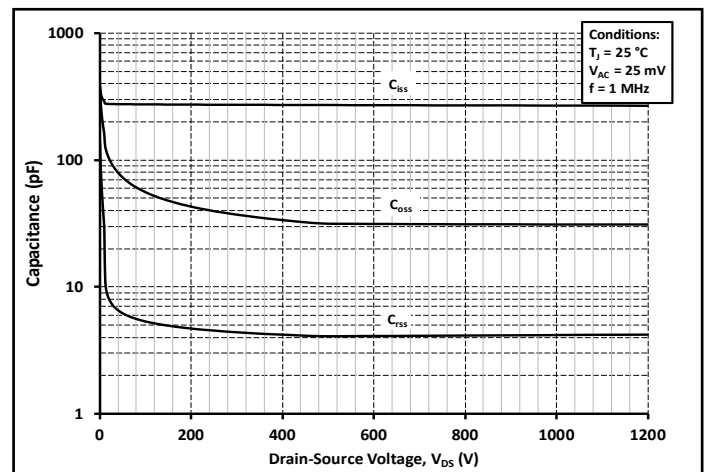


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1200 V)

Typical Performance

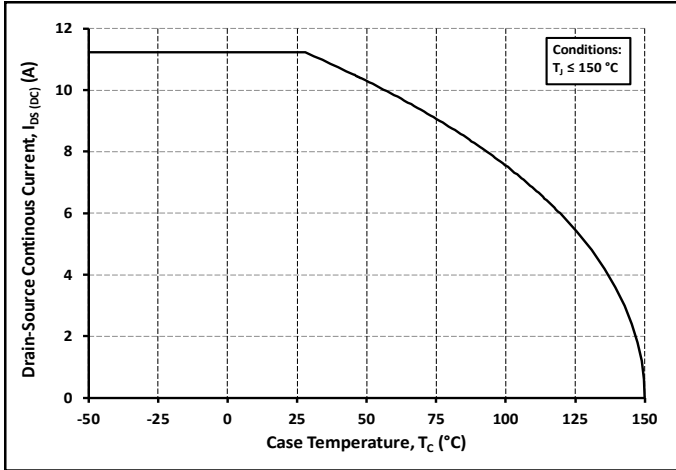


Figure 19. Continuous Drain Current Derating vs. Case Temperature

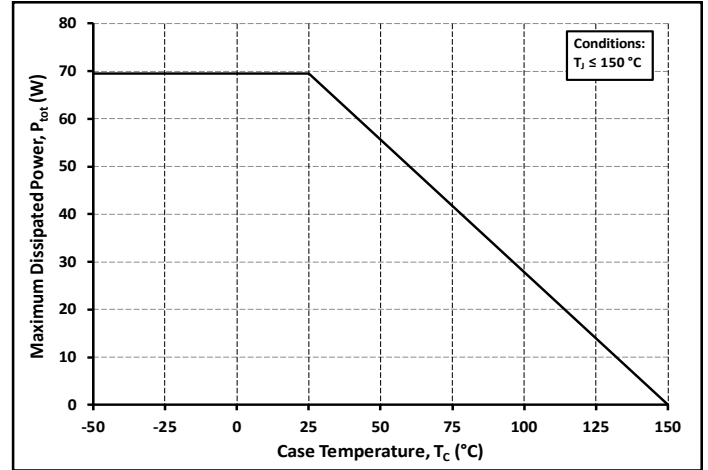


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

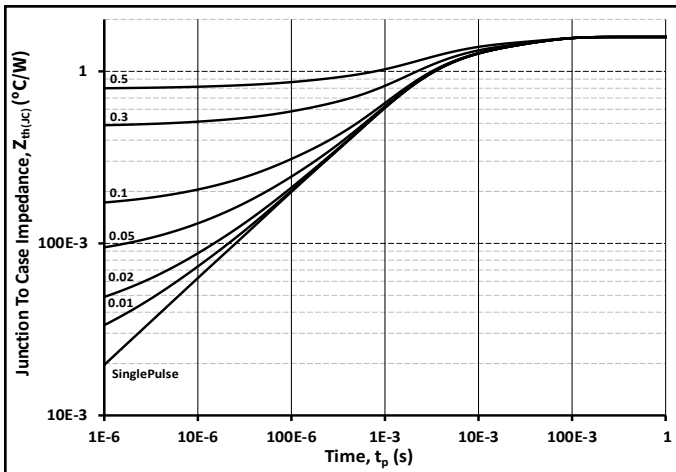


Figure 21. Transient Thermal Impedance (Junction - Case)

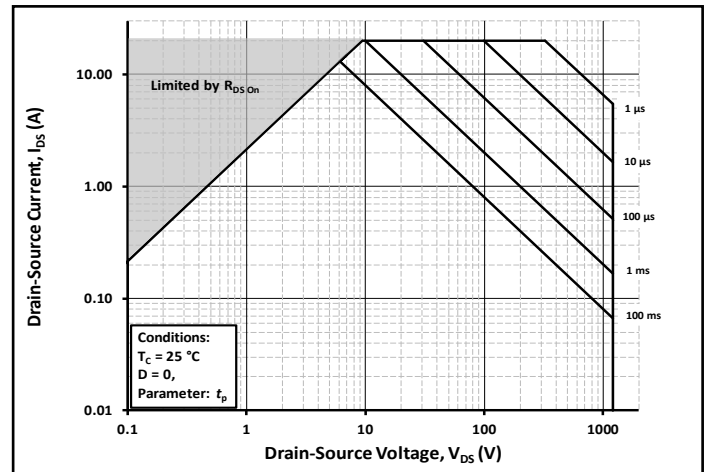


Figure 22. Safe Operating Area

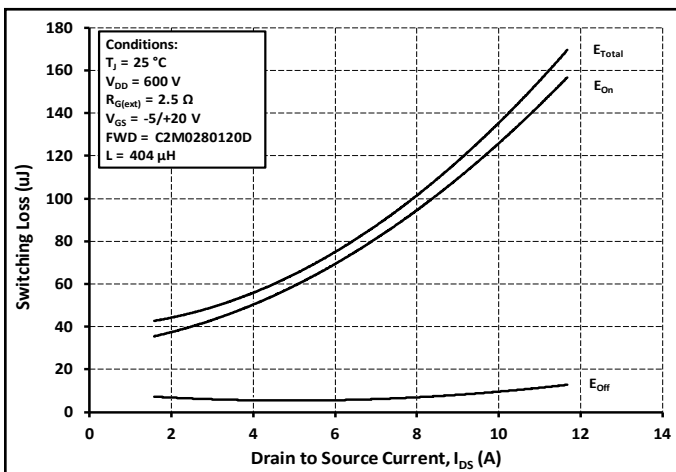


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600\text{ V}$)

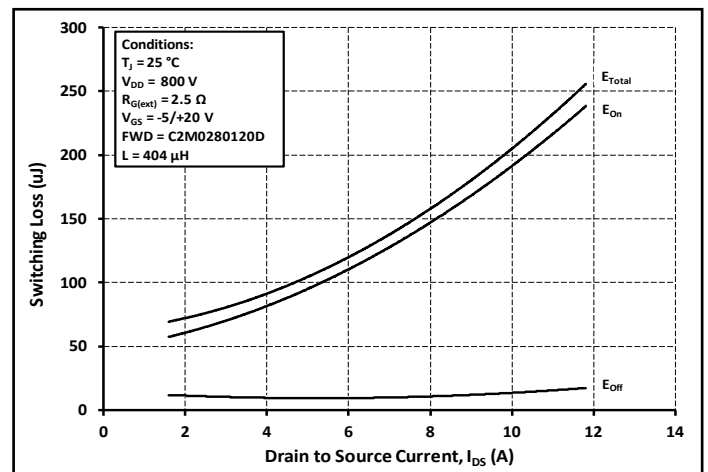


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 800\text{ V}$)

Typical Performance

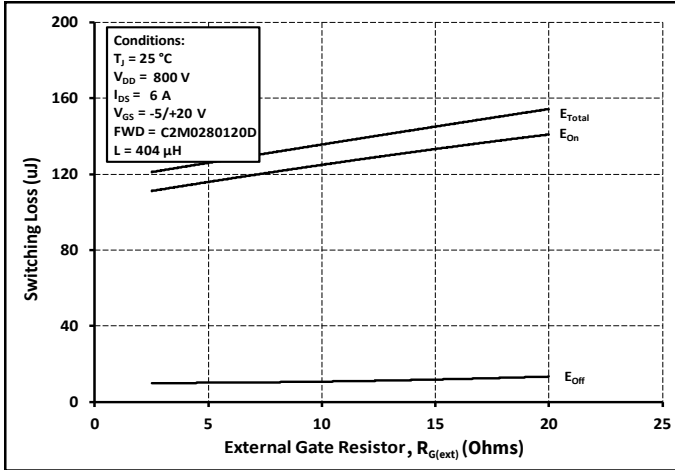


Figure 25. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

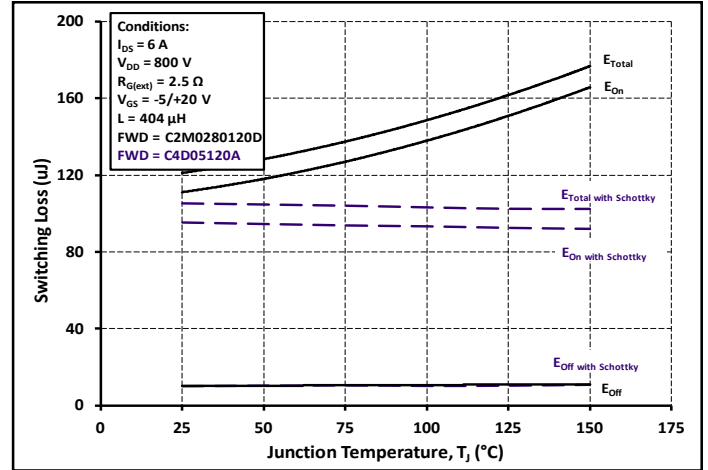


Figure 26. Clamped Inductive Switching Energy vs. Temperature

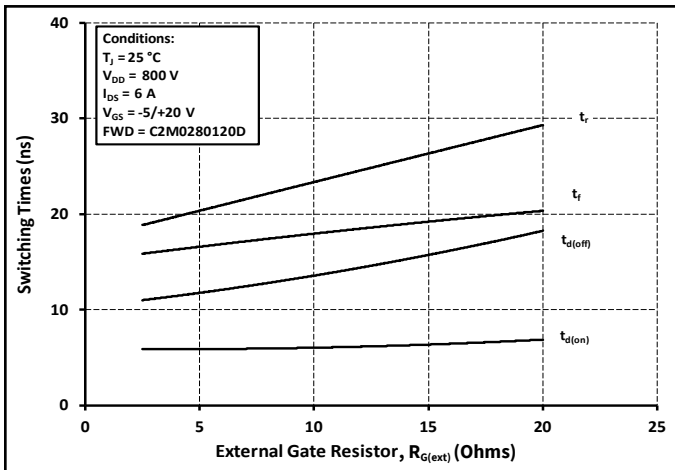


Figure 27. Switching Times vs. $R_{G(ext)}$

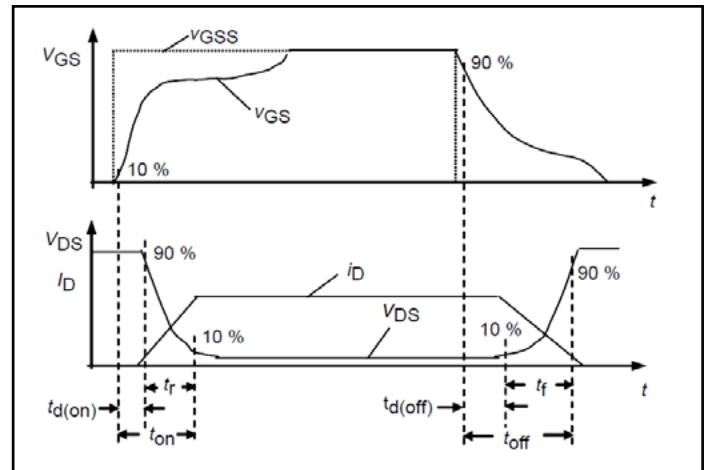


Figure 28. Switching Times Definition

Test Circuit Schematic

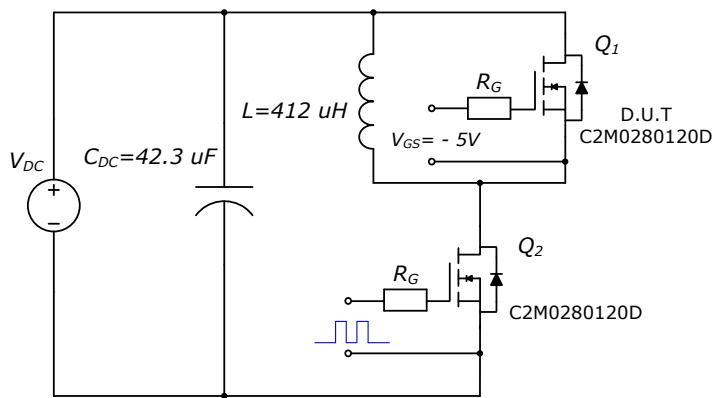
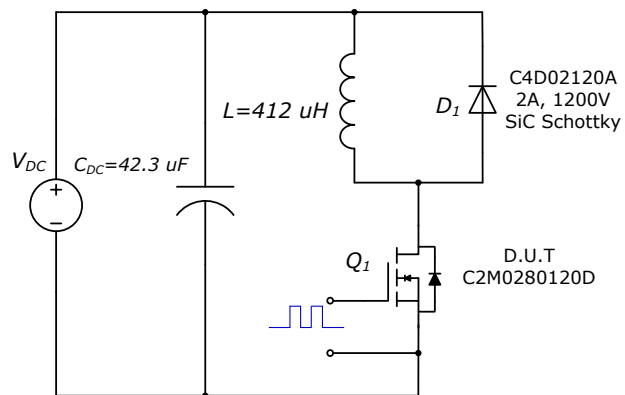
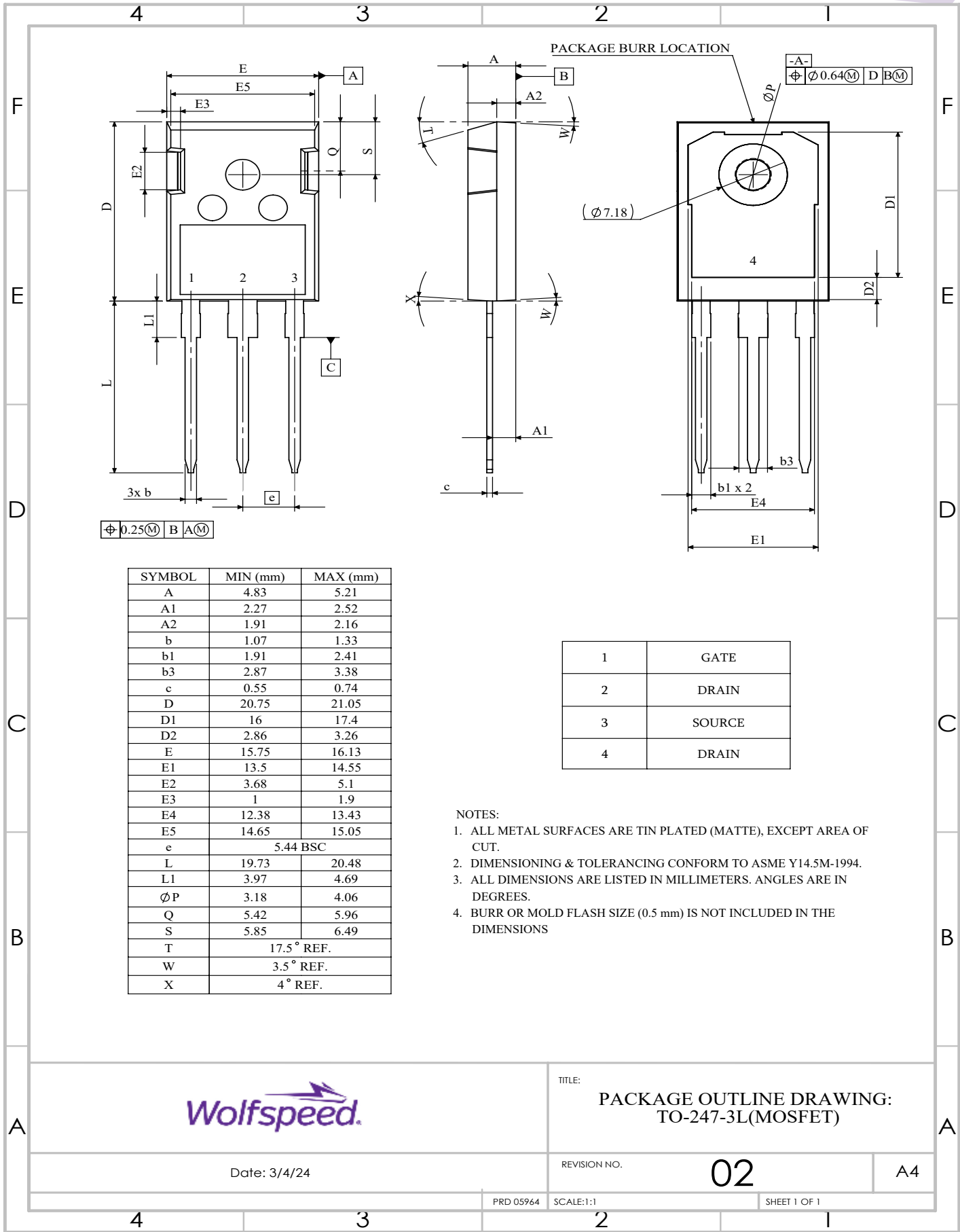
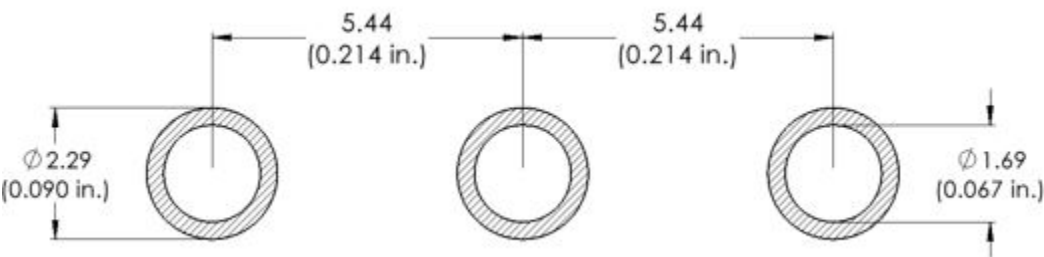


Figure 29. Clamped Inductive Switching Waveform Test Circuit

Package Dimensions – TO-247-3L



Recommended Solder Pad Layout



Revision History

Current Revision	Date of Release	Description of Changes
3	February-2021	N/A
4	November-2023	Updated Wolfspeed branding, package drawing, package image, and solder pad layout, added Revision History Table
5	October - 2024	Legal Disclaimer, POD, Table 1 layout, Diode Pulse Current Symbol

Related Links

- [SPICE Models](http://wolfspeed.com/power/tools-and-support): <http://wolfspeed.com/power/tools-and-support>
- [SiC MOSFET Isolated Gate Driver Reference Design](http://wolfspeed.com/power/tools-and-support): <http://wolfspeed.com/power/tools-and-support>
- [SiC MOSFET Evaluation Board](http://wolfspeed.com/power/tools-and-support): <http://wolfspeed.com/power/tools-and-support>

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