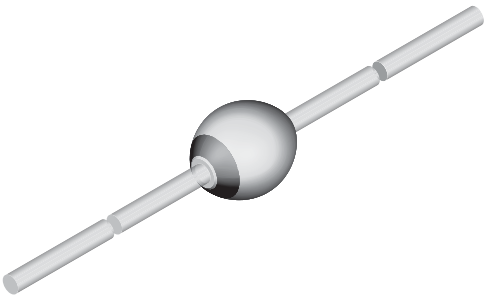
Standard Avalanche Sinterglass Diode



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FEATURES

- Controlled avalanche characteristics
- Glass passivated junction
- Hermetically sealed package
- Low reverse current
- High surge current loading
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT
HALOGEN FREE

DESIGN SUPPORT TOOLS

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3D Models Available

APPLICATIONS

- Rectification, general purpose

MECHANICAL DATA

Case: SOD-57
Terminals: plated axial leads, solderable per MIL-STD-750, method 2026
Polarity: color band denotes cathode end
Mounting position: any
Weight: approx. 369 mg

ORDERING INFORMATION (Example)

DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY
BYW56	BYW56-TR	5000 per 10" tape and reel	25 000
BYW56	BYW56-TAP	5000 per ammopack	25 000

PARTS TABLE

PART	TYPE DIFFERENTIATION	PACKAGE
BYW52	V_R = 200 V; $I_{F(AV)}$ = 2 A	SOD-57
BYW53	V_R = 400 V; $I_{F(AV)}$ = 2 A	SOD-57
BYW54	V_R = 600 V; $I_{F(AV)}$ = 2 A	SOD-57
BYW55	V_R = 800 V; $I_{F(AV)}$ = 2 A	SOD-57
BYW56	V_R = 1000 V; $I_{F(AV)}$ = 2 A	SOD-57

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
Reverse voltage = repetitive peak reverse voltage	See electrical characteristics	BYW52	$V_R = V_{RRM}$	200	V
		BYW53	$V_R = V_{RRM}$	400	V
		BYW54	$V_R = V_{RRM}$	600	V
		BYW55	$V_R = V_{RRM}$	800	V
		BYW56	$V_R = V_{RRM}$	1000	V
Peak forward surge current	t_p = 10 ms, half sine wave		I_{FSM}	50	A
Repetitive peak forward current			I_{FRM}	12	A
Average forward current	φ = 180 °		$I_{F(AV)}$	2	A
Pulse avalanche peak power	t_p = 20 µs half sine wave, T_j = 175 °C		P_R	1000	W
Pulse energy in avalanche mode, non repetitive (inductive load switch off)	$I_{(BR)R}$ = 1 A, T_j = 175 °C		E_R	20	mJ
i^2t-rating			i^2t	8	A^2s
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C

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MAXIMUM THERMAL RESISTANCE (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**TEST CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
Junction ambient	Lead length l = 10 mm, T_L = constant	R_{thJA}	45	K/W
	On PC board with spacing 25 mm	R_{thJA}	100	K/W

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Forward voltage	I_F = 1 A	V_F	-	0.9	1	V
Reverse current	$V_R = V_{RRM}$	I_R	-	0.1	1	µA
	$V_R = V_{RRM}$, T_j = 100 °C	I_R	-	5	10	µA
Breakdown voltage	I_R = 100 µA, t_p/T = 0.01, t_p = 0.3 ms	$V_{(BR)}$	-	-	1600	V
Diode capacitance	V_R = 4 V, f = 1 MHz	C_D	-	18	-	pF
Reverse recovery time	I_F = 0.5 A, I_R = 1 A, i_R = 0.25 A	t_{rr}	-	-	4000	ns
	I_F = 1 A, dI/dt = 5 A/µs, V_R = 50 V	t_{rr}	-	-	4000	ns
Reverse recovery charge	I_F = 1 A, dI/dt = 5 A/µs	Q_{rr}	-	-	200	nC

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

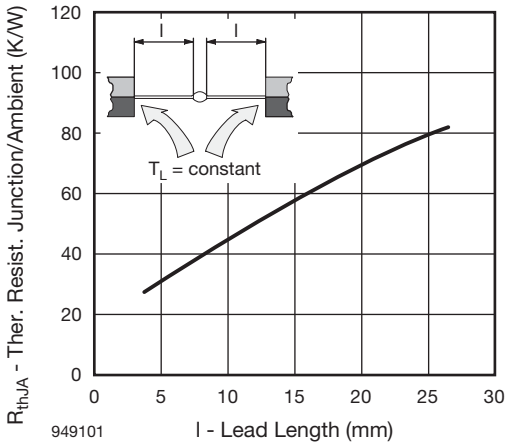

Fig. 1 - Typ. Thermal Resistance vs. Lead Length

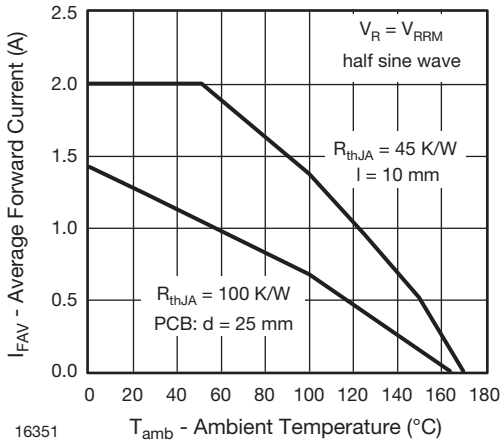

Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

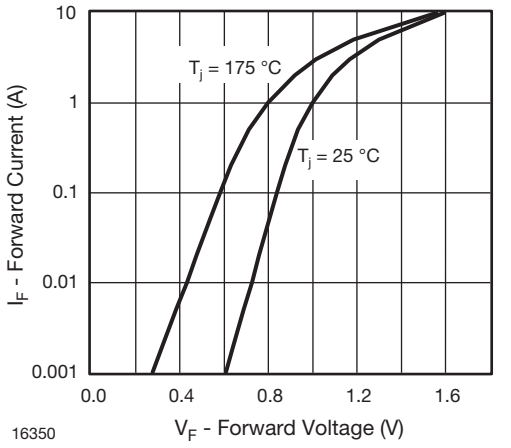

Fig. 2 - Forward Current vs. Forward Voltage

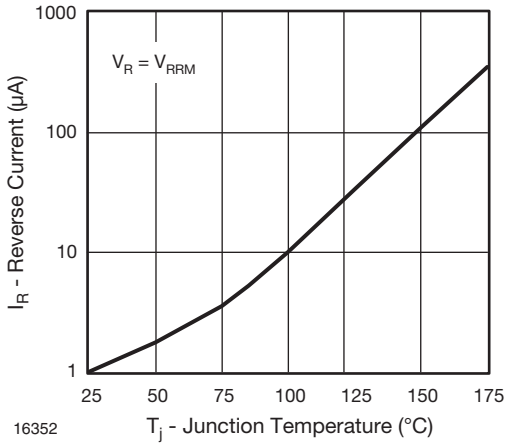

Fig. 4 - Reverse Current vs. Junction Temperature

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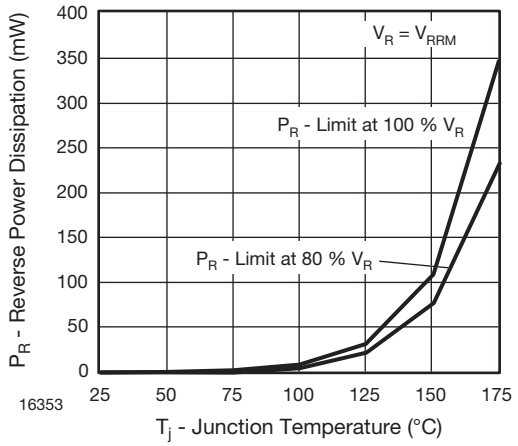


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

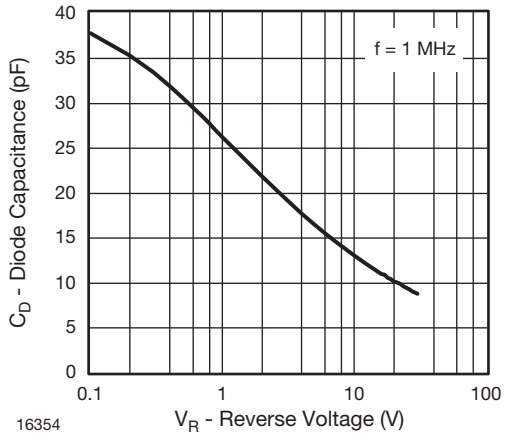


Fig. 6 - Diode Capacitance vs. Reverse Voltage

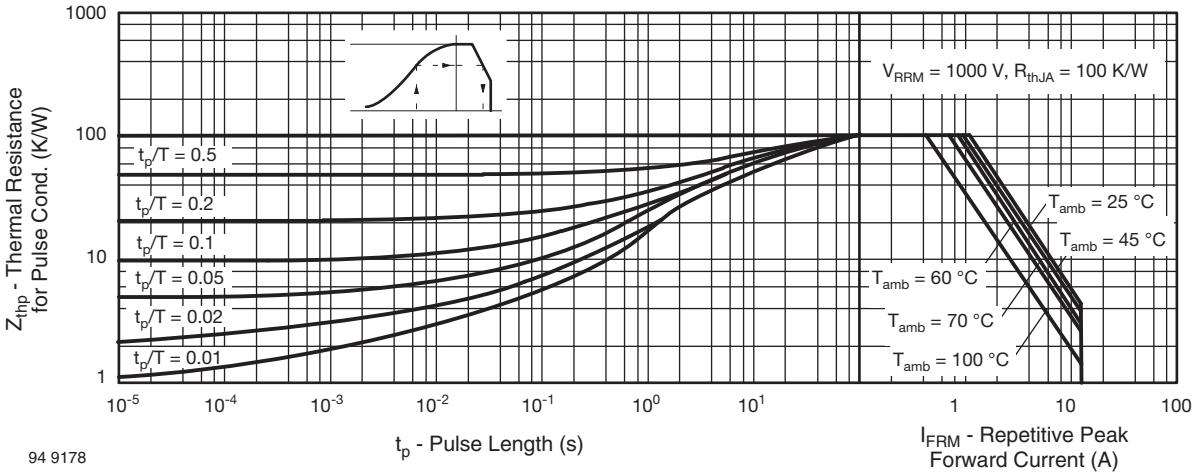
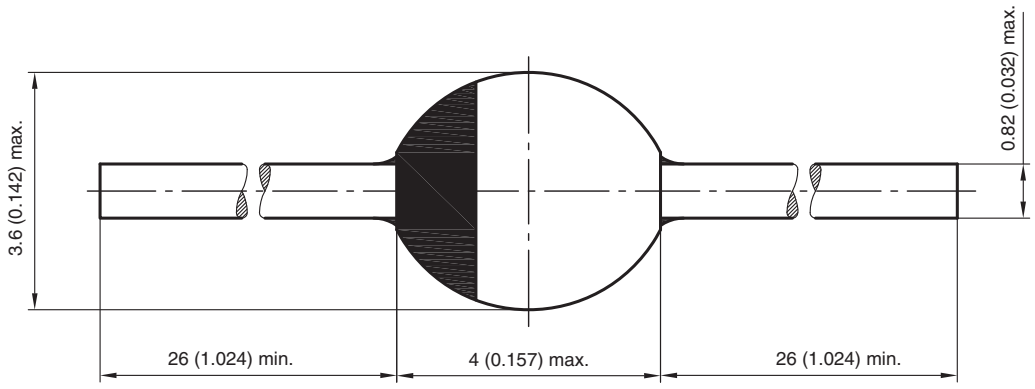


Fig. 7 - Thermal Response

PACKAGE DIMENSIONS in millimeters (inches): **SOD-57**



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