

BY550-50 - BY550-1000

AXIAL LEADED SILICON RECTIFIER DIODES

VOLTAGE RANGE: 50 - 1000V

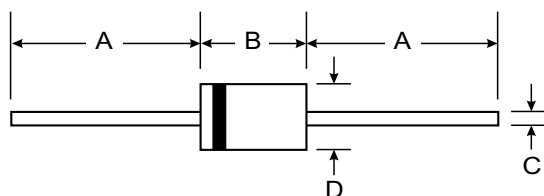
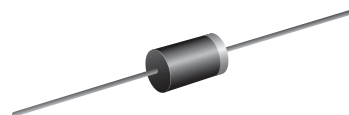
CURRENT: 5.0A

Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

Mechanical Data

- Case: DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



DO-201AD		
Dim	Min	Max
A	25.40	—
B	7.20	9.50
C	1.20	1.30
D	4.80	5.30
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	BY550 - 50	BY550 - 100	BY550 - 200	BY550 - 400	BY550 - 600	BY550 - 800	BY550 - 1000	Unit
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Current 0.375"(9.5mm) Lead Length T _a = 60°C	I _F	5.0							A
Peak Forward Surge Current 8.3ms Single half sine wave Superimposed on rated load (JEDEC Method)	I _{FSM}	300							A
Maximum Forward Voltage at I _F = 5.0 Amps.	V _F	1.1							V
Maximum DC Reverse Current T _a = 25 °C at rated DC Blocking Voltage T _a = 100 °C	I _R	20							μA
	I _{R(H)}	50							μA
Typical Junction Capacitance (Note1)	C _J	50							pF
Typical Thermal Resistance (Note2)	R _{θJA}	18							°C/W
Junction Temperature Range	T _J	- 65 to + 175							°C
Storage Temperature Range	T _{STG}	- 65 to + 175							°C

Notes :

- (1) Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc
- (2) Thermal resistance from Junction to Ambient at 0.375" (9.5mm) Lead Lengths, P.C. Board Mounted.

RATING AND CHARACTERISTIC CURVES (BY550-50 - BY550-1000)

FIG.1 - DERATING CURVE FOR OUTPUT RECTIFIED CURRENT

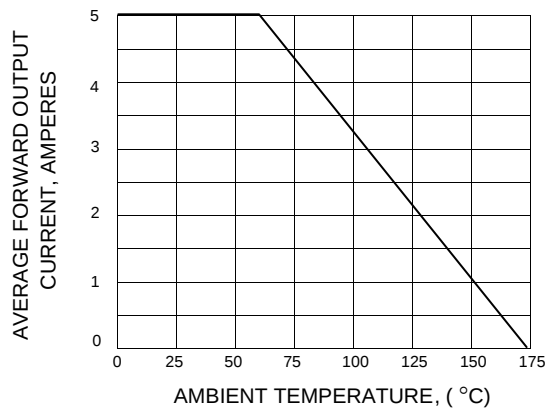


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

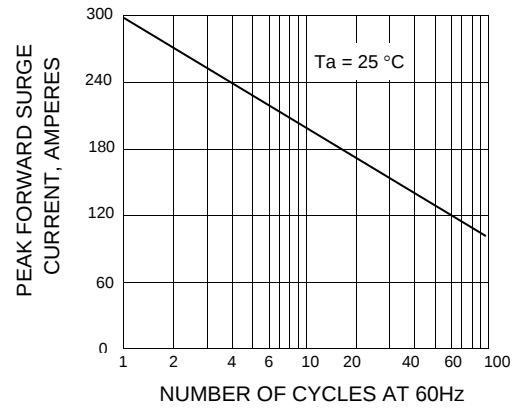


FIG.3 - TYPICAL FORWARD CHARACTERISTICS

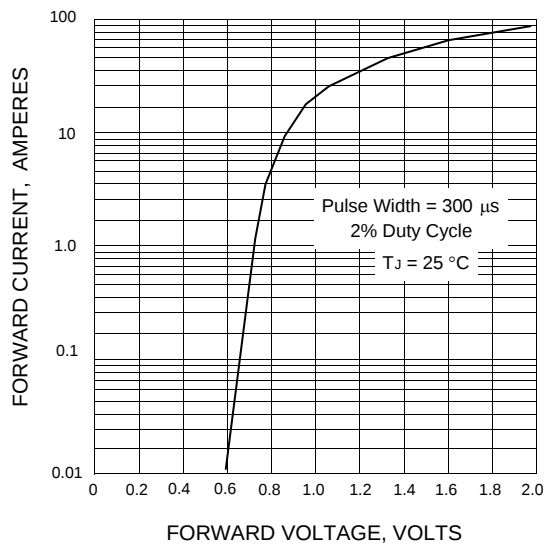


FIG.4 - TYPICAL JUNCTION CAPACITANCE

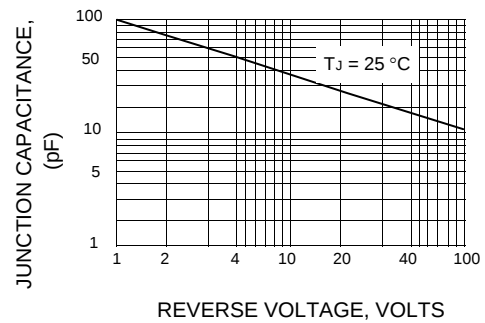


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

