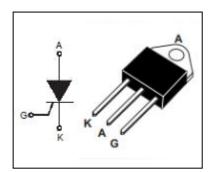


isc Thyristors BTW69-1200

## **APPLICATIONS**

- Available in high power packages, BTW69 Series is suitable in applications where power handling and power dissipation are critical, such as solid state relays, welding equipment, high power motor
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	1200	V
$V_{RRM}$	Repetitive peak reverse voltage	1200	V
I <sub>T(AV)</sub>	Average on-stage current , Tc=70° C	31	Α
I <sub>T(RMS)</sub>	RMS on-state current Tc=70° C	50	Α
I <sub>TSM</sub>	Surge non-repetitive on-state current T <sub>P</sub> =10ms	500	Α
P <sub>G(AV)</sub>	Average gate power dissipation	1	W
Tj	Operating junction temperature	-40~125	$^{\circ}$
T <sub>stg</sub>	Storage temperature	-40~150	$^{\circ}\mathbb{C}$

## **ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>RM</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25℃		10	μ <b>A</b>
			T <sub>j</sub> =125℃		5	mA
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>DM</sub> =V <sub>DRM</sub>	T <sub>j</sub> =25℃		10	μА
			T <sub>j</sub> =125℃		5	mA
$V_{TM}$	On-state voltage	Ітм= 100А			1.6	٧
I <sub>GT</sub>	Gate-trigger current	$V_D$ = 12 V; RL= 33 $\Omega$			50	mA
$V_{GT}$	Gate-trigger voltage	V <sub>D</sub> = 12 V; RL= 33 Ω			1.3	V
R <sub>th(j-c)</sub>	Thermal resistance	Junction to case			0.45	°C/W

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BTW69-1200



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## **NOTICE:**

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