

UTC UNISONIC TECHNOLOGIES CO., LTD

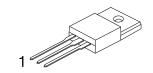
BTA16A **TRIAC**

16A TRIACS

DESCRIPTION

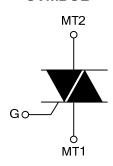
The UTC BTA16A is a 16A triacs which can be operated in 3 quadrants only, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA16A is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.



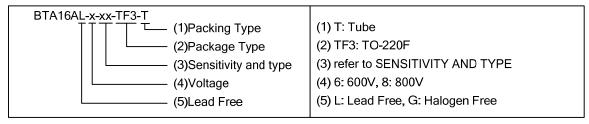
TO-220F

SYMBOL



ORDERING INFORMATION

Ordering	Doolsone	Pin .	Assignn	Daakina			
Lead Free	Halogen Free	Package	1	2	3	Packing	
BTA16AL-x-xx-TF3-T	BTA16AG-x-xx-TF3-T	TO-220F	MT1	MT2	G	Tube	

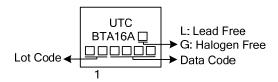


SENSITIVITY AND TYPE

DART NUMBER VOL		ΓAGE	OFNOITIV/ITV	TVDE
PART NUMBER	600V	800V	SENSITIVITY	TYPE
BW	0	©	50mA	SNUBBERLESS
CW	0	©	35mA	SNUBBERLESS
SW	0	©	10mA	LOGIC LEVEL

: Available

MARKING



www.unisonic.com.tw 1 of 4 BTA16A TRIAC

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave) T _C =86°C		T _C =86°C	I _{T(RMS)}	16	Α
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz	t=20ms	I	160	Α
Cycle, T _J initial=25°C)	F=60 Hz	t=16.7ms	I _{TSM}	168	Α
I ² t Value for Fusing	t _P =10ms		l ² t	144	A^2s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	T _J =125°C	dl/dt	50	A/μs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	T _J =25°C	V _{DSM} /V _{RSM}	V _{DRM} /V _{RRM} +100	٧
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation T _J =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature		T_J	-40~+125	°C	
Storage Junction Temperature			T _{STG}	-40~+150	°C

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	60	°C/W	
Junction to Case (AC)	θ_{JC}	2.1	°C/W	

■ ELECTRICAL CHARACTERISTICS (T_J =25°C unless otherwise specified.)

FOR SNUBBERLESS TYPE and LOGIC LEVEL TYPE (3 QUADRANTS)

DADAMETED	SYMBOL TEST CONDITIONS		IONIC	SW			CW			BW			UNIT
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V, R _L =33Ω	1-11-111			10			35			50	mA
Gate Trigger Voltage	V_{GT}		1-11-111			1.3			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	1-11-111	0.2			0.2			0.2			٧
Holding Current (Note 2)	I _H	I _T =500mA				15			35			50	mA
Latching Current	L	I _G =1.2I _{GT}	1-111			25			50			70	mA
Latering Current	ıL	IG-1.2IG	II			30			60			80	mΑ
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		40			500			1000			V/µs
Critical Rate of Rise		(dV/dt)c=0.1V/μs, Τ _J =125°C		8.5									A/ms
of Off-State Voltage at Commutation	(dl/dt)c	dl/dt)c (dV/dt)c=10V/µs, T _J =125°C		3.0									A/ms
(Note 2)		Without Snubber T _J =125°C					8.5			14			A/ms

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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■ STATIC CHARACTERISTICS

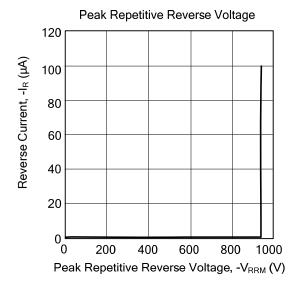
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note 2)	V_{TM}	I_{TM} =22.5A, t_p =380 μ s	T _J =25°C			1.55	V
Threshold Voltage(Note 2)	V_{TO}		T _J =125°C			0.85	٧
Dynamic Resistance(Note 2)	R_D		T _J =125°C			25	mΩ
Denetitive Deals Off State Comment	I _{DRM}	-\/	T _J =25°C			5	μΑ
Repetitive Peak Off-State Current	I _{RRM}	$V_{DRM}=V_{RRM}$	T _J =125°C			2	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

^{2.} For both polarities of MT2 referenced to MT1.

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■ TYPICAL CHARACTERISTICS



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