

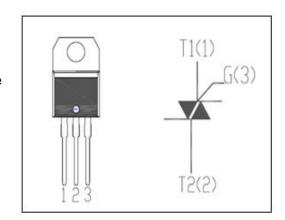
# isc Thyristors BTA16-400B

## **DESCRIPTION**

- With TO-220 packaging
- Operating in 3 quadrants
- · High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## **APPLICATIONS**

- Switching applications
- Phase control
- Static switching on inductive or resistive load



## ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER			UNIT
$V_{DRM}$	Repetitive peak off-state voltage			V
$V_{RRM}$	Repetitive peak reverse voltage	400	V	
I <sub>T(RSM)</sub>	Average on-state current	<b>@</b> Tc=80℃	16	Α
I <sub>TSM</sub>	Surge non-repetitive on-state current	50Hz 60Hz	160 170	Α
P <sub>G(AV)</sub>	Average gate power dissipation ( over any 20 ms period )			W
Tj	Operating junction temperature			${\mathbb C}$
T <sub>stg</sub>	Storage temperature			$^{\circ}$



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## **ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS			MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> Rated;	V <sub>R</sub> =V <sub>RRM</sub> Rated; Tj=25℃			0.01	^
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> =V <sub>DRM</sub> Rated;	Tj=125℃			2.0	mA
$V_{TM}$	On-state voltage	I <sub>T</sub> =22.5A				1.6	V
І <sub>бт</sub>	Gate-trigger current	)	I		50	- mA	
		V <sub>D</sub> =12V;R <sub>L</sub> =33 Ω	II				50
		VD = 12 V,11\(\(\text{L} = 00 \) as		III			50
		IV				100	
$V_{GT}$	Gate-trigger voltage	V <sub>D</sub> =12V;R <sub>L</sub> =33 Ω				1.5	V
Rth (j-c)	Junction to case	For DC				2.9	°C/W

#### **NOTICE:**

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