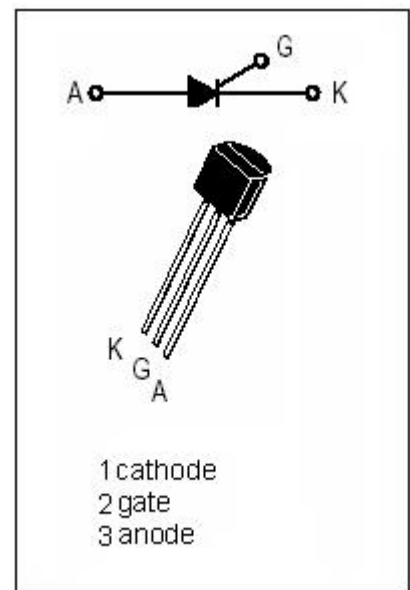


FEATURES

- With TO-92 package
- Sensitive gate trigger current
- Low reverse and forward blocking current
- Low holding current
- For general purpose switching and phase control applications.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	600	V
V_{RRM}	Repetitive peak off-state voltage	600	V
$I_{\text{T(AV)}}$	Average on-state current	0.5	A
$I_{\text{T(RMS)}}$	RMS on-state current	0.8	A
P_{GM}	Peak gate power	2	W
$P_{\text{G(AV)}}$	Average gate power	0.1	W
I_{TSM}	Non-repetitive peak on-state current	8	A
T_j	Operating junction temperature	125	$^\circ\text{C}$
T_{stg}	Storage temperature	-40~+ 150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage	$I_D=0.1\text{mA}$	600		V
V_{RRM}	Repetitive peak reverse voltage	$I_D=0.5\text{mA}$	600		V
I_{RRM}	Repetitive peak reverse current	$V_{\text{RRM}}=600\text{V}$		10	μA
I_{DRM}	Repetitive peak off-state current	$V_{\text{DRM}}=600\text{V}$		10	μA
I_{GT}	Gate trigger current	$V_D=12\text{V}; I_T=10\text{mA}$		200	μA
V_{TM}	On-state voltage	$I_T=1\text{A}$		1.35	V
I_{H}	Holding current	$I_{\text{GT}}=0.5\text{mA}, V_D=12\text{V}$		5	mA
V_{GT}	Gate trigger voltage	$V_D=12\text{V}; I_T=10\text{mA}$		0.8	V

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