

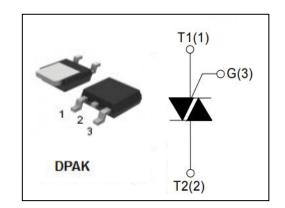
# isc Thyristors BT137S-800F

### **DESCRIPTION**

- · With TO-252( DPAK ) packaging
- · Operating in 4 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### **APPLICATIONS**

- · Switching applications
- Phase control
- · Static switching on inductive or resistive load



### ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25℃)

SYMBOL	PARAMETER			UNIT
$V_{DRM}$	Repetitive peak off-state voltage			V
$V_{RRM}$	Repetitive peak reverse voltage			V
I <sub>T(RSM)</sub>	Average on-state current			Α
I <sub>TSM</sub>	Surge non-repetitive on-state current	50HZ 60HZ	65 71	А
$P_{G(AV)}$	Average gate power dissipation ( over any 20 ms period )			W
T <sub>j</sub>	Operating junction temperature			$^{\circ}\mathbb{C}$
T <sub>stg</sub>	Storage temperature			$^{\circ}\!\mathbb{C}$

## **ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS			MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> Rated;			0.5		
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> =V <sub>DRM</sub> Rated;	Tj=125℃			0.5	mA
$V_{TM}$	On-state voltage	I <sub>T</sub> =10A				1.65	V
I <sub>GT</sub>	Gate-trigger current			I		25	
		\/ -40\/:I -0 4A:		II		25	
		$V_D = 12V; I_T = 0.1A;$		III		25	mA
				IV		70	
V <sub>GT</sub>	Gate-trigger voltage	$V_D = 12V; I_T = 0.1A;$				1.5	V
Rth (j-mb)	Junction to mounting base	Half cycle				2.4	°C/W



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