

# isc Thyristors

### **INCHANGE Semiconductor**

DPAK

# BT137S-800E

Δ.

7(3)

T1(1)

#### DESCRIPTION

- With TO-252( DPAK ) packaging
- Operating in 4 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

#### **APPLICATIONS**

- Switching applications
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- •

#### Α

Phase control Static switching on inductive or resistive load							
ABSOLU	TE MAXIMUM RATINGS(Ta=25℃)						
SYMBOL	PARAMETER	MAX	UNIT				
V <sub>DRM</sub>	Repetitive peak off-state voltage	800	V				
$V_{RRM}$	Repetitive peak reverse voltage	800	V				
I <sub>T(RSM)</sub>	Average on-state current	8	Α				
I <sub>TSM</sub>	Surge non-repetitive on-state current50HZ60HZ		А				
$P_{G(AV)}$	Average gate power dissipation (over any 20 ms period)	0.5	W				
Tj	Operating junction temperature	-40~125	°C				
T <sub>stg</sub>	Storage temperature	-40~150	°C				

#### ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS			MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> Rated;	T: 405°0			0.5	mA
I <sub>DRM</sub>	Repetitive peak off-state current	$V_D = V_{DRM}$ Rated; $Tj = 125^{\circ}C$		C			
V <sub>TM</sub>	On-state voltage	I <sub>T</sub> =10A				1.65	V
Igt	Gate-trigger current			Ι		10	
				II		10	]
		V <sub>D</sub> =12V;I <sub>T</sub> =0.1A;		III		10 mA	
				IV		20	]
V <sub>GT</sub>	Gate-trigger voltage	V <sub>D</sub> =12V;I <sub>T</sub> =0.1A;				1.5	V
Rth (j-mb)	Junction to mounting base	Half cycle				2.4	°C/W

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