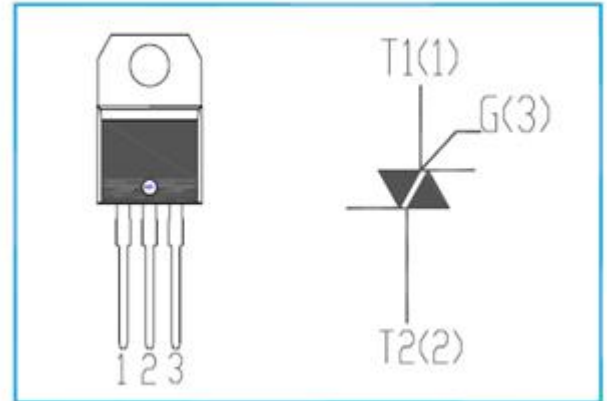


**FEATURES**

- With TO-220 package
- Glass passivated triacs in a plastic envelope, for use in general purpose bidirectional switching and phase control applications, which are intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**ABSOLUTE MAXIMUM RATINGS**( $T_a=25^\circ\text{C}$ )

SYMBOL	PARAMETER	MIN	UNIT
$V_{\text{DRM}}$	Repetitive peak off-state voltage	800	V
$V_{\text{RRM}}$	Repetitive peak off-state voltage	800	V
$I_{\text{T(RMS)}}$	RMS on-state current (full sine wave)	8	A
$I_{\text{TSM}}$	Non-repetitive peak on-state current	65	A
$P_{\text{GM}}$	Peak gate power dissipation	5	W
$P_{\text{G(AV)}}$	Average gate power dissipation	0.5	W
$T_j$	Operating junction temperature	110	$^\circ\text{C}$
$T_{\text{stg}}$	Storage temperature	-45~150	$^\circ\text{C}$

**ELECTRICAL CHARACTERISTICS** ( $T_c=25^\circ\text{C}$  unless otherwise specified)

SYMBOL	PARAMETER		CONDITIONS	MIN	MAX	UNIT
$I_{\text{RRM}}$	Repetitive peak reverse current		$V_R=V_{\text{RRM}}$ , $V_R=V_{\text{RRM}}$ , $T_j=110^\circ\text{C}$		0.02 0.5	mA
$I_{\text{DRM}}$	Repetitive peak off-state current		$V_D=V_{\text{DRM}}$ , $V_D=V_{\text{DRM}}$ , $T_j=110^\circ\text{C}$		0.02 0.5	mA
$I_{\text{GT}}$	Gate trigger current	I	$V_D=12\text{V}$ ; $I_T=0.1\text{A}$ , $R_L=30\ \Omega$		50	mA
		II			50	
		III			50	
		IV			100	
$V_{\text{TM}}$	On-state voltage		$I_T=10\text{A}$		1.65	V
$I_{\text{H}}$	Holding current		$I_{\text{GT}}=0.1\text{A}$ , $V_D=12\text{V}$		40	mA
$V_{\text{GT}}$	Gate trigger voltage		$V_D=12\text{V}$ ; $R_L=30\ \Omega$ all quadrant		1.5	V

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