

# BSX 29

## HIGH-SPEED SWITCH AND RF AMPLIFIER

### PNP DIFFUSED SILICON PLANAR EPITAXIAL TRANSISTOR

**GENERAL DESCRIPTION**-The BSX 29 is a 700 Mc/s PNP silicon PLANAR epitaxial transistor designed for saturated and nonsaturated switching circuits requiring up to 200 milliamperes of collector current. It is suitable for 20 Mc/s amplifiers, 10.7 Mc/s IF amplifiers, and 100 Mc/s oscillator converter circuits.

#### ABSOLUTE MAXIMUM RATINGS (Note 1)

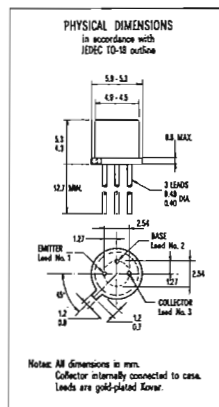
Maximum Temperatures	
Storage Temperature	-65°C to +200°C
Operating Junction Temperature	200°C Maximum
Lead Temperature (Soldering, 60 sec Time Limit)	300°C Maximum

#### Maximum Power Dissipations

Total Dissipation at 25°C Case Temperature (Notes 2 and 3)	1.2 Watts
at 25°C Ambient Temperature (Notes 2 and 3)	0.36 Watt

#### Maximum Voltages

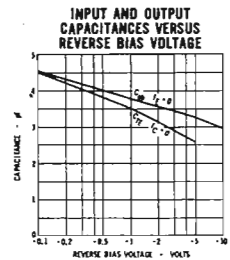
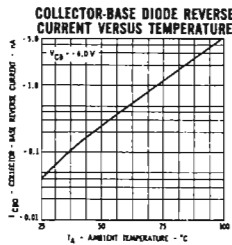
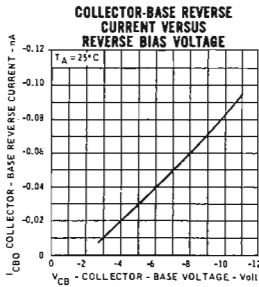
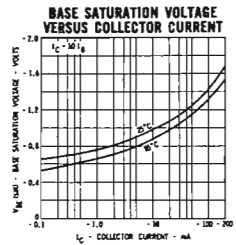
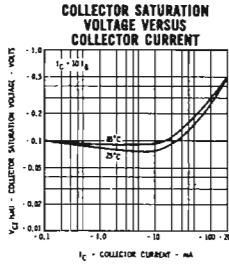
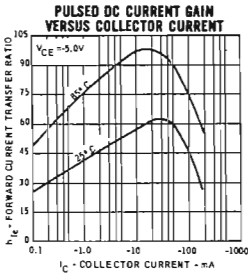
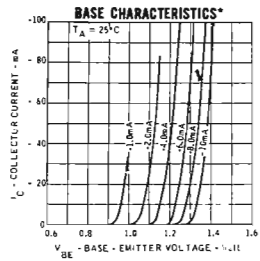
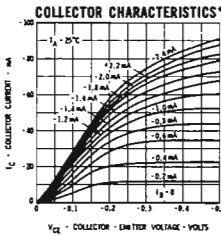
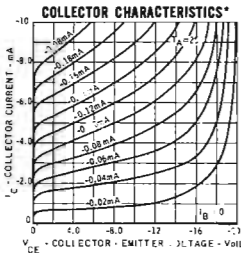
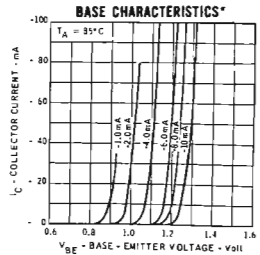
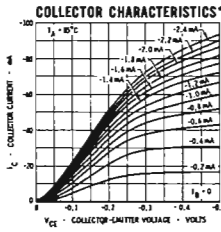
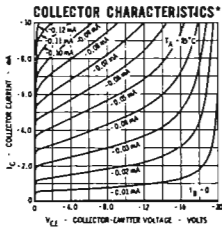
V <sub>CB0</sub> Collector to Base Voltage	-12 Volts
V <sub>CEO</sub> Collector to Emitter Voltage (Note 4)	-12 Volts
V <sub>CES</sub> Collector to Emitter Voltage	-12 Volts
V <sub>EBO</sub> Emitter to Base Voltage	-4.0 Volts



#### ELECTRICAL CHARACTERISTICS (25°C free air temperature unless otherwise noted)

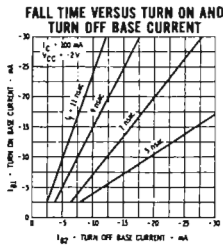
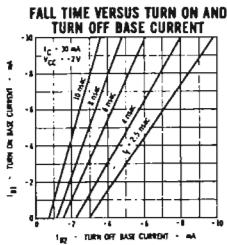
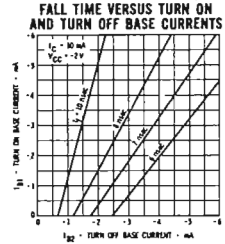
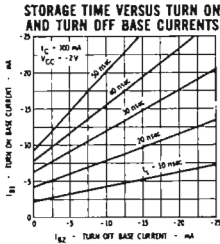
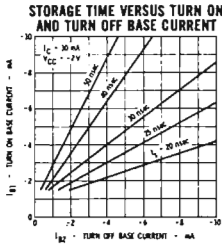
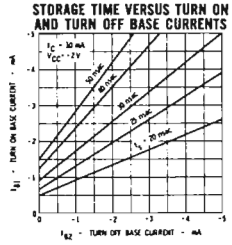
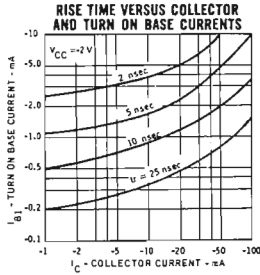
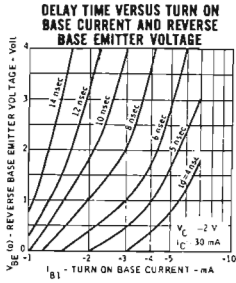
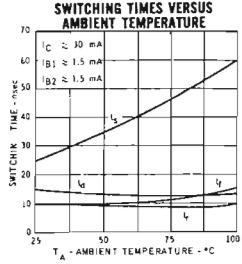
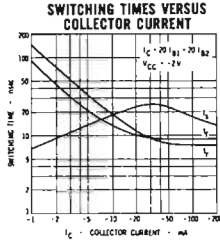
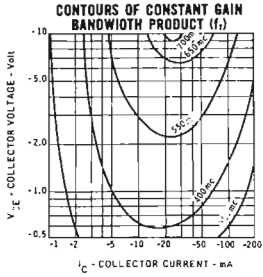
SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNITS	TEST CONDITIONS
h <sub>FE</sub>	DC Pulse Current Gain (Note 5)	25	50			I <sub>C</sub> = 10 mA, V <sub>CE</sub> = -0.3 V
h <sub>FE</sub>	DC Pulse Current Gain (Note 5)	30	60	120		I <sub>C</sub> = 30 mA, V <sub>CE</sub> = -0.5 V
h <sub>FE</sub>	DC Pulse Current Gain (Note 5)	20	40			I <sub>C</sub> = 100 mA, V <sub>CE</sub> = -1.0 V
V <sub>BE</sub> (sat)	Base Saturation Voltage	-0.78	-0.96	-0.98	V	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1.0 mA
V <sub>BE</sub> (sat)	Base Saturation Voltage	-0.85	-1.12	-1.2	V	I <sub>C</sub> = 30 mA, I <sub>B</sub> = 3.0 mA
V <sub>BE</sub> (sat)	Base Saturation Voltage		-1.4	-1.7	V	I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA
V <sub>CE</sub> (sat)	Collector Saturation Voltage	-0.07	-0.15	V		I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1.0 mA
V <sub>CE</sub> (sat)	Collector Saturation Voltage	-0.1	-0.2	V		I <sub>C</sub> = 30 mA, I <sub>B</sub> = 3.0 mA
V <sub>CE</sub> (sat)	Collector Saturation Voltage	-0.25	-0.5	V		I <sub>C</sub> = 100 mA, I <sub>B</sub> = 10 mA
V <sub>CE</sub> (sat)(85°C)	Collector Saturation Voltage	-0.15	-0.4	V		I <sub>C</sub> = 30 mA, I <sub>B</sub> = 3.0 mA
I <sub>CES</sub>	Collector Reverse Current	-0.05	80	nA		V <sub>CE</sub> = -6.0 V, V <sub>BE</sub> = 0
I <sub>CES</sub> (85°C)	Collector Reverse Current	0.003	5.0	μA		V <sub>CE</sub> = -6.0 V, V <sub>BE</sub> = 0
BV <sub>CB0</sub>	Collector to Base Breakdown Voltage	-12		V		I <sub>C</sub> = 10 μA, I <sub>E</sub> = 0
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	-12		V		I <sub>C</sub> = 10 μA, I <sub>E</sub> = 0
BV <sub>EBO</sub>	Emitter to Base Breakdown Voltage	-4.0		V		I <sub>E</sub> = 100 μA, I <sub>C</sub> = 0
V <sub>CEO</sub> (sust)	Collector to Emitter Sustaining Voltage (Notes 4 and 5)	-12		V		I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0 (pulsed)
h <sub>fe</sub>	High Frequency Current Gain (f = 100 Mc/s)	4.0	7.0			I <sub>C</sub> = 30 mA, V <sub>CE</sub> = -10 V
C <sub>ob</sub>	Output Capacitance	3.3	6.0	pF		I <sub>E</sub> = 0, V <sub>CB</sub> = -5.0 V
C <sub>TE</sub>	Emitter Transition Capacitance	3.8	6.0	pF		I <sub>C</sub> = 0, V <sub>EB</sub> = -0.5 V
t <sub>on</sub>	Turn On Time (Note 6)	25	60	nsec		I <sub>C</sub> = 30 mA, I <sub>BI</sub> = 1.5 mA
t <sub>off</sub>	Turn Off Time (Note 6)	35	90	nsec		I <sub>C</sub> = 30 mA, I <sub>BI</sub> = 1.5 mA, I <sub>B2</sub> = -1.5 mA

TYPICAL ELECTRICAL CHARACTERISTICS

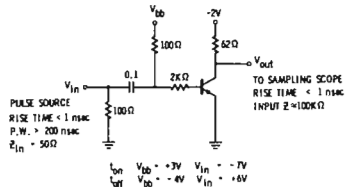


\*Single family characteristics on Transistor Curve Tracer.

## TYPICAL ELECTRICAL CHARACTERISTICS

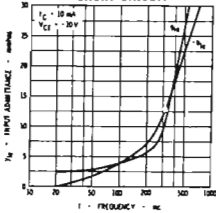


### SWITCHING TIME TEST CIRCUIT

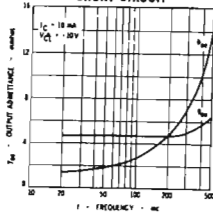


TYPICAL ELECTRICAL CHARACTERISTICS

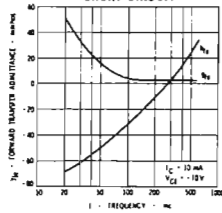
INPUT ADMITTANCE VERSUS FREQUENCY-OUTPUT SHORT CIRCUIT



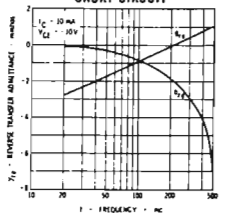
OUTPUT ADMITTANCE VERSUS FREQUENCY-INPUT SHORT CIRCUIT



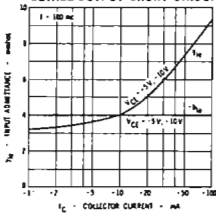
FORWARD TRANSFER ADMITTANCE VERSUS FREQUENCY-OUTPUT SHORT CIRCUIT



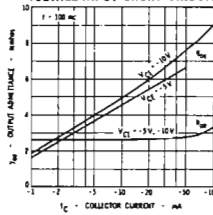
REVERSE TRANSFER ADMITTANCE VERSUS FREQUENCY-INPUT SHORT CIRCUIT



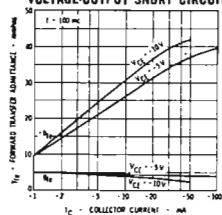
INPUT ADMITTANCE VERSUS COLLECTOR CURRENT AND VOLTAGE-OUTPUT SHORT CIRCUIT



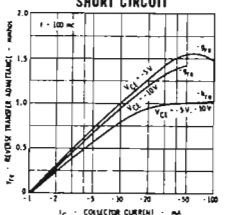
OUTPUT ADMITTANCE VERSUS COLLECTOR CURRENT AND VOLTAGE-INPUT SHORT CIRCUIT



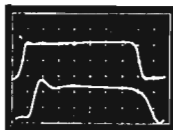
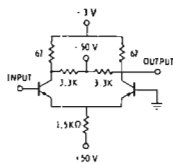
FORWARD TRANSFER ADMITTANCE VERSUS COLLECTOR CURRENT AND VOLTAGE-OUTPUT SHORT CIRCUIT



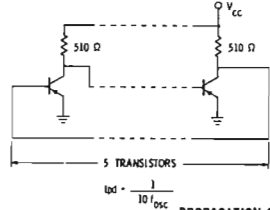
REVERSE TRANSFER ADMITTANCE VERSUS COLLECTOR CURRENT AND VOLTAGE-INPUT SHORT CIRCUIT



NON SATURATED SWITCHING PERFORMANCE

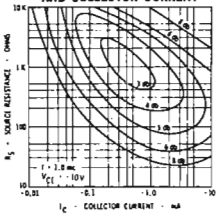


FIVE STAGE RING OSCILLATOR FOR MEASUREMENT OF PROPAGATION DELAY

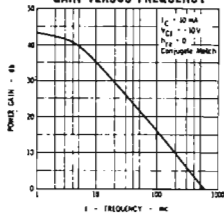


$$t_{pd} = \frac{1}{10 f_{osc}}$$

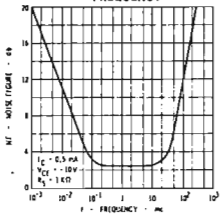
NOISE FIGURE VERSUS SOURCE RESISTANCE AND COLLECTOR CURRENT



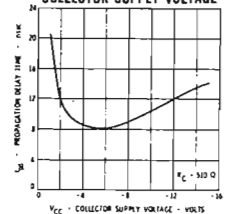
IDEALIZED SMALL SIGNAL POWER GAIN VERSUS FREQUENCY



NOISE FIGURE VERSUS FREQUENCY



PROPAGATION DELAY TIME VERSUS COLLECTOR SUPPLY VOLTAGE



NOTES:

- (1) These ratings are limiting values above which the serviceability of any individual semiconductor device may be impaired.
- (2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- (3) These ratings give a maximum junction temperature of 200°C and junction-to-case thermal resistance of 146°C/watt (derating factor of 6.85 mW/°C). Junction-to-ambient thermal resistance of 486°C/watt (derating factor of 2.06 mW/°C).
- (4) Rating refers to a high-current point where collector-to-emitter voltage is lowest. For more information send for SGS Publication AR 5.
- (5) Pulse Conditions: length = 300 μsec; duty cycle = 1%.
- (6) See switching circuits for exact values of I<sub>C</sub>, I<sub>B1</sub>, and I<sub>B2</sub>.