BLF7G27L-100; BLF7G27LS-100 Power LDMOS transistor

AMPLEON

Rev. 5 — 1 December 2016

Product data sheet

Product profile

1.1 General description

100 W LDMOS power transistor for base station applications at frequencies from 2500 MHz to 2700 MHz.

Typical performance Table 1.

Typical RF performance at $T_{case} = 25 \, ^{\circ}$ C in a common source class-AB production test circuit.

Mode of operation	f	I _{Dq}	V _{DS}	P _{L(AV)}	Gp	η_D	ACPR _{885k}	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2500 to 2700	900	28	20	18	28	-45 <u>[1]</u>	-
Single carrier W-CDMA	2500 to 2700	900	28	25	17.5	30	-	-41 <mark>2</mark>

^[1] Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 2500 MHz to 2700 MHz frequency range.

^{[2] 3}GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

2. Pinning information

Table 2. Pinning

Pin	Description		Simplified outline	Graphic symbol
BLF7G27L	-100 (SOT502A)			
1	drain			_
2	gate		5 1 5	1
3	source	[1]	2 2 3	2 - 3 3 sym112
BLF7G27L	S-100 (SOT502B)			-
1	drain			
2	gate		1 3	1
3	source	<u>[1]</u>	2	2 3 3 sym112

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BLF7G27L-100	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A		
BLF7G27LS-100	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	28	Α
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	T_{case} = 80 °C; P_{L} = 100 W	0.25	K/W

BLF7G27L-100_7G27LS-100

6. Characteristics

Table 6. Characteristics

 T_i = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 1 \text{ mA}$	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 153 mA	1.5	1.8	2.3	٧
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	5	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	25.1	29	-	А
I_{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	500	nA
g _{fs}	forward transconductance	V_{DS} = 10 V; I_{D} = 153 mA	-	1.34	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 5.35 \text{ A}$	-	0.1	-	Ω

7. Test information

Remark: All testing performed in a class-AB production test circuit.

Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz; f_1 = 2500 MHz; f_2 = 2700 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 900 mA; T_{case} = 25 °C; unless otherwise specified.

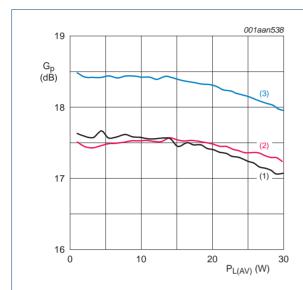
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	20	-	W
Gp	power gain		16.3	18	-	dB
RLin	input return loss		-	-10	-	dB
η_{D}	drain efficiency		24	28	-	%
ACPR _{885k}	adjacent channel power ratio (885 kHz)		-	-45	-40	dBc

7.1 Ruggedness in class-AB operation

The BLF7G27L-100 and BLF7G27LS-100 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: $V_{DS} = 28 \text{ V}$; $I_{Dq} = 900 \text{ mA}$; $P_L = 100 \text{ W}$ (CW); f = 2500 MHz.

7.2 Single carrier IS-95

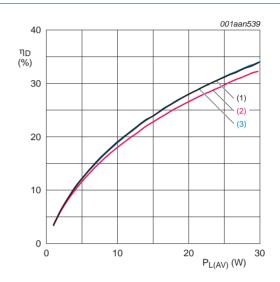
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

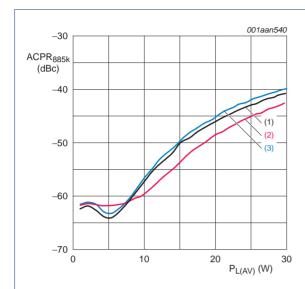
Fig 1. Single carrier IS-95 power gain as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Da} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

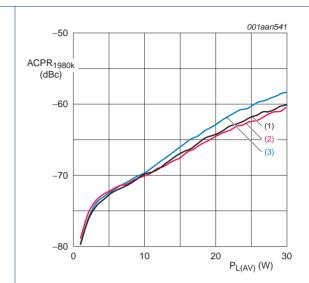
Fig 2. Single carrier IS-95 drain efficiency as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

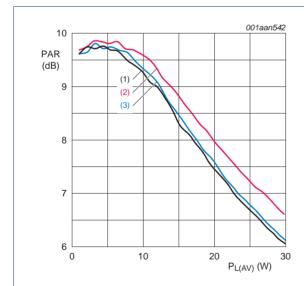
Fig 3. Single carrier IS-95 ACPR at 885 kHz as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

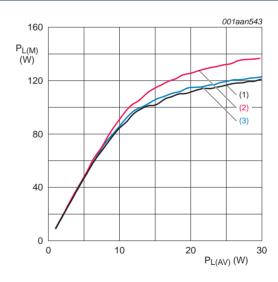
Fig 4. Single carrier IS-95 ACPR at 1980 kHz as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 5. Single carrier IS-95 peak-to-average power ratio as a function of average output power; typical values

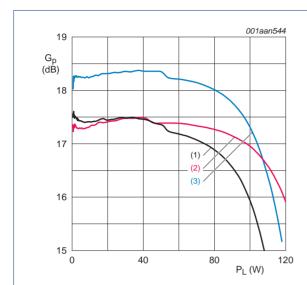


 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 6. Single carrier IS-95 peak output power as a function of average output power; typical values

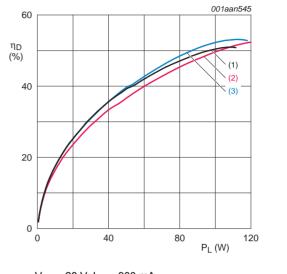
7.3 Pulsed CW



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 7. Pulsed CW power gain as a function of output power; typical values



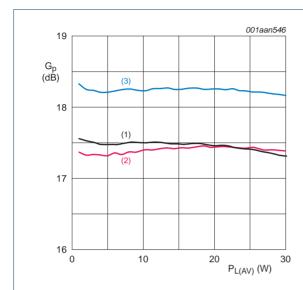
 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 8. Pulsed CW drain efficiency as a function of output power; typical values

7.4 Single carrier W-CDMA

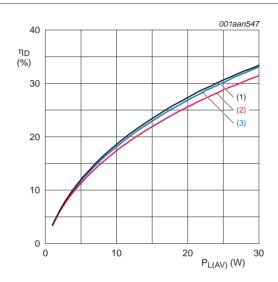
3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

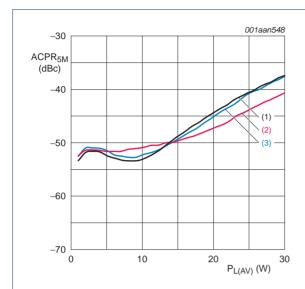
Fig 9. Single carrier W-CDMA power gain as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

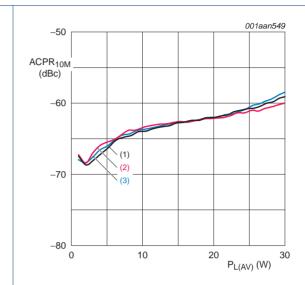
Fig 10. Single carrier W-CDMA drain efficiency as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

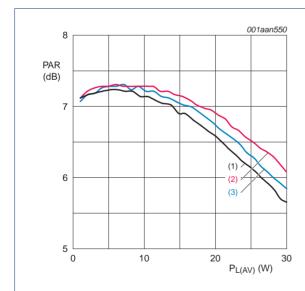
Fig 11. Single carrier W-CDMA ACPR at 5 MHz as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

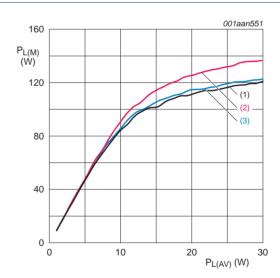
Fig 12. Single carrier W-CDMA ACPR at 10 MHz as a function of average output power; typical values



 V_{DS} = 28 V; I_{Dq} = 900 mA.

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 13. Single carrier W-CDMA peak-to-average power ratio as a function of average output power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$

- (1) f = 2500 MHz
- (2) f = 2600 MHz
- (3) f = 2700 MHz

Fig 14. Single carrier W-CDMA peak output power as a function of average output power; typical values

8. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT502A

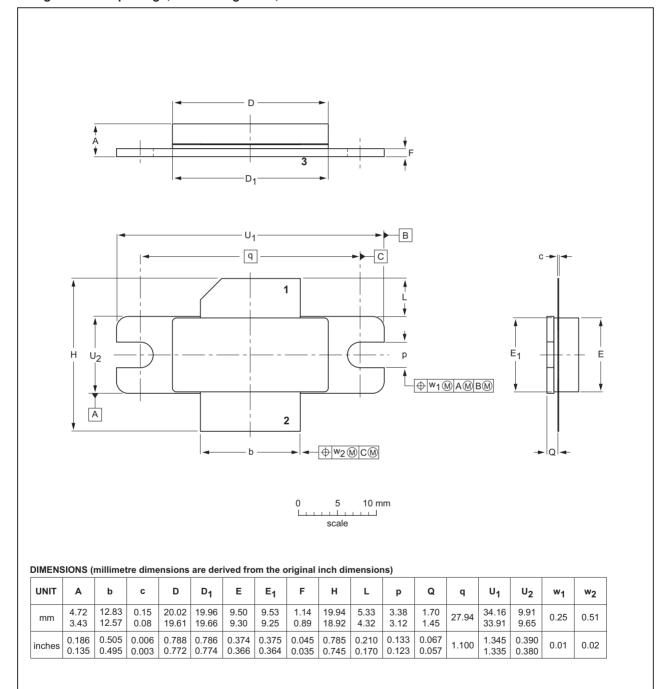


Fig 15. Package outline SOT502A

IEC

OUTLINE

VERSION

SOT502A

JEITA

REFERENCES

JEDEC

ISSUE DATE

-03-01-10

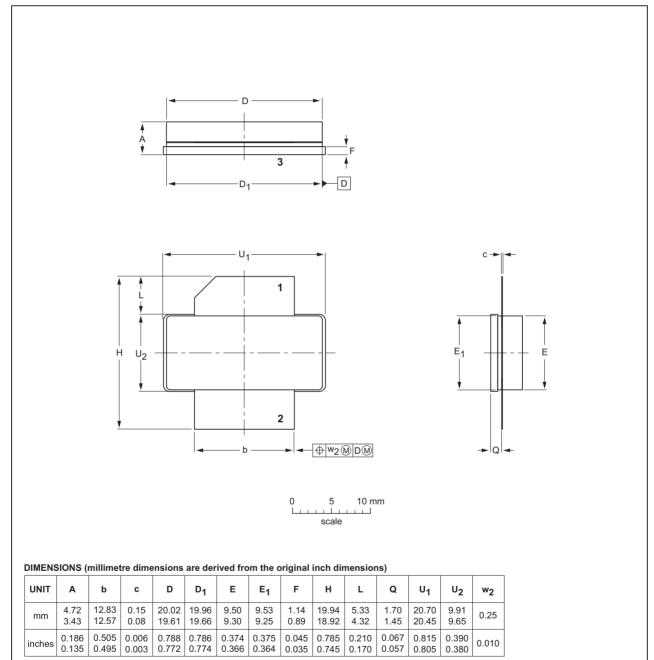
12-05-02

EUROPEAN

PROJECTION

Earless flanged ceramic package; 2 leads

SOT502B



OUTLINE		REFERENCES			EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT502B						07-05-09 12-05-02	

Fig 16. Package outline SOT502B

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 8. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	3A [2]

^[1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V, but fails after exposure to an ESD pulse of 750 V.

10. Abbreviations

Table 9. Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio

^[2] HBM classification 3A is granted to any part that passes after exposure to an ESD pulse of 4000 V, but fails after exposure to an ESD pulse of 8000 V.

11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G27L-100_7G27LS-100 v.5	20161201	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.4
Modifications:	Section 9 on page 11: updated Handling information			
BLF7G27L-100_7G27LS-100 v.4	20150901	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.3
BLF7G27L-100_7G27LS-100 v.3	20110722	Product data sheet	-	BLF7G27L-100_7G27LS-100 v.2
BLF7G27L-100_7G27LS-100 v.2	20110405	Preliminary data sheet	-	BLF7G27L-100_7G27LS-100 v.1
BLF7G27L-100_7G27LS-100 v.1	20100421	Objective data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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Power LDMOS transistor

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