BLF7G24L-140; BLF7G24LS-140 Power LDMOS transistor Rev. 4 – 1 September 2015

AMPLEON Product data sheet

Product profile 1.

1.1 General description

140 W LDMOS power transistor for base station applications at frequencies from 2300 MHz to 2400 MHz.

Typical performance Table 1.

Typical RF performance at $T_{case} = 25 \ ^{\circ}C$ in a common source class-AB production test circuit.

Mode of operation	f	I _{Dq}	V_{DS}	$P_{L(AV)}$	Gp	η_D	ACPR _{885k}	ACPR _{5M}
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2300 to 2400	1300	28	30	18.5	26.5	-45 <mark>[1]</mark>	
1 carrier W-CDMA	2300 to 2400	1300	28	50	18.5	33	-	-35 <mark>[2]</mark>

[1] Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

[2] 3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R_{th} providing excellent thermal stability
- Designed for low memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 2300 MHz to 2400 MHz frequency range

Power LDMOS transistor

2. Pinning information

Pin	Description		Simplified outline	Graphic symbol
BLF7G2	4L-140 (SOT502A)			
1	drain			
2	gate		$ \int \int]]]]]]]]]]]]]]]]] $	1 لـــــا
3	source	<u>[1]</u>		
				sym112
BLF7G24	4LS-140 (SOT502B)			
1	drain			
2	gate			1 لــــا
3	source	<u>[1]</u>		
				2 1 3
				sym

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information						
Packag	je					
Name	Description	Version				
-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A				
-	earless flanged LDMOST ceramic package; 2 leads	SOT502B				
	Packaç Name -	Package Name Description - flanged LDMOST ceramic package; 2 mounting holes; 2 leads				

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	28	А
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C
-					

5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-c)}	thermal resistance from junction to case	T_{case} = 80 °C; P_L = 125 W	0.28	K/W

6. Characteristics

Table 6.	Characteristics
$T_{i} = 25 \ ^{\circ}C$	unless otherwise specified

1 _j =20 C	, uniess otherwise specified.					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V_{GS} = 0 V; I _D = 1 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V_{DS} = 10 V; I _D = 216 mA	1.5	1.8	2.3	V
I _{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	5	μA
I _{DSX}	drain cut-off current	$\label{eq:VGS} \begin{array}{l} V_{\mathrm{GS}} = V_{\mathrm{GS}(\mathrm{th})} + 3.75 \ V; \\ V_{\mathrm{DS}} = 10 \ V \end{array}$	34	42	-	A
I _{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	500	nA
g _{fs}	forward transconductance	V_{DS} = 10 V; I_D = 216 mA	-	1.87	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ I _D = 7.56 A	-	69	-	mΩ

7. Test information

Remark: All testing performed in a class-AB production test circuit.

Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz; $f_1 = 2300$ MHz; $f_2 = 2400$ MHz; RF performance at $V_{DS} = 28$ V; $I_{Dq} = 1300$ mA; $T_{case} = 25$ °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
$P_{L(AV)}$	average output power		-	30	-	W
G _p	power gain		17.5	18.5	-	dB
RL _{in}	input return loss		-	-12	-	dB
η_D	drain efficiency		23	26.5	-	%
ACPR _{885k}	adjacent channel power ratio (885 kHz)		-	-45	-40	dBc

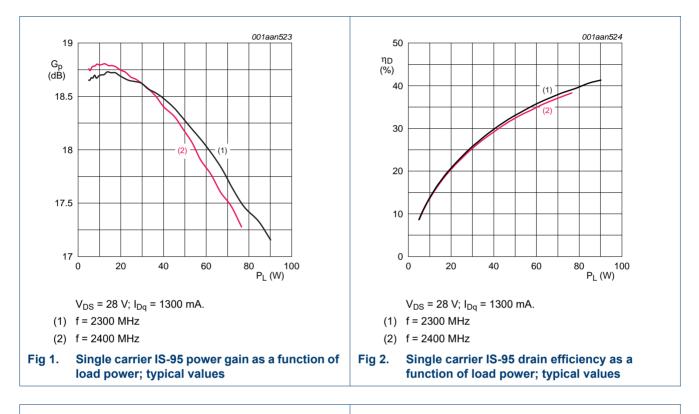
7.1 Ruggedness in class-AB operation

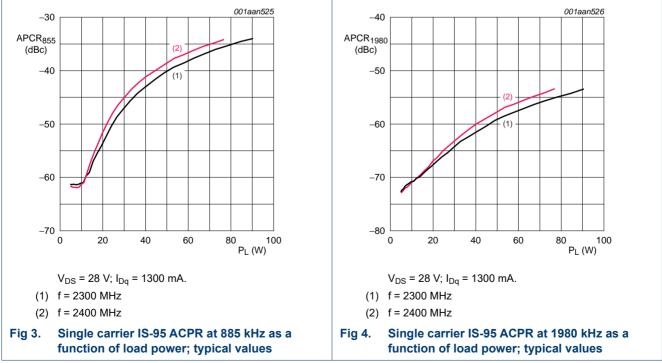
The BLF7G24L-140 and BLF7G24LS-140 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: $V_{DS} = 28 \text{ V}; I_{Dq} = 1300 \text{ mA}; P_L = 140 \text{ W} (CW); f = 2300 \text{ MHz}.$

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7.2 Single carrier IS-95

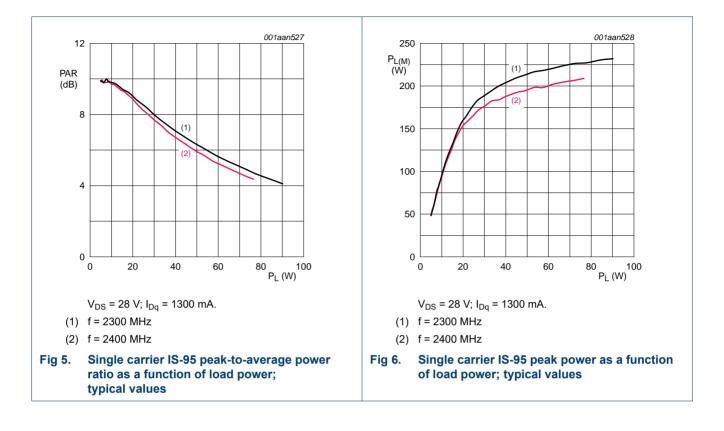
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz





BLF7G24L-140; BLF7G24LS-140

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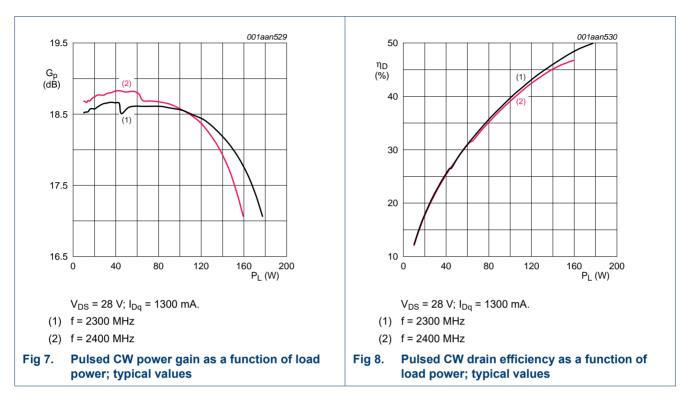


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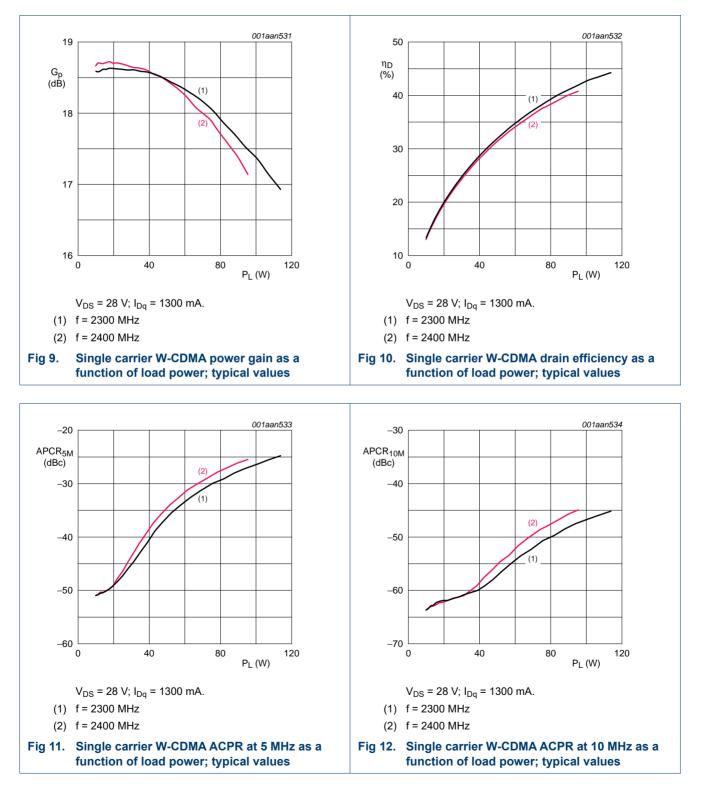


7.3 Pulsed CW

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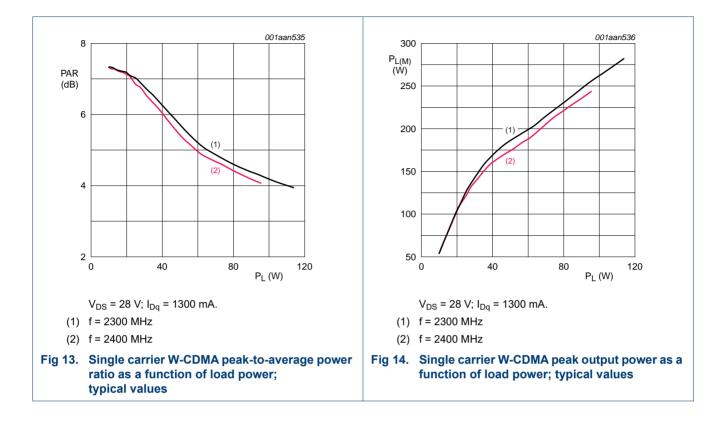
7.4 Single carrier W-CDMA

3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



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8. Package outline

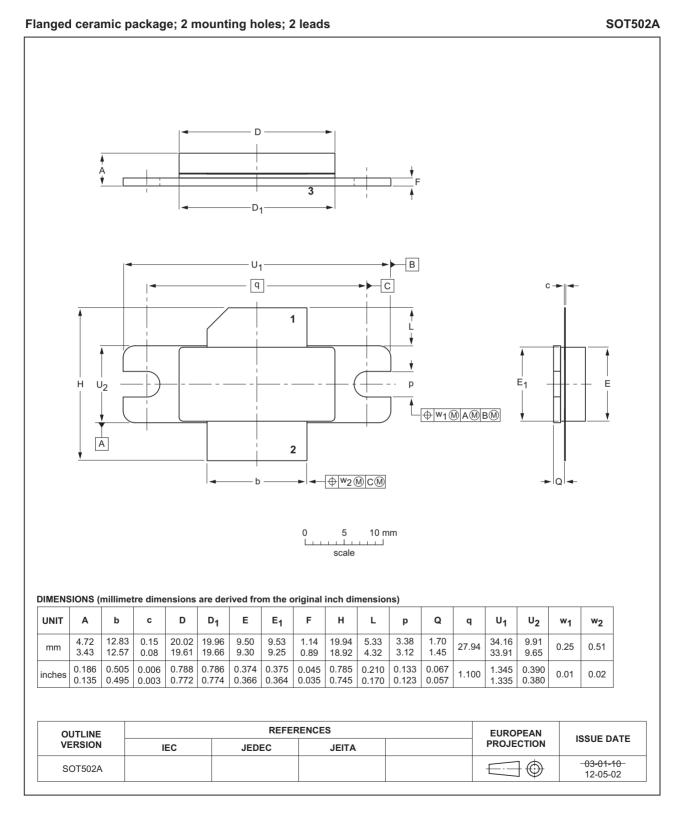


Fig 15. Package outline SOT502A

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SOT502B

Earless flanged ceramic package; 2 leads

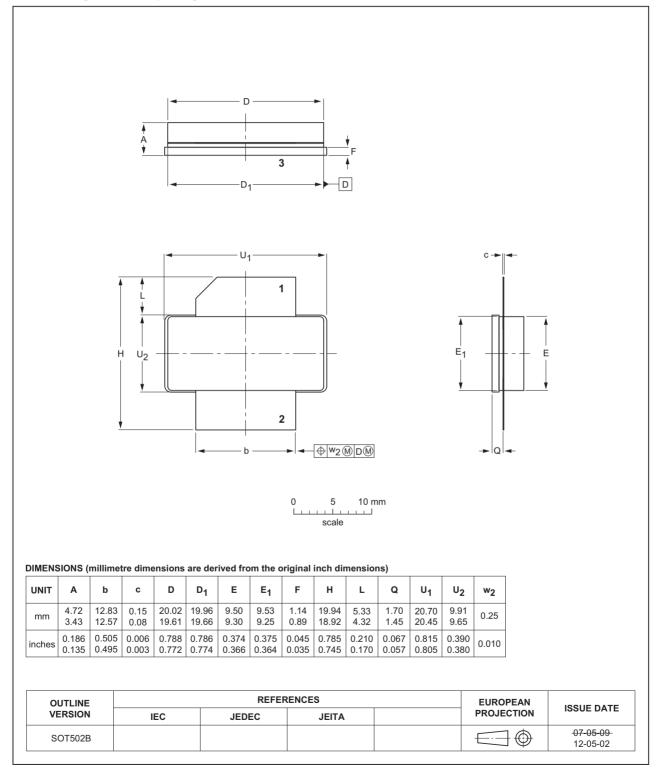


Fig 16. Package outline SOT502B

9. Abbreviations

Table 8.	Abbreviations
Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

10. Revision history

Table 9.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G24L-140_7G24LS-140#4	20150901	Product data sheet	-	BLF7G24L-140_7G24LS- 140 v.3
Modifications:	• The format of this document has been redesigned to comply with the new guidelines of Ampleon.			
	 Legal texts 	have been adapted to the	e new company na	ame where appropriate.
BLF7G24L-140_7G24LS-140 v.3	20110801	Product data sheet	-	BLF7G24L-140_7G24LS- 140 v.2
BLF7G24L-140_7G24LS-140 v.2	20110405	Preliminary data sheet	-	BLF7G24L-140_7G24LS- 140 v.1
BLF7G24L-140_7G24LS-140 v.1	20100805	Objective data sheet	-	-

11. Legal information

11.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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