# BLF7G24L-100; BLF7G24LS-100 Power LDMOS transistor Rev. 5 — 1 September 2015

**AMPLEON** 

Product data sheet

## **Product profile**

#### 1.1 General description

100 W LDMOS power transistor for base station applications at frequencies from 2300 MHz to 2400 MHz.

**Typical performance** Table 1.

Typical RF performance at  $T_{case} = 25$  °C in a common source class-AB production test circuit.

Mode of operation	f	I <sub>Dq</sub>	V <sub>DS</sub>	P <sub>L(AV)</sub>	Gp	$\eta_D$	ACPR <sub>885k</sub>	ACPR <sub>5M</sub>
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
IS-95	2300 to 2400	900	28	20	18	27	-46 <sup>[1]</sup>	-
1 carrier W-CDMA	2300 to 2400	900	28	30	18.7	33	-	-40 <mark>[2]</mark>

<sup>[1]</sup> Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.

#### 1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low R<sub>th</sub> providing excellent thermal stability
- Designed for low memory effects providing excellent digital pre-distortion capability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Restriction of Hazardous Substances (RoHS) Directive 2002/95/EC

#### 1.3 Applications

RF power amplifiers for base stations and multi carrier applications in the 2300 MHz to 2400 MHz frequency range

<sup>[2] 3</sup>GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.

## 2. Pinning information

Table 2. Pinning

Table 2.	- Illining			
Pin	Description		Simplified outline	Graphic symbol
BLF7G24	4L-100 (SOT502A)			
1	drain			,
2	gate		$\begin{array}{c c} \hline  & 1 \\ \hline  & 5 \\ \hline  & 3 \end{array}$	1   <u> </u>
3	source	<u>[1]</u>		2
				3 sym112
DI 5700	41 C 400 (COTFOOD)			3911112
BLF/G24	4LS-100 (SOT502B)			
1	drain			4
2	gate			, L
3	source	<u>[1]</u>		2
				3
				sym112

<sup>[1]</sup> Connected to flange.

## 3. Ordering information

Table 3. Ordering information

Type number	Packag	Package								
	Name	Description	Version							
BLF7G24L-100	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A							
BLF7G24LS-100	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B							

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	М	in	Max	Unit
$V_{DS}$	drain-source voltage		-		65	V
$V_{GS}$	gate-source voltage		-(	0.5	+13	V
I <sub>D</sub>	drain current		-		28	Α
T <sub>stg</sub>	storage temperature		-6	35	+150	°C
Tj	junction temperature		-		200	°C

## 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	$T_{case}$ = 80 °C; $P_{L}$ = 100 W	0.3	K/W

BLF7G24L-100\_7G24LS-100#5

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## 6. Characteristics

Table 6. Characteristics

 $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS}$ = 0 V; $I_D$ = 1 mA	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$V_{DS}$ = 10 V; $I_{D}$ = 150 mA	1.5	1.8	2.3	V
I <sub>DSS</sub>	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	5	μΑ
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	25.1	29	-	Α
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-	-	500	nA
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 5.35 A	-	10.5	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 5.25 \text{ A}$	-	0.1	-	Ω

### 7. Test information

Remark: All testing performed in a class-AB production test circuit.

#### Table 7. Functional test information

Mode of operation: 1-carrier N-CDMA, single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF, channel bandwidth is 1.2288 MHz;  $f_1$  = 2300 MHz;  $f_2$  = 2400 MHz; RF performance at  $V_{DS}$  = 28 V;  $I_{Dq}$  = 900 mA;  $T_{Case}$  = 25 °C; unless otherwise specified.

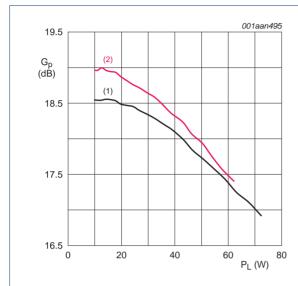
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	20	-	W
Gp	power gain		17.3	18	-	dB
RLin	input return loss		-	-14	-	dB
$\eta_{D}$	drain efficiency		22	27	-	%
ACPR <sub>885k</sub>	adjacent channel power ratio (885 kHz)		-	-46	-40	dBc

### 7.1 Ruggedness in class-AB operation

The BLF7G24L-100 and BLF7G24LS-100 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS} = 28 \text{ V}$ ;  $I_{Dq} = 900 \text{ mA}$ ;  $P_L = 100 \text{ W}$  (CW); f = 2300 MHz.

#### 7.2 Single carrier IS-95

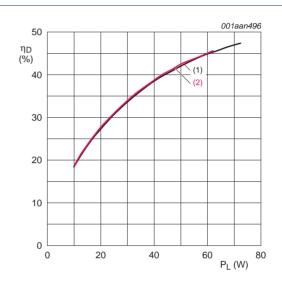
Single carrier IS-95 with pilot, paging, sync and 6 traffic channels (Walsh codes 8 - 13). PAR = 9.7 dB at 0.01 % probability on the CCDF. Channel bandwidth is 1.2288 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

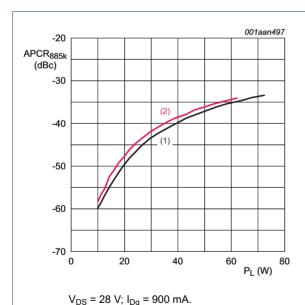
Fig 1. Single carrier IS-95 power gain as a function of load power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

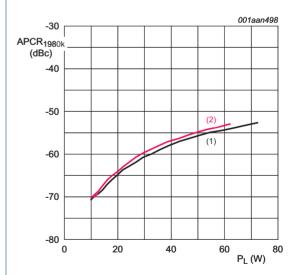
Fig 2. Single carrier IS-95 drain efficiency as a function of load power; typical values



(1) f = 2300 MHz

(2) f = 2400 MHz

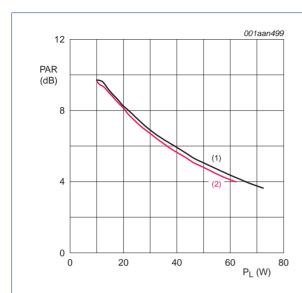
Fig 3. Single carrier IS-95 ACPR at 885 kHz as a function of load power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

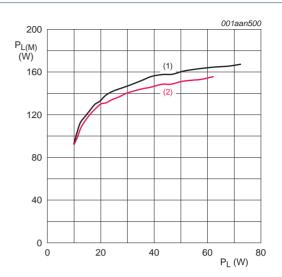
Fig 4. Single carrier IS-95 ACPR at 1980 kHz as a function of load power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 5. Single carrier IS-95 peak-to-average power ratio as a function of load power; typical values

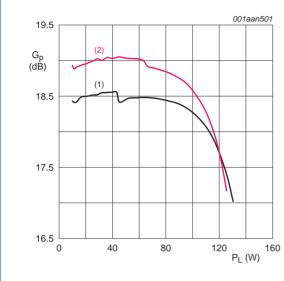


 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 6. Single carrier IS-95 peak power as a function of load power; typical values

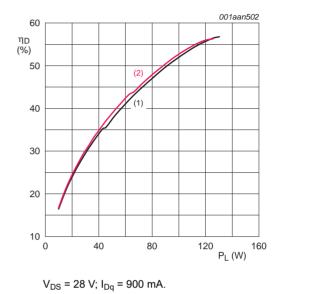
#### 7.3 Pulsed CW



 $V_{DS}$  = 28 V;  $I_{Dq}$  = 900 mA.

- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 7. Pulsed CW power gain as a function of load power; typical values

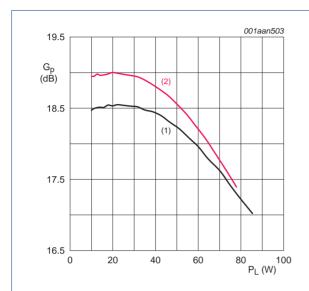


- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 8. Pulsed CW drain efficiency as a function of load power; typical values

## 7.4 Single carrier W-CDMA

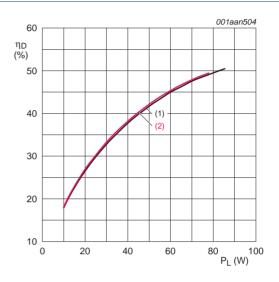
3GPP; test model 1; 64 DPCH; PAR = 7.2 dB at 0.01 % probability on CCDF. Channel bandwidth is 3.84 MHz.



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

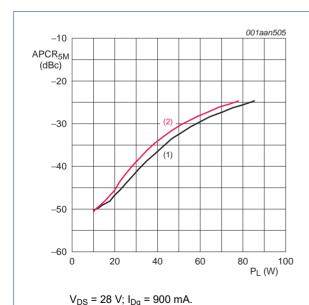
Fig 9. Single carrier W-CDMA power gain as a function of load power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

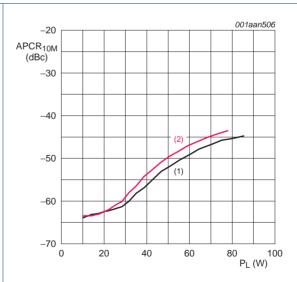
Fig 10. Single carrier W-CDMA drain efficiency as a function of load power; typical values



(1) f = 2300 MHz

(2) f = 2400 MHz

Fig 11. Single carrier W-CDMA ACPR at 5 MHz as a function of load power; typical values



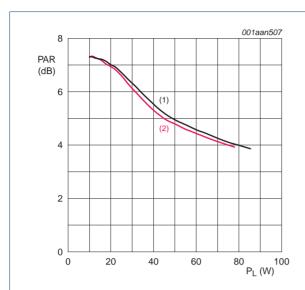
 $V_{DS} = 28 \text{ V}; I_{Da} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 12. Single carrier W-CDMA ACPR at 10 MHz as a function of load power; typical values

# BLF7G24L-100; BLF7G24LS-100

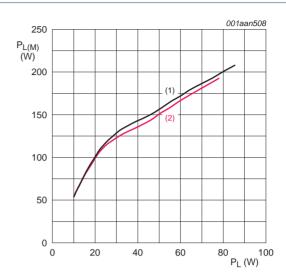
**Power LDMOS transistor** 



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 13. Single carrier W-CDMA peak-to-average power ratio as a function of load power; typical values



 $V_{DS} = 28 \text{ V}; I_{Dq} = 900 \text{ mA}.$ 

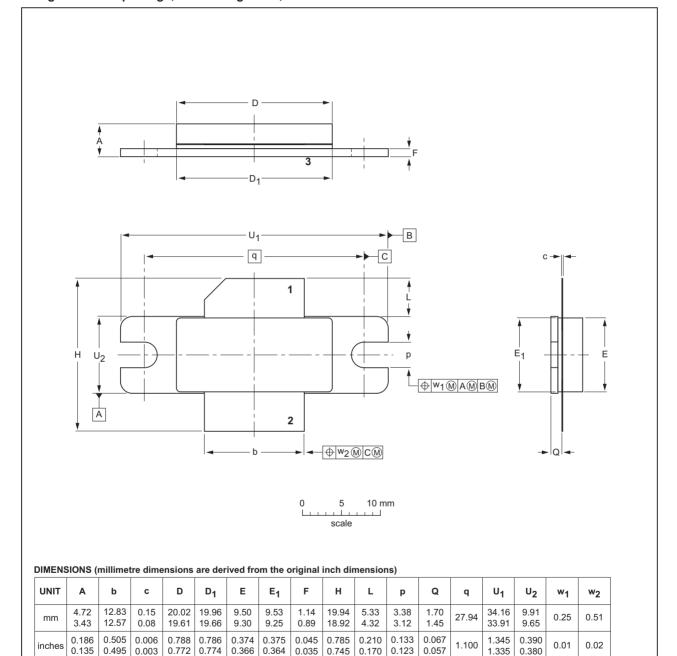
- (1) f = 2300 MHz
- (2) f = 2400 MHz

Fig 14. Single carrier W-CDMA peak output power as a function of load power; typical values

## 8. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT502A



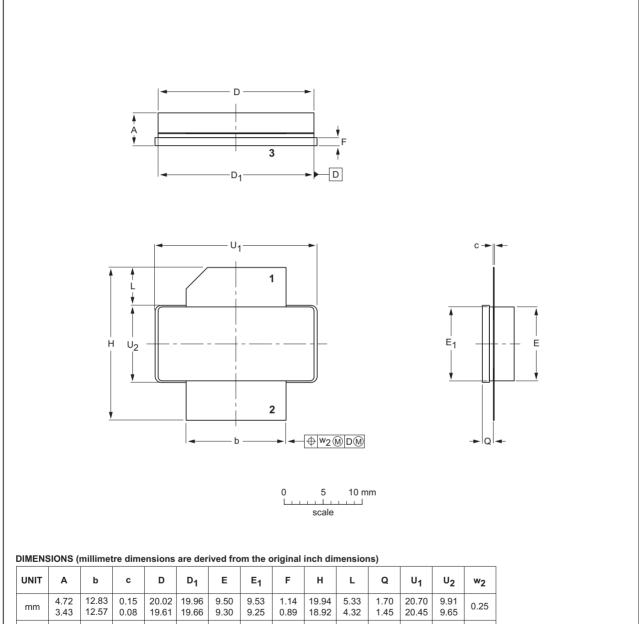
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VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT502A					<del>-03-01-10-</del> 12-05-02

Fig 15. Package outline SOT502A

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#### Earless flanged ceramic package; 2 leads

SOT502B



	_								'					
1 0.25	9.91	20.70	1.70	5.33	19.94	1.14	9.53	9.50	19.96	20.02	0.15	12.83	4.72	
5   0.25	9.65	20.45	1.45	4.32	18.92	0.89	9.25	9.30	19.66	19.61	0.08	12.57	3.43	mm
0 0.010	0.390	0.815	0.067	0.210	0.785	0.045	0.375	0.374	0.786	0.788	0.006	0.505	0.186	inahaa
0.010	0.380	0.805	0.057	0.170	0.745	0.035	0.364	0.366	0.774	0.772	0.003	0.495	0.135	inches
9	0.3	0.815	0.067	0.210	0.785	0.045	0.375	0.374	0.786	0.788	0.006	0.505	0.186	inches

	REFER	ENCES		EUROPEAN	ISSUE DATE
IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
					<del>07-05-09</del> 12-05-02
	IEC		REFERENCES  IEC JEDEC JEITA		IEC JEDEC JEITA PROJECTION

Fig 16. Package outline SOT502B

## 9. Abbreviations

Table 8. Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
IS-95	Interim Standard 95
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
N-CDMA	Narrowband Code Division Multiple Access
PAR	Peak-to-Average power Ratio
RF	Radio Frequency
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

## 10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
BLF7G24L-100_7G24LS-100 v.5	20150901	Product data sheet	-	BLF7G24L-100_7G24LS-100 v.4				
Modifications:	guidelines	ne format of this document has been redesigned to comply with the new identity idelines of Ampleon.  egal texts have been adapted to the new company name where appropriate.						
BLF7G24L-100_7G24LS-100 v.4		Product data sheet	-	BLF7G24L-100_7G24LS-100 v.3				
BLF7G24L-100_7G24LS-100 v.3	20110405	Preliminary data sheet	-	BLF7G24L-100_7G24LS-100 v.2				
BLF7G24L-100_7G24LS-100 v.2	20100714	Objective data sheet	-	BLF7G24L-100_7G24LS-100 v.1				
BLF7G24L-100_7G24LS-100 v.1	20100414	Objective data sheet	-	-				

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Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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