

BFU580G NPN wideband silicon RF transistor Rev. 1 — 28 April 2014

Product data sheet

1. **Product profile**

1.1 General description

NPN silicon microwave transistor for high speed, medium power applications in a plastic, 4-pin SOT223 package.

The BFU580G is part of the BFU5 family of transistors, suitable for small signal to medium power applications up to 2 GHz.

1.2 Features and benefits

- Low noise, high linearity, high breakdown RF transistor
- AEC-Q101 qualified
- Minimum noise figure (NF_{min}) = 0.75 dB at 900 MHz
- Maximum stable gain 15.5 dB at 900 MHz
- 11 GHz f_T silicon technology

1.3 Applications

- Applications requiring high supply voltages and high breakdown voltages
- Broadband amplifiers up to 2 GHz
- Low noise, high linearity amplifiers for ISM applications
- Automotive applications (e.g., antenna amplifiers)

1.4 Quick reference data

Table 1. **Quick reference data**

$T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter		-	-	24	V
V _{CE}	collector-emitter voltage	open base		-	-	12	V
		shorted base		-	-	24	V
V _{EB}	emitter-base voltage	open collector		-	-	2	V
I _C	collector current			-	30	60	mA
P _{tot}	total power dissipation	$T_{sp} \le 120 \ ^{\circ}C$	<u>[1]</u>	-	-	1000	mW
h _{FE}	DC current gain	I _C = 30 mA; V _{CE} = 8 V		60	95	130	
C _c	collector capacitance	V _{CB} = 8 V; f = 1 MHz		-	1.1	-	pF
f _T	transition frequency	I _C = 30 mA; V _{CE} = 8 V; f = 900 MHz		-	11	-	GHz



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$T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
G _{p(max)}	maximum power gain	I _C = 30 mA; V _{CE} = 8 V; f = 900 MHz	1 -	15.5	-	dB	
NF _{min}	minimum noise figure	I_{C} = 5 mA; V_{CE} = 8 V; f = 900 MHz; $\Gamma_{S} = \Gamma_{opt}$	-	0.75	-	dB	
P _{L(1dB)}	output power at 1 dB gain compression	I _C = 30 mA; V _{CE} = 8 V; Z _S = Z _L = 50 Ω ; f = 900 MHz	-	13	-	dBm	

Table 1. Quick reference data ...continued

[1] T_{sp} is the temperature at the solder point of the collector lead.

[2] If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	base		4
3	emitter		2
4	collector		1, 3
			mbb159

3. Ordering information

Table 3. Ordering information

Type number	Package	3	
	Name	Description	Version
BFU580G	-	plastic surface-mounted package with increased heatsink; 4 leads	SOT223
OM7966	-	Customer evaluation kit for BFU580G and BFU590G [1]	-

[1] The customer evaluation kit contains the following:

- a) Unpopulated RF amplifier Printed-Circuit Board (PCB)
- b) Unpopulated RF amplifier Printed-Circuit Board (PCB) with emitter degeneration
- c) Four SMA connectors for fitting unpopulated Printed-Circuit Board (PCB)
- d) BFU580G and BFU590G samples
- e) USB stick with data sheets, application notes, models, S-parameter and noise files

4. Marking

Table 4.	Marking

Type number	Marking
BFU580G	BFU580

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5. Design support

Table 5. Available design support

Download from the BFU580G product information page on http://www.nxp.com.

Support item	Available	Remarks
Device models for Agilent EEsof EDA ADS	yes	Based on Mextram device model.
SPICE model	yes	Based on Gummel-Poon device model.
S-parameters	yes	
Noise parameters	yes	
Customer evaluation kit	yes	See Section 3 and Section 10.
Solder pattern	yes	
Application notes	yes	See Section 10.1

6. Limiting values

Table 6.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	30	V
V _{CE}	collector-emitter voltage	open base	-		V
V _{EB} e		shorted base	-	30	V
V _{EB}	emitter-base voltage	open collector	-	3	V
I _C	collector current		-	100	mA
T _{stg}	storage temperature		-65	+150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to JEDEC standard 22-A114E	-	±150	V
		Charged Device Model (CDM) According to JEDEC standard 22-C101B	-	±2	kV

7. Recommended operating conditions

Table 7. Characteristics						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	-	24	V
V _{CE}	collector-emitter voltage	open base	-	-	12	V
		shorted base	-	-	24	V
V _{EB}	emitter-base voltage	open collector	-	-	2	V
l _C	collector current		-	-	60	mA
Pi	input power	Z _S = 50 Ω	-	-	10	dBm
Tj	junction temperature		-40	-	+150	°C
P _{tot}	total power dissipation	$T_{sp} \le 120 \ ^{\circ}C$	<u>[1]</u> _	-	1000	mW

[1] T_{sp} is the temperature at the solder point of the collector lead.

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8. Thermal characteristics

Table 8.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point	[1]	30	K/W

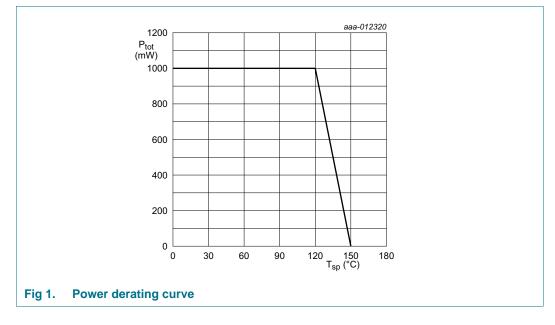
[1] T_{sp} is the temperature at the solder point of the collector lead.

 T_{sp} has the following relation to the ambient temperature T_{amb} :

 $T_{sp} = T_{amb} + P \times R_{th(sp-a)}$

With P being the power dissipation and $R_{th(sp-a)}$ being the thermal resistance between the solder point and ambient. $R_{th(sp-a)}$ is determined by the heat transfer properties in the application.

The heat transfer properties are set by the application board materials, the board layout and the environment e.g. housing.



9. Characteristics

Table 9. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_{\rm C} = 100 \text{ nA}; I_{\rm E} = 0 \text{ mA}$	24	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 150 nA; I _B = 0 mA	12	-	-	V
I _C	collector current		-	30	60	mA
I _{CBO}	collector-base cut-off current	I _E = 0 mA; V _{CB} = 8 V	-	<1	-	nA
h _{FE}	DC current gain	I _C = 30 mA; V _{CE} = 8 V	60	95	130	
C _e	emitter capacitance	V _{EB} = 0.5 V; f = 1 MHz	-	1.5	-	pF
C _{re}	feedback capacitance	V _{CE} = 8 V; f = 1 MHz	-	0.54	-	pF
C _c	collector capacitance	V _{CB} = 8 V; f = 1 MHz	-	1.1	-	pF
f _T	transition frequency	I _C = 30 mA; V _{CE} = 8 V; f = 900 MHz	-	11	-	GHz

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Table 9. Characteristics ...continued

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _{p(max)}	maximum power gain	f = 433 MHz; V _{CE} = 8 V	[1]			
		$I_{\rm C} = 5 \rm{mA}$	-	20	- -	dB
		I _C = 20 mA	-	22		dB
		I _C = 30 mA	-	22.5	-	dB
		f = 900 MHz; V _{CE} = 8 V	[1]			
		$I_{\rm C} = 5 \rm{mA}$	-	16	-	dB
		I _C = 20 mA	-	15.5	-	dB
		I _C = 30 mA	-	15.5	-	dB
		f = 1800 MHz; V _{CE} = 8 V	[1]			
		$I_{\rm C} = 5 \rm{mA}$	-	9.5	-	dB
		I _C = 20 mA	-	10	-	dB
		I _C = 30 mA	-	10.5	-	dB
s ₂₁ ²	insertion power gain	f = 433 MHz; V _{CE} = 8 V				
		$I_{\rm C} = 5 \rm{mA}$	-	18	-	dB
		I _C = 20 mA	-	20	-	dB
		I _C = 30 mA	-	20.5	-	dB
		f = 900 MHz; V _{CE} = 8 V				
		$I_{\rm C} = 5 \rm{mA}$	-	12.5	-	dB
		I _C = 20 mA	-	14	-	dB
		I _C = 30 mA	-	14.5	-	dB
		f = 1800 MHz; V _{CE} = 8 V				
		$I_{\rm C} = 5 \rm{mA}$	-	7.5	-	dB
		I _C = 20 mA	-	8.5	-	dB
		I _C = 30 mA	-	8.5	-	dB
NF _{min}	minimum noise figure	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		$I_{\rm C} = 5 \rm{mA}$	-	0.65	-	dB
		I _C = 20 mA	-	1.05	-	dB
		I _C = 30 mA	-	1.25	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		$I_{\rm C} = 5 \rm{mA}$	-	0.75	-	dB
		I _C = 20 mA	-	1.1	-	dB
		I _C = 30 mA	-	1.3	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 5 mA	-	0.9	-	dB
		I _C = 20 mA	-	1.2	-	dB
		I _C = 30 mA	-	1.4	-	dB

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Table 9. Characteristics ...continued

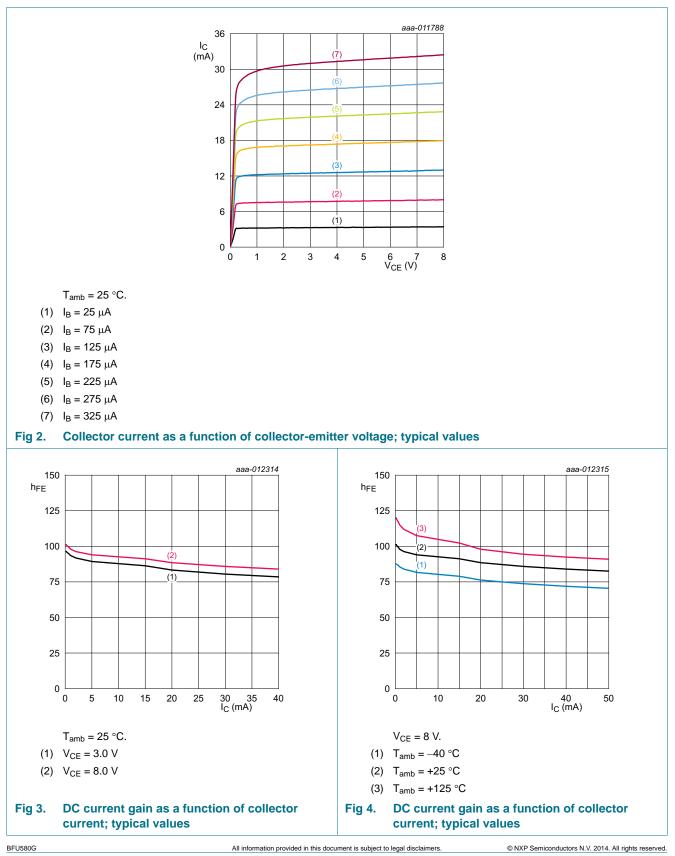
 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _{ass}	associated gain	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 5 mA	-	19.5	-	dB
		I _C = 20 mA	-	20.5	-	dB
		I _C = 30 mA	-	20.5	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 5 mA	-	13.5	-	dB
		I _C = 20 mA	-	14.5	-	dB
		I _C = 30 mA	-	14.5	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 5 mA	-	8.5	-	dB
		I _C = 20 mA	-	9	-	dB
		I _C = 30 mA	-	9	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	f = 433 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 20 mA	-	12.5	-	dBm
		I _C = 30 mA	-	15.5	-	dBm
		f = 900 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 20 mA	-	12.5	-	dBm
		I _C = 30 mA	-	14.5	-	dBm
		f = 1800 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω			- a	
		I _C = 20 mA	-	12	-	dBm
		I _C = 30 mA	-	14	-	dBm
IP3 _o	output third-order intercept point	$ f_1 = 433 \text{ MHz}; \ f_2 = 434 \text{ MHz}; \ V_{CE} = 8 \text{ V}; \\ Z_S = Z_L = 50 \ \Omega $				-
		I _C = 20 mA	-	22	-	dBm
		I _C = 30 mA	-	25	-	dBm
		$ f_1 = 900 \text{ MHz}; \ f_2 = 901 \text{ MHz}; \ V_{CE} = 8 \text{ V}; \\ Z_S = Z_L = 50 \ \Omega $				-
		I _C = 20 mA	-	22	-	dBm
		I _C = 30 mA	-	24	-	dBm
		f_1 = 1800 MHz; f_2 = 1801 MHz; V _{CE} = 8 V; Z _S = Z _L = 50 Ω				
		I _C = 20 mA	-	21.5	-	dBm
		I _C = 30 mA	-	23.5	-	dBm

 $\label{eq:general} \mbox{[1]} \quad \mbox{If } K > 1 \mbox{ then } G_{p(max)} \mbox{ is the maximum power gain. If } K < 1 \mbox{ then } G_{p(max)} = MSG.$

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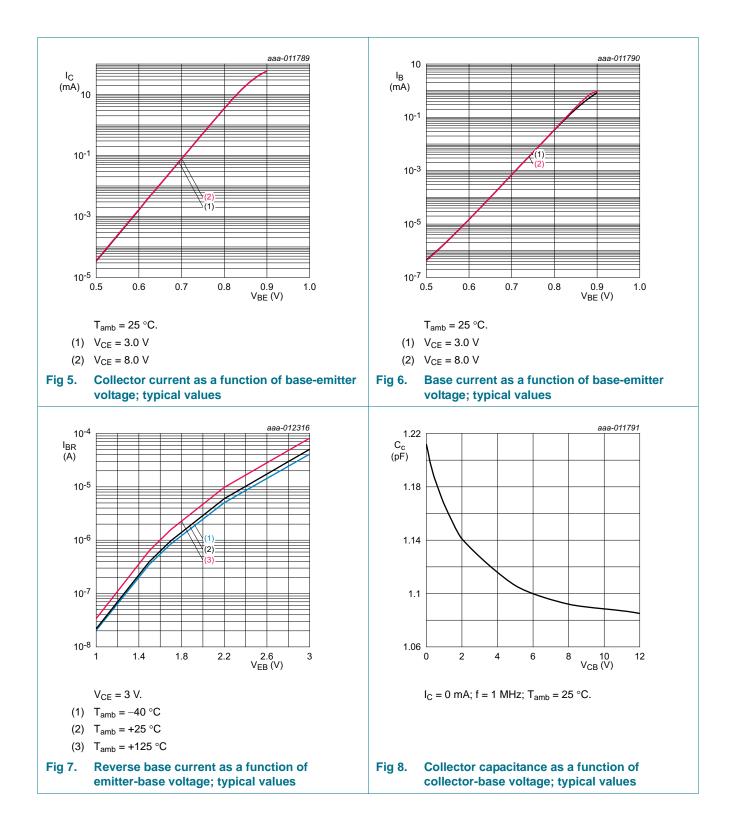




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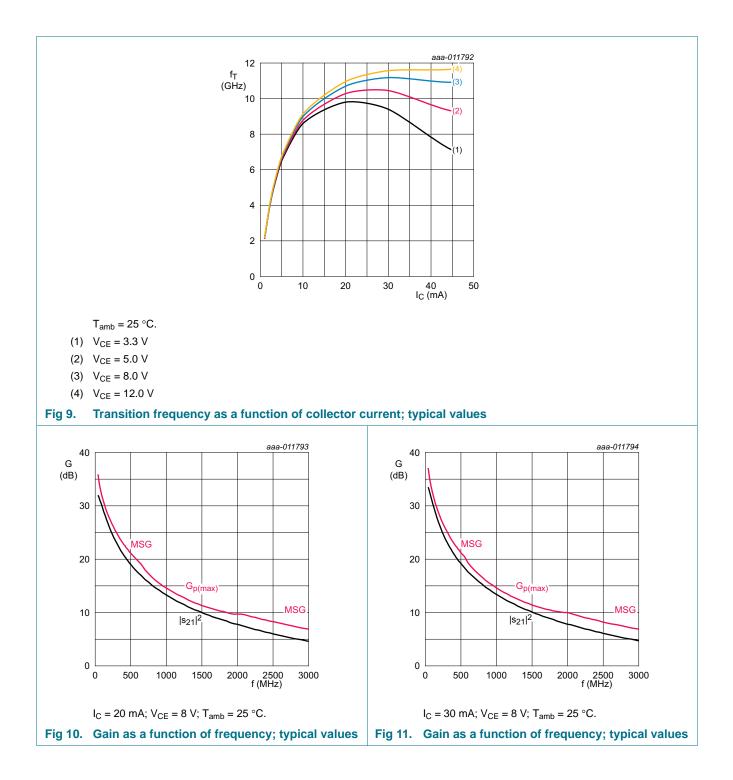
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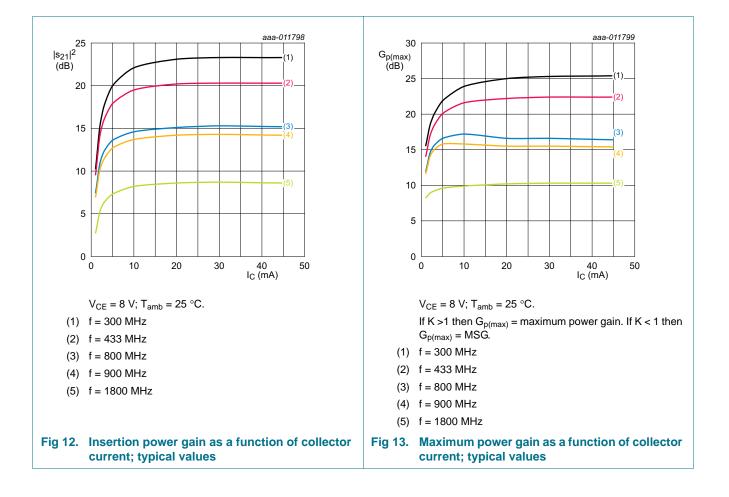
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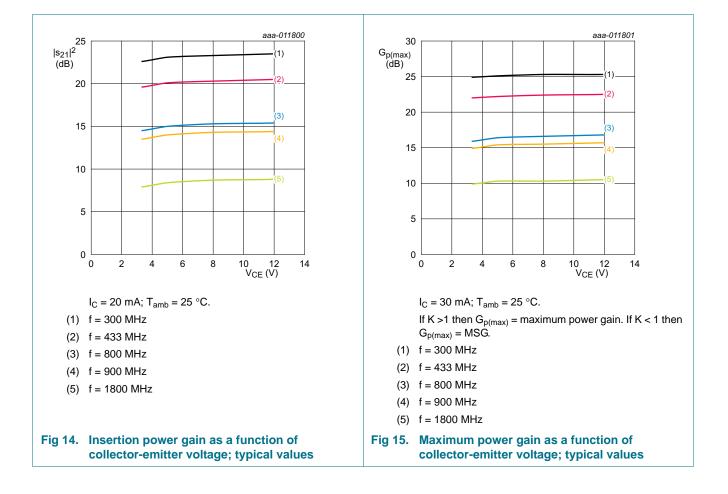


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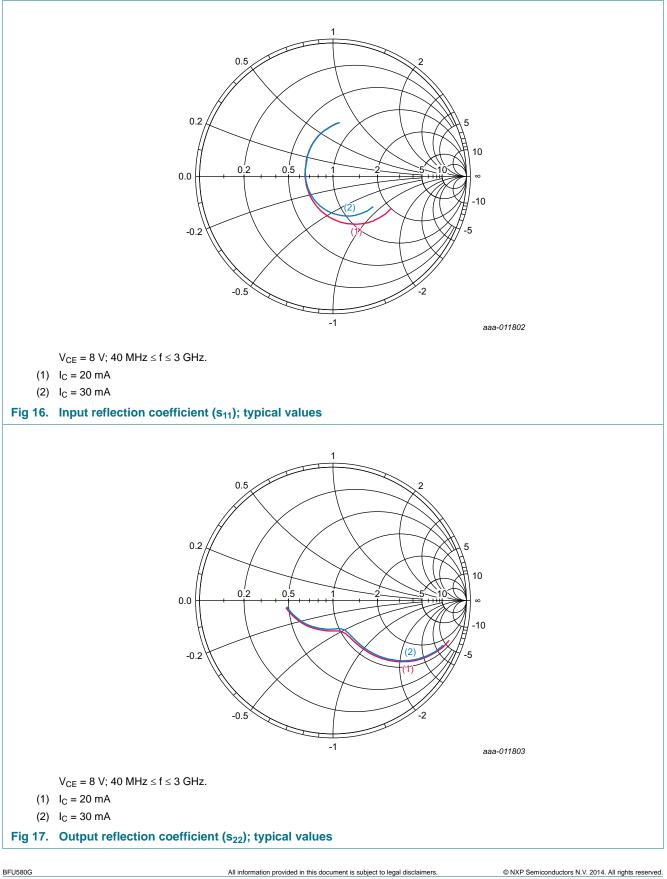
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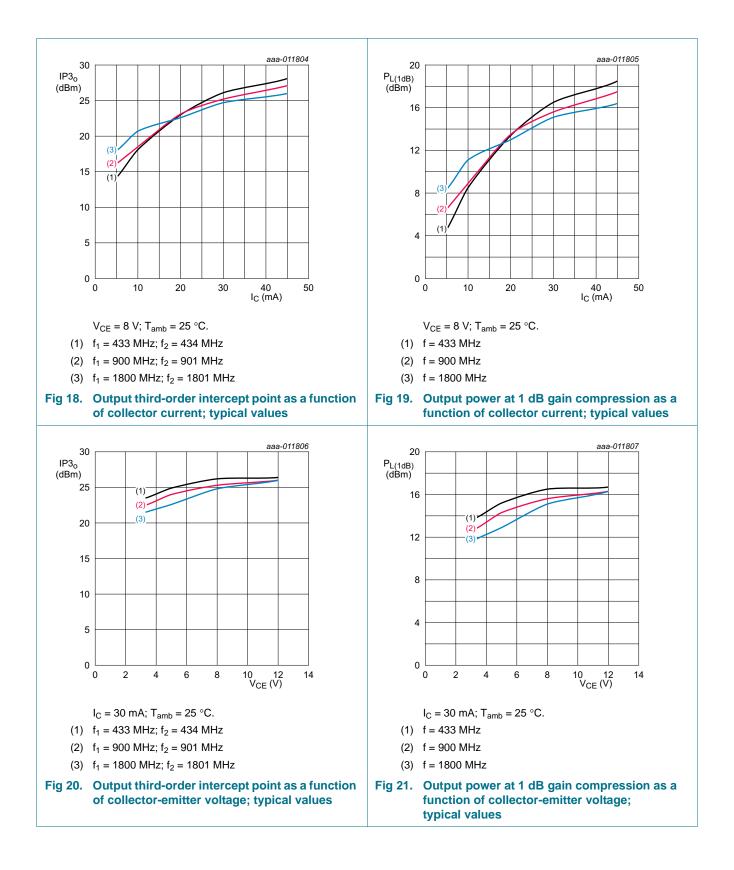
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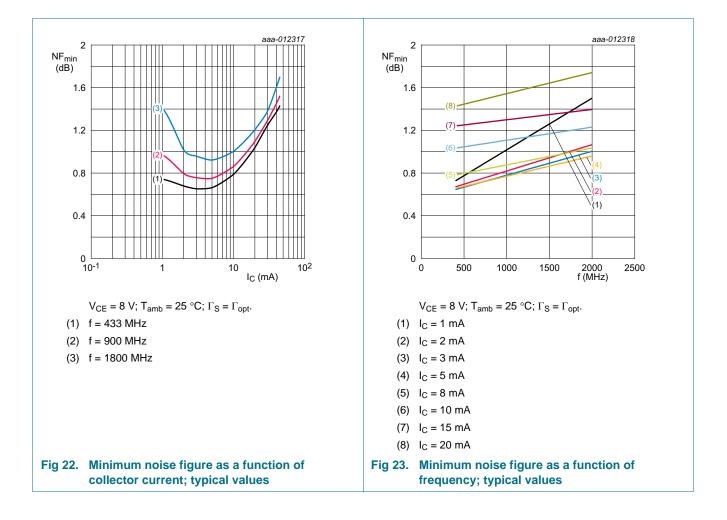


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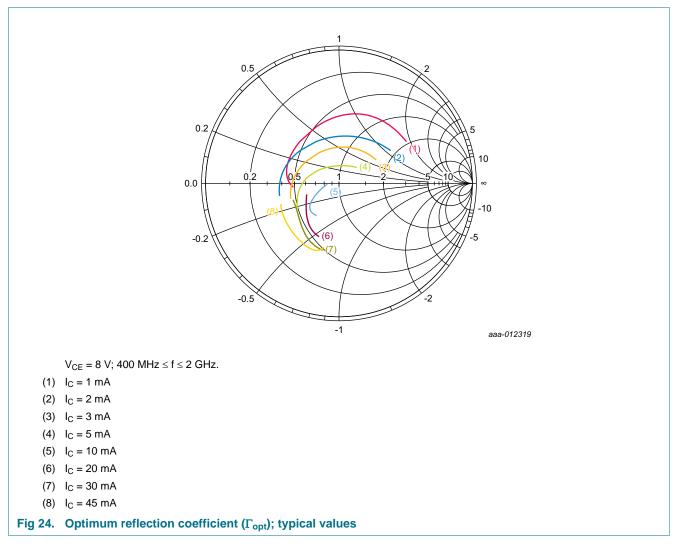
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10. Application information

More information about the following application example can be found in the application notes. See <u>Section 5 "Design support</u>".

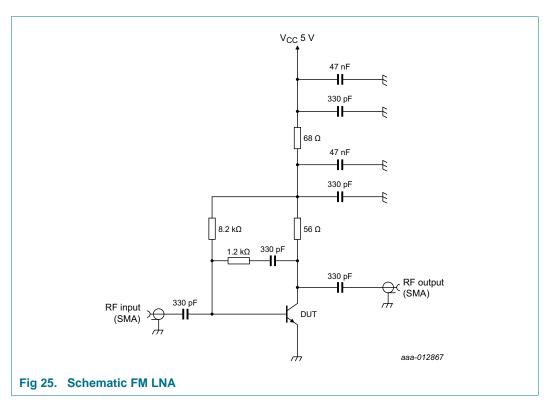
The following application example can be implemented using the evaluation kit. See Section 3 "Ordering information" for the order type number.

The following application example can be simulated using the simulation package. See <u>Section 5 "Design support</u>".

10.1 Application example: FM LNA

FM LNA, optimized for low noise.

More detailed information of the application example can be found in the application note: *AN11500.*



Remark: fine tuning of components maybe required depending on PCB parasitics.

Table 10. Application performance data at 98 MHz

$I_{\rm CC} = 25 r$	nA; $V_{CC} = 5 V$					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$ s_{21} ^2$	insertion power gain		-	22	-	dB
NF	noise figure		-	1.6	-	dB
IP3 _o	output third-order intercept point	f = 88 MHz to 108 MHz; carrier spacing = 100 kHz	-	15	-	dBm

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11. Package outline

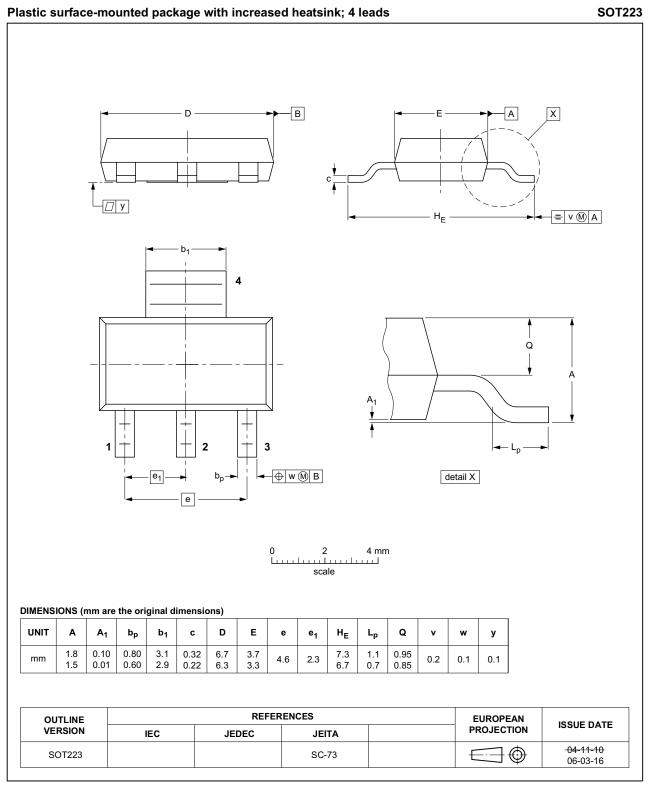


Fig 26. Package outline SOT223

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12. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

13. Abbreviations

Acronym	Description
AEC	Automotive Electronics Council
FM	Frequency Modulation
ISM	Industrial, Scientific and Medical
LNA	Low-Noise Amplifier
MSG	Maximum Stable Gain
NPN	Negative-Positive-Negative
SMA	SubMiniature version A

14. Revision history

Table 12.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU580G v.1	20140428	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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