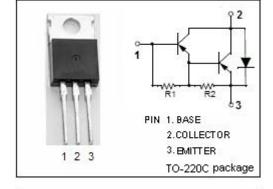


isc Silicon PNP Darlington Power Transistor

BDX54C

DESCRIPTION

- · Collector-Emitter Sustaining Voltage-
 - : V_{CEO(sus)}= -100V(Min)
- · High DC Current Gain
- : h_{FE}= 750(Min) @I_C= -3A
- Low Collector Saturation Voltage
 - : $V_{CE(sat)} = -2.0 \text{ V (Max)} @ I_C = -3.0 \text{ A}$
- Complement to Type BDX53C
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

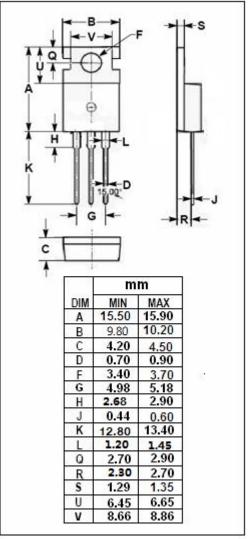
 Designed for general-purpose amplifier and low-speed switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-100	V
V _{CEO}	Collector-Emitter Voltage	-100	V
V_{EBO}	Emitter-Base Voltage	-5	V
Ic	Collector Current-Continuous	-8	Α
I _{CP}	Collector Current-Peak	-12	Α
I _B	Base Current-Continuous	-0.2	Α
Pc	Collector Power Dissipation @ T _C =25°C 60		W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature Range	-65~150	$^{\circ}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance,Junction to Case	1.92	°C/W





isc Silicon PNP Darlington Power Transistor

BDX54C

ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Breakdown Voltage	I _C = -50mA; I _B = 0	-100			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -12mA			-2.0	٧
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	I _C = -3A; I _B = -12mA			-2.5	٧
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-0.2	mA
I _{CEO}	Collector Cutoff Current	V _{CE} = -50V; I _B = 0			-0.5	mA
І _{ЕВО}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-2	mA
h _{FE}	DC Current Gain	I _C = -3A ; V _{CE} = -3V	750			

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