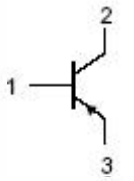
isc Silicon PNP Power Transistor — BD736



1 2 3

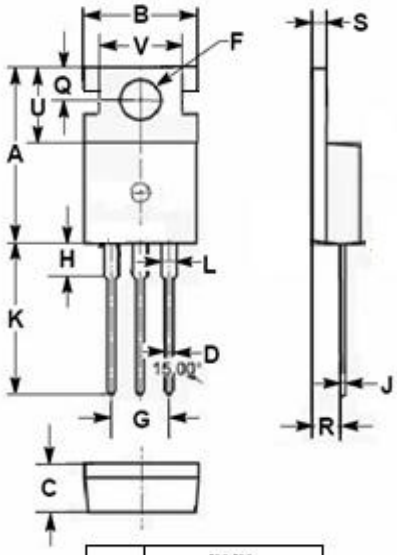
PIN 1. BASE
2.COLLECTOR
3. EMITTER
TO-220C package

DESCRIPTION

- DC Current Gain -
  : h_{FE} = 40(Min.)@ I_C= -20mA
- Collector-Emitter Breakdown Voltage-
  : $V_{(BR)CEO}$= -35V(Min.)
- Complement to Type BD735
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for amplifier and switching applications.



DIM	mm MIN	mm MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-35	V
V_{CEO}	Collector-Emitter Voltage	-35	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current-Continuous	-4	A
I_{CM}	Collector Current-Peak	-7	A
I_B	Base Current-Continuous	-1	A
P_C	Collector Power Dissipation @ T_a=25℃	2	W
	Collector Power Dissipation @ T_C=25℃	40	
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature Range	-55~150	℃

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	I_C= -30mA; I_B= 0	-35		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	I_C= -0.1mA; I_E= 0	-35		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	I_E= -1mA; I_C= 0	-5		V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	I_C= -2A; I_B= -0.2A		-0.6	V
$V_{BE(on)}$	Base-Emitter On Voltage	I_C= -2A; V_{CE}= -1V		-1.1	V
I_{CES}	Collector Cutoff Current	V_{CE}= -35V; V_{BE}= 0		-0.2	mA
h_{FE-1}	DC Current Gain	I_C= -20mA; V_{CE}= -4V	40		
h_{FE-2}	DC Current Gain	I_C= -2A; V_{CE}= -1V	40		