**Product data sheet** 

# 1. General description

PNP Darlington transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: BCV47-Q

## 2. Features and benefits

- High current
- · High current gain
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

# 3. Applications

For general AF applications and where high amplification is required

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-	-80	V
V <sub>CES</sub>	collector-emitter voltage	base short-circuited to emitter		-	-	-60	V
I <sub>C</sub>	collector current			-	-	-500	mA
I <sub>CM</sub>	peak collector current			-	-	-800	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; $I_{C}$ = -100 mA; $T_{amb}$ = 25 °C	[1]	10000	-	-	

<sup>[1]</sup> Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

# 5. Pinning information

**Table 2. Pinning information** 

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	B C
2	Е	emitter		I TO
3	С	collector		TR1 TR2
			SOT23	aaa-034789



## **PNP Darlington transistor**

# 6. Ordering information

#### **Table 3. Ordering information**

Type number	Package	age			
	Name	Description	Version		
BCV46-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code[1]
BCV46-Q	FE%

<sup>[1] % =</sup> placeholder for manufacturing site code

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-80	V
V <sub>CES</sub>	collector-emitter voltage	base short-circuited to emitter		-	-60	V
$V_{EBO}$	emitter-base voltage	open collector		-	-10	V
I <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current			-	-800	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
T <sub>j</sub>	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

#### **Table 6. Thermal characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

# **PNP** Darlington transistor

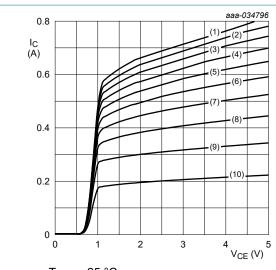
# 10. Characteristics

**Table 7. Characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{(BR)CBO}$	collector-base breakdown voltage	$I_C = -100 \ \mu A; I_E = 0 \ A; T_{amb} = 25 \ ^{\circ}C$		-80	-	-	V
V <sub>(BR)CES</sub>	collector-emitter breakdown voltage	$I_C = -2 \text{ mA}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$		-60	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	$I_C = 0 \text{ A}; I_E = -100 \mu\text{A}; T_{amb} = 25 \text{ °C}$		-10	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = -60 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>CE</sub> = -60 V; V <sub>BE</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	-	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -10 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; $I_{C}$ = -1 mA; $T_{amb}$ = 25 °C	[1]	2000	-	-	
		$V_{CE}$ = -5 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	[1]	4000	-	-	
		$V_{CE}$ = -5 V; $I_{C}$ = -100 mA; $T_{amb}$ = 25 °C	[1]	10000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$_{\rm C}$ = -100 mA; $_{\rm I_B}$ = -0.1 mA; $_{\rm Tamb}$ = 25 °C		-	-	-1	V
V <sub>BEsat</sub>	base-emitter saturation voltage			-	-	-1.5	V
$V_{BEon}$	base-emitter turn-on voltage	$I_C$ = -10 mA; $V_{CE}$ = -5 V; $T_{amb}$ = 25 °C		-	-	-1.4	V
t <sub>d</sub>	delay time	I <sub>C</sub> = 100 mA; I <sub>Bon</sub> = 0.1 mA;		-	225	-	ns
t <sub>r</sub>	rise time	$I_{Boff}$ = -0.1 mA; $V_{CC}$ = 5 V; $T_{amb}$ = 25 °C		-	200	-	ns
t <sub>on</sub>	turn-on time			-	425	-	ns
t <sub>s</sub>	storage time			-	520	-	ns
t <sub>f</sub>	fall time			-	810	-	ns
t <sub>off</sub>	turn-off time			-	1330	-	ns

<sup>[1]</sup> Pulse test:  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

#### **PNP Darlington transistor**



 $T_{amb}$  = 25 °C

(1)  $I_B = 35.0 \mu A$ 

(2)  $I_B = 31.5 \mu A$ (3)  $I_B = 28.0 \mu A$ 

(4)  $I_B = 24.5 \,\mu A$ 

 $(5) I_B = 21.0 \mu A$ 

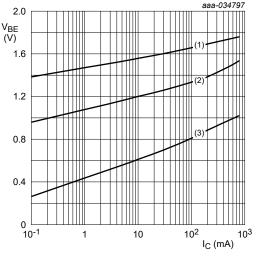
(6)  $I_B = 17.5 \,\mu A$ 

 $(7) I_B = 14.0 \mu A$ 

 $(8) I_B = 10.5 \mu A$ 

(9)  $I_B = 7.0 \, \mu A$ 

(10)  $I_B = 3.5 \mu A$ 

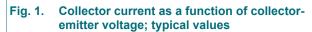


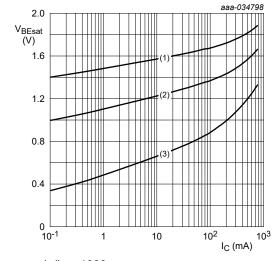
 $V_{CE} = 5 V$ (1)  $T_{amb} = -55 °C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Fig. 2. Base-emitter voltage as a function of collector current; typical values





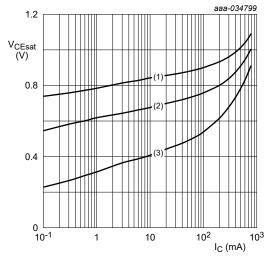
 $I_{\rm C}/I_{\rm B} = 1000$ 

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2)  $T_{amb}$  = 25 °C

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Base-emitter saturation voltage as a function of Fig. 4. Fig. 3. collector current; typical values



 $I_{\rm C}/I_{\rm B} = 1000$ 

(1)  $T_{amb} = -55 \, ^{\circ}C$ 

(2)  $T_{amb} = 25 \, ^{\circ}C$ 

(3)  $T_{amb} = 150 \, ^{\circ}C$ 

Collector-emitter saturation voltage as a function of collector current; typical values

## **PNP Darlington transistor**

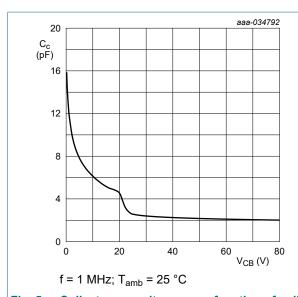
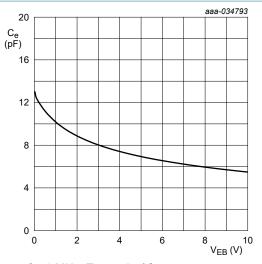
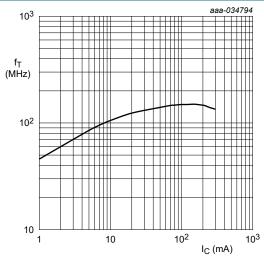


Fig. 5. Collector capacitance as a function of collectorbase voltage; typical values



 $f = 1 \text{ MHz}; T_{amb} = 25 \text{ }^{\circ}\text{C}$ 

Fig. 6. Emitter capacitance as a function of emitterbase voltage; typical values



 $V_{CE}$  = 5 V; F = 1000 Hz

Fig. 7. Transition frequency as a function of collector current; typical values

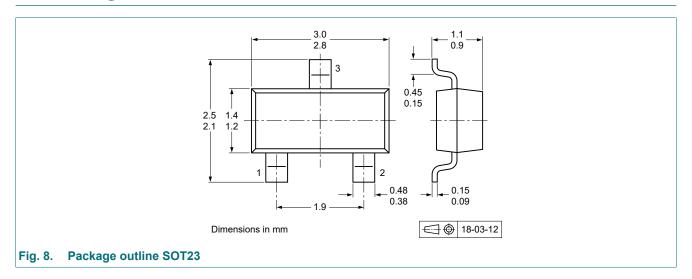
## 11. Test information

### **Quality information**

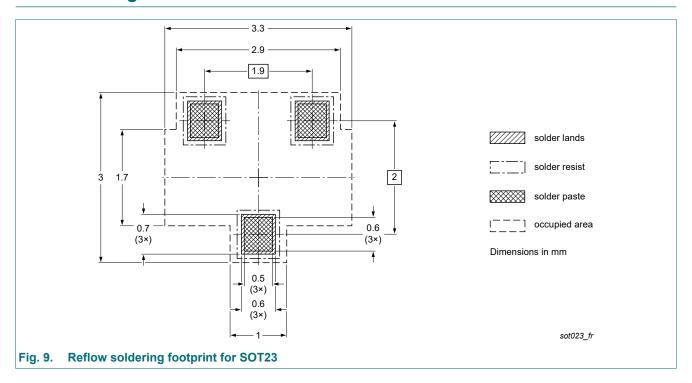
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## **PNP Darlington transistor**

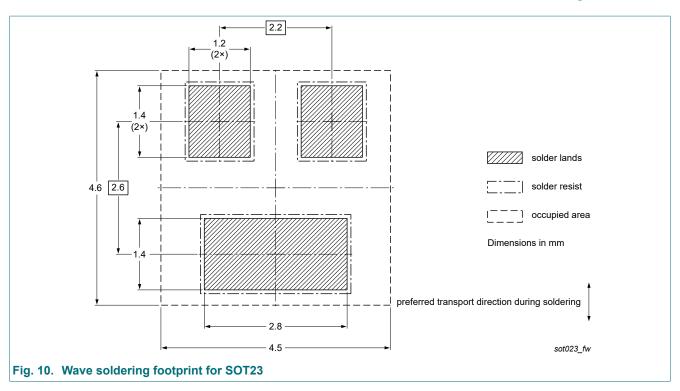
# 12. Package outline



# 13. Soldering



## **PNP Darlington transistor**



# **PNP** Darlington transistor

# 14. Revision history

## Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCV46-Q v.1	20220512	Product data sheet	-	-

### **PNP Darlington transistor**

## 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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