# **Switching Diode**

### **Features**

 These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Continuous Reverse Voltage	$V_R$	100	Vdc
Peak Forward Current	I <sub>F</sub>	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL CHARACTERISTICS

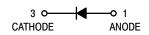
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	260	mW
Derated above 25°C		2.0	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ hetaJA}$	490	°C/W
Total Device Dissipation, FR-4 Board (Note 2) T <sub>A</sub> = 25°C	P <sub>D</sub>	580	mW
Derated above 25°C		4.6	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	215	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

- 1. FR-4 @ Minimum Pad
- 2. FR-4  $\stackrel{.}{@}$  1.0  $\times$  1.0 Inch Pad



## ON Semiconductor®

http://onsemi.com



### MARKING DIAGRAM





A6 = Specific Device Code M = Date Code

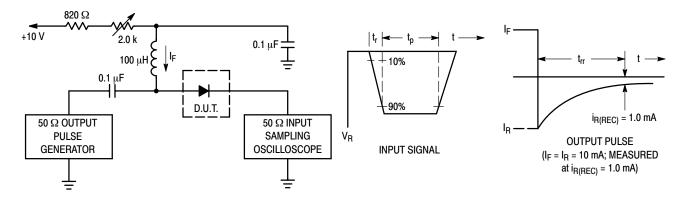
### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
BAS16M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# 

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•		
Reverse Voltage Leakage Current $(V_R = 100 \text{ Vdc})$ $(V_R = 75 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	IR	- - -	1.0 50 30	μAdc
Reverse Breakdown Voltage (I <sub>BR</sub> = 100 µAdc)	V <sub>(BR)</sub>	100	-	Vdc
Forward Voltage $ \begin{aligned} &(I_F = 1.0 \text{ mAdc}) \\ &(I_F = 10 \text{ mAdc}) \\ &(I_F = 50 \text{ mAdc}) \\ &(I_F = 150 \text{ mAdc}) \end{aligned} $	V <sub>F</sub>	- - - -	715 855 1000 1250	mV
Diode Capacitance (V <sub>R</sub> = 0, f = 1.0 MHz)	C <sub>D</sub>	-	2.0	pF
Forward Recovery Voltage (I <sub>F</sub> = 10 mAdc, t <sub>r</sub> = 20 ns)	V <sub>FR</sub>	-	1.75	Vdc
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mAdc, R <sub>L</sub> = 50 $\Omega$ )	t <sub>rr</sub>	-	6.0	ns
Stored Charge (I <sub>F</sub> = 10 mAdc to $V_R$ = 5.0 Vdc, $R_L$ = 500 $\Omega$ )	Qs	-	45	рС



Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.

- 2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.
- 3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

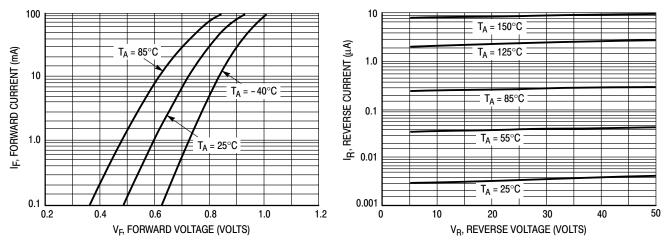


Figure 2. Forward Voltage

Figure 3. Leakage Current

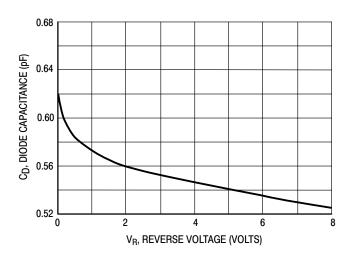
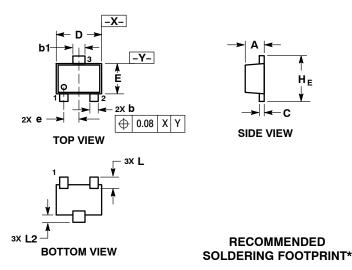


Figure 4. Capacitance

### PACKAGE DIMENSIONS

### SOT-723 CASE 631AA ISSUE D



#### NOTES:

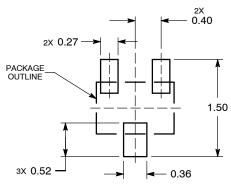
- DIMENSIONING AND TOLERANCING PER ASME
- 714.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETERS.
  MAXIMUM LEAD THICKNESS INCLUDES LEAD
  FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM
- THICKNESS OF BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD
  FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
C	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
е	0.40 BSC		
HΕ	1.15	1.20	1.25
Г	0.29 REF		
L2	0.15	0.20	0.25

STYLE 2:

PIN 1. ANODE 2. N/C

3. CATHODE



**DIMENSIONS: MILLIMETERS** 

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, ON Semiconductor and war registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC wors the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent—Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implications the polar or other applications intended to surgical implication in which the failure of the SCILLC products could create a situation where surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT: Literature Distribution Center for ON Semiconductor

P.O. Box 5163, Denver, Colorado 80217 USA **Phone**: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative