SILICON TRANSISTOR **2SB1453**

PNP SILICON EPITAXIAL POWER TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SB1453 is a power transistor that can directly drive from the IC output. This transistor is ideal for motor drivers and solenoid drivers in such as OA and FA equipment.

In addition, a small resin-molded insulation type package contributes to high-density mounting and reduction of mounting cost.

FEATURES

NEC

- · High DC current amplifier ratio $h_{FE} \ge 100 (V_{CE} = -5 V, I_{C} = -0.5 A)$
- · Mold package that does not require an insulating board or insulation bushing

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)								
Parameter	Symbol	Ratings	Unit					
Collector to base voltage	Vсво	-60	V					
Collector to emitter voltage	VCEO	-60	V					
Emitter to base voltage	VEBO	-7.0	V					
Collector current (DC)	IC(DC)	-3.0	А					
Collector current (pulse)	C(pulse)*	-6.0	А					
Base current (DC)	IB(DC)	-1.0	А					
Total power dissipation	P⊤ (Tc = 25°C)	25	W					
Total power dissipation	P⊤ (Ta = 25°C)	2.0	W					
Junction temperature	Tj	150	°C					
Storage temperature	Tstg	–55 to +150	°C					

A

 $PW \le 10 \text{ ms}$, duty cycle $\le 50\%$

$1\underline{0.3}\pm0.3$ 5 ± 0.2 $\phi 3.2 \pm 0.2$ <u>2.7 ± 0.</u>2 0 +| 15.0 ± 0.3 0.65 ± 0.1 2.5 ± 0.1 0.7 2.54 TYP .54 TYP Electrode Connection 1. Base 2. Collector 3. Emitter

PACKAGE DRAWING (UNIT: mm)

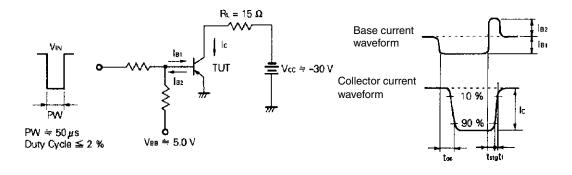
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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

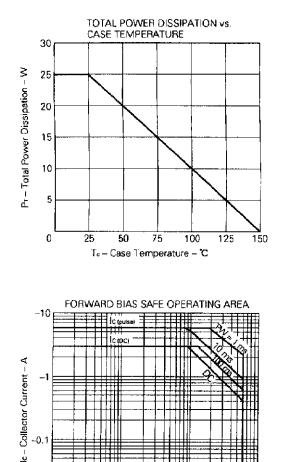
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Collector cutoff current	Ісво	$V_{CB} = -60 \text{ V}, \text{ I}_{E} = 0$			-10	μA
	DC current gain	hfe1**	Vce = -5.0 V, Ic = -0.5 A	100		400	_
	DC current gain	hfe2**	$V_{CE} = -5 V$, $I_C = -3 A$	20			-
	Collector saturation voltage	V _{CE(sat)} **	Ic = -3.0 A, Iв = -300 mA			-1.0	V
	Base saturation voltage	V _{BE(sat)} **	Ic = -3.0 A, Iв = -300 mA			-2.0	V
	Gain bandwidth product	f⊤	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$		5		MHz
	Collector capacitance	Cob	V _{CB} = -10 V, I _E = 0, f = 1.0 MHz		80		pF
ww.DataSheei41	Turn-on time	ton	$\label{eq:lc} \begin{array}{l} I_{C}=-2.0 \text{ A}, \ I_{B1}=-I_{B2}=-200 \text{ mA}, \\ R_{L}=15 \ \Omega, \ V_{CC}\cong -30 \text{ V} \\ \end{array}$ Refer to the test circuit.		0.4		μs
	Storage time	tstg			1.7		μs
	Fall time	tr			0.5		μs

** Pulse test PW \leq 350 μ s, duty cycle \leq 2%

SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT



TYPICAL CHARACTERISTICS (Ta = 25°C)



11

-10

-100

-1

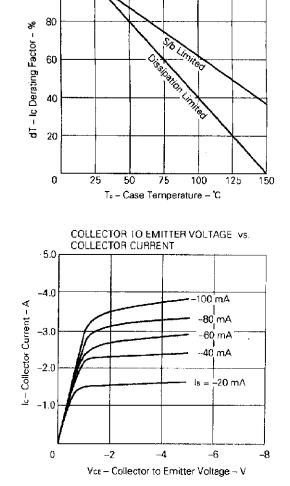
-0.1

--0.01 -0.1

T_c = 25 °C Single Pulse

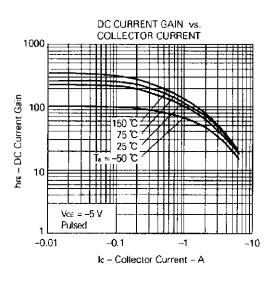
--1

Vce - Collector to Emitter Voltage -- V

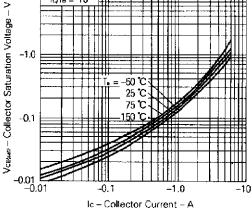


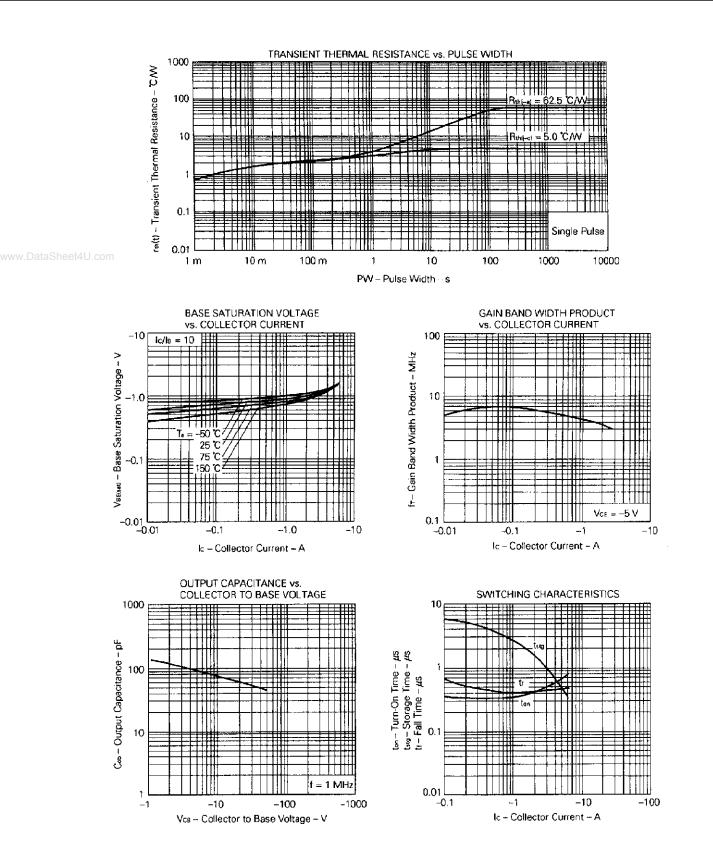
DERATING CURVE OF SAFE OPERATING AREA

100









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