



AiP74LVC2G14

Dual Inverter Schmitt Trigger

Product Specification

Specification Revision History:

Version	Date	Description
2019-05-A1	2019-05	New



1、General Description

The AiP74LVC2G14 provides two inverting buffers with Schmitt-trigger input. It is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

The inputs can be driven from either 3.3V or 5V devices. This feature allows the use of this device in a mixed 3.3V and 5V environment. Schmitt-trigger action at the inputs makes the circuit tolerant of slower input rise and fall time. This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Features:

- Wide supply voltage range from 1.65V to 5.5V
- 5V tolerant inputs for interfacing with 5V logic
- $\pm 24\text{mA}$ output drive ($V_{CC}=3.0\text{V}$)
- CMOS low power consumption
- Latch-up performance exceeds 250mA
- Direct interface with TTL levels
- Unlimited rise and fall times
- Input accepts voltages up to 5V
- Specified from -40°C to $+85^{\circ}\text{C}$
- Packaging information: SOT-23-6/ SOT-363

Ordering Information:

Reel packing specifications:

Type number	Packaging form	Marking code	Reel quantity	Boxed reel quantity	Packing quantity	Notes
AiP74LVC2G14GB.TR	SOT-23-6	AP	3000 PCS/reel	30000 PCS/box	120000 PCS/pack	Dimensions of plastic enclosure: 2.9mm×1.6mm Pin spacing: 0.95mm
AiP74LVC2G14GC.TR	SOT-363(1)	AP	2500 PCS/reel	25000 PCS/box	100000 PCS/pack	Dimensions of plastic enclosure: 2.1mm×1.3mm Pin spacing: 0.65mm
AiP74LVC2G14GC.TR	SOT-363(2)	AP	3000 PCS/reel	30000 PCS/box	120000 PCS/pack	Dimensions of plastic enclosure: 2.1mm×1.3mm Pin spacing: 0.65mm

Note: If the physical information is inconsistent with the ordering information, please refer to the actual product.



2、Block Diagram And Pin Description

2.1、Block Diagram

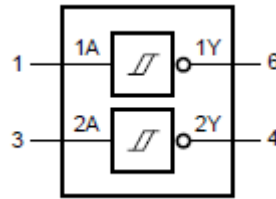


Figure 1. Logic symbol

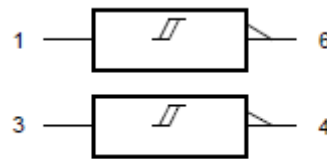


Figure 2. IEC logic symbol

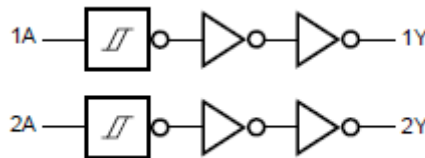
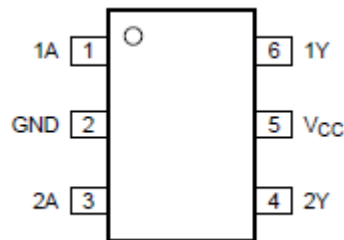


Figure 3. Logic diagram

2.2、Pin Configurations



2.3、Pin Description

Pin No.	Pin Name	Description
1	1A	data input
2	GND	ground (0V)
3	2A	data input
4	2Y	data output
5	V _{CC}	supply voltage
6	1Y	data output

**2.4、Function Table**

Input	Output
nA	nY
L	H
H	L

Note: H=HIGH voltage level; L=LOW voltage level.

3、Electrical Parameter**3.1、Absolute Maximum Ratings**

(Voltages are referenced to GND(ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions	Min.	Max.	Unit
supply voltage	V_{CC}	-	-0.5	+6.5	V
input voltage	V_I	-	-0.5	+6.5	V
output voltage	V_O	Active mode	-0.5	$V_{CC}+0.5$	V
		Power-down mode; $V_{CC}=0V$	-0.5	+6.5	V
input clamping current	I_{IK}	$V_I < 0V$	-50	-	mA
output clamping current	I_{OK}	$V_O > V_{CC}$ or $V_O < 0V$	-	± 50	mA
output current	I_O	$V_O=0V$ to V_{CC}	-	± 50	mA
supply current	I_{CC}	-	-	100	mA
ground current	I_{GND}	-	-100	-	mA
storage temperature	T_{stg}	-	-65	+150	°C
total power dissipation	P_{tot}	-	-	250	mW
Soldering temperature	T_L	10s	250		°C

3.2、Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
supply voltage	V_{CC}	-	1.65	-	5.5	V
input voltage	V_I	-	0	-	5.5	V
output voltage	V_O	Active mode	0	-	V_{CC}	V
		Power-down mode; $V_{CC}=0V$	0	-	5.5	V
ambient temperature	T_{amb}	-	-40	-	+85	°C



3.3、Electrical Characteristics

3.3.1、DC Characteristics

(T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
HIGH-level output voltage	V _{OH}	V _I = V _{T+} or V _{T-}	I _O =-100uA; V _{CC} =1.65V to 5.5V	V _{CC} - 0.1	-	-	V
			I _O =-4mA; V _{CC} =1.65V	1.2	-	-	V
			I _O =-8mA; V _{CC} =2.3V	1.9	-	-	V
			I _O =-12mA; V _{CC} =2.7V	2.2	-	-	V
			I _O =-24mA; V _{CC} =3.0V	2.3	-	-	V
			I _O =-32mA; V _{CC} =4.5V	3.8	-	-	V
LOW-level output voltage	V _{OL}	V _I = V _{T+} or V _{T-}	I _O =100uA; V _{CC} =1.65V to 5.5V	-	-	0.10	V
			I _O =4mA; V _{CC} =1.65V	-	-	0.45	V
			I _O =8mA; V _{CC} =2.3V	-	-	0.30	V
			I _O =12mA; V _{CC} =2.7V	-	-	0.40	V
			I _O =24mA; V _{CC} =3.0V	-	-	0.55	V
			I _O =32mA; V _{CC} =4.5V	-	-	0.55	V
input leakage current	I _I	V _I =5.5V or GND; V _{CC} =0V to 5.5V		-	±0.1	±1	uA
power-off leakage current	I _{OFF}	V _I or V _O =5.5V; V _{CC} =0V		-	±0.1	±2	uA
supply current	I _{CC}	V _I =5.5V or GND; I _O =0A; V _{CC} =1.65V to 5.5V		-	0.1	4	uA
additional supply current	ΔI _{CC}	V _I =V _{CC} -0.6V; I _O =0A; V _{CC} =2.3V to 5.5V		-	5	500	uA
input capacitance	C _I	V _{CC} =3.3V; V _I =GND to V _{CC}		-	3.5	-	pF

Note: All typical values are measured at maximum V_{CC} and T_{amb}=25°C.

3.3.2、AC Characteristics

(T_{amb}=-40°C to +85°C, voltages are referenced to GND (ground=0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
nA to nY propagation delay	t _{pd}	see Figure 5	V _{CC} =1.65V to 1.95V	1.0	5.6	11.0	ns
			V _{CC} =2.3V to 2.7V	0.5	3.7	6.5	ns
			V _{CC} =2.7V	0.5	4.1	7.0	ns
			V _{CC} =3.0V to 3.6V	0.5	3.9	6.0	ns
			V _{CC} =4.5V to 5.5V	0.5	2.7	4.3	ns
Power dissipation capacitance	C _{PD}	V _{CC} =3.3V; V _I =GND to V _{CC}		-	18.1	-	pF

Note:

[1] Typical values are measured at T_{amb}=25°C and V_{CC}=1.8V, 2.5V, 2.7V, 3.3V and 5.0V respectively.[2] t_{pd} is the same as t_{PLH} and t_{PHL}.[3] C_{PD} is used to determine the dynamic power dissipation (P_D in uW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i=input frequency in MHz;



f_o =output frequency in MHz;

C_L =output load capacitance in pF;

V_{CC} =supply voltage in V;

N =number of inputs switching;

$\sum(C_L \times V_{CC}^2 \times f_o)$ =sum of outputs.

3.3.3、Transfer Characteristics

($T_{amb}=-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, voltages are referenced to GND (ground = 0V), unless otherwise specified.)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
positive-going threshold voltage	V _{T+}	see Figure 6 and Figure 7	V _{CC} =1.8V	0.70	1.10	1.50	V
			V _{CC} =2.3V	1.00	1.40	1.80	V
			V _{CC} =3.0V;see Figure 8	1.30	1.76	2.20	V
			V _{CC} =4.5V	1.90	2.47	3.10	V
			V _{CC} =5.5V	2.20	2.91	3.60	V
negative-going threshold voltage	V _{T-}	see Figure 6 and Figure 7	V _{CC} =1.8V	0.25	0.61	0.90	V
			V _{CC} =2.3V	0.40	0.80	1.15	V
			V _{CC} =3.0V;see Figure 8	0.60	1.04	1.50	V
			V _{CC} =4.5V	1.00	1.55	2.00	V
			V _{CC} =5.5V	1.20	1.86	2.30	V
hysteresis voltage	V _H	(V _{T+} - V _{T-}); see Figure 6, Figure 7 and Figure 8	V _{CC} =1.8V	0.15	0.49	1.00	V
			V _{CC} =2.3V	0.25	0.60	1.10	V
			V _{CC} =3.0V;see Figure 8	0.40	0.73	1.20	V
			V _{CC} =4.5V	0.60	0.92	1.50	V
			V _{CC} =5.5V	0.70	1.02	1.70	V

4、Testing Circuit

4.1、AC Testing Circuit

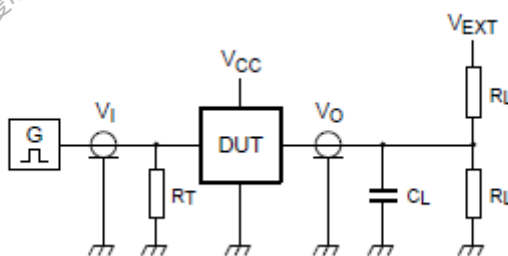


Figure 4. Test circuit for measuring switching times

Definitions for test circuit:

R_L =Load resistance.

C_L =Load capacitance including jig and probe capacitance.

R_T =Termination resistance; should be equal to the output impedance Z_o of the pulse generator.

V_{EXT} =External voltage for measuring switching times.



4.2、AC Testing Waveforms

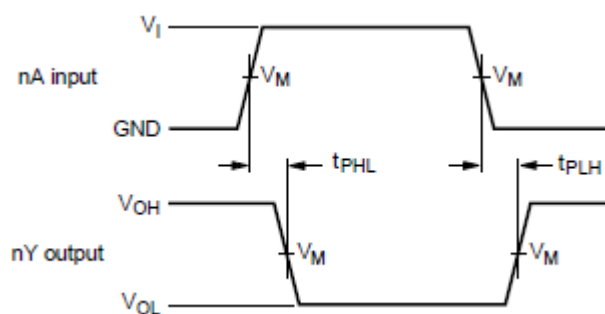


Figure 5. The input nA to output nY propagation delays

4.3、Transfer Characteristics Waveforms

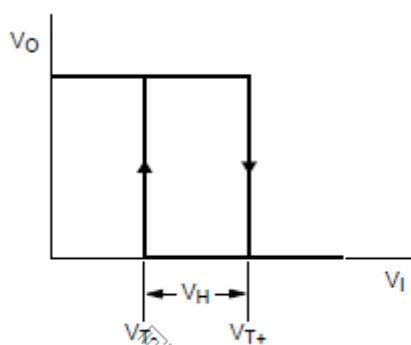


Figure 6. Transfer characteristic

V_{T+} and V_{T-} limits at 70% and 20%:

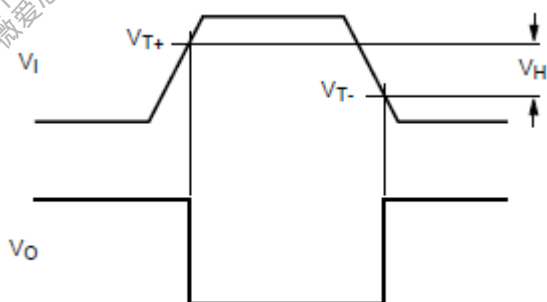


Figure 7. Definition of V_{T+} , V_{T-} and V_H



$V_{CC}=3.0V$:

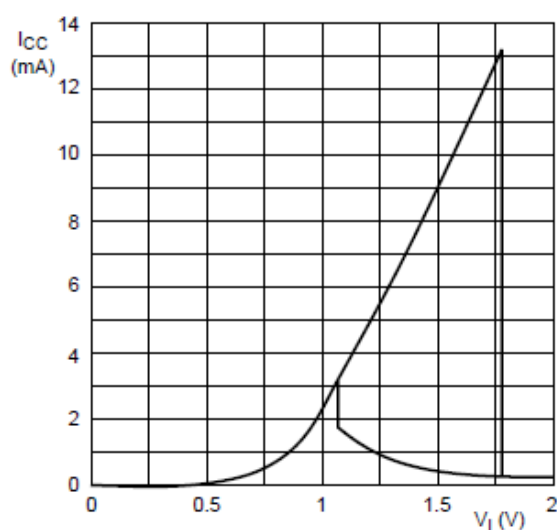


Figure 8. Typical transfer characteristics

4.4、Measurement Points

Supply voltage	Input	Output
V_{CC}	V_M	V_M
1.65V to 1.95V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
2.3V to 2.7V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$
2.7V	1.5V	1.5V
3.0V to 3.6V	1.5V	1.5V
4.5V to 5.5V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$

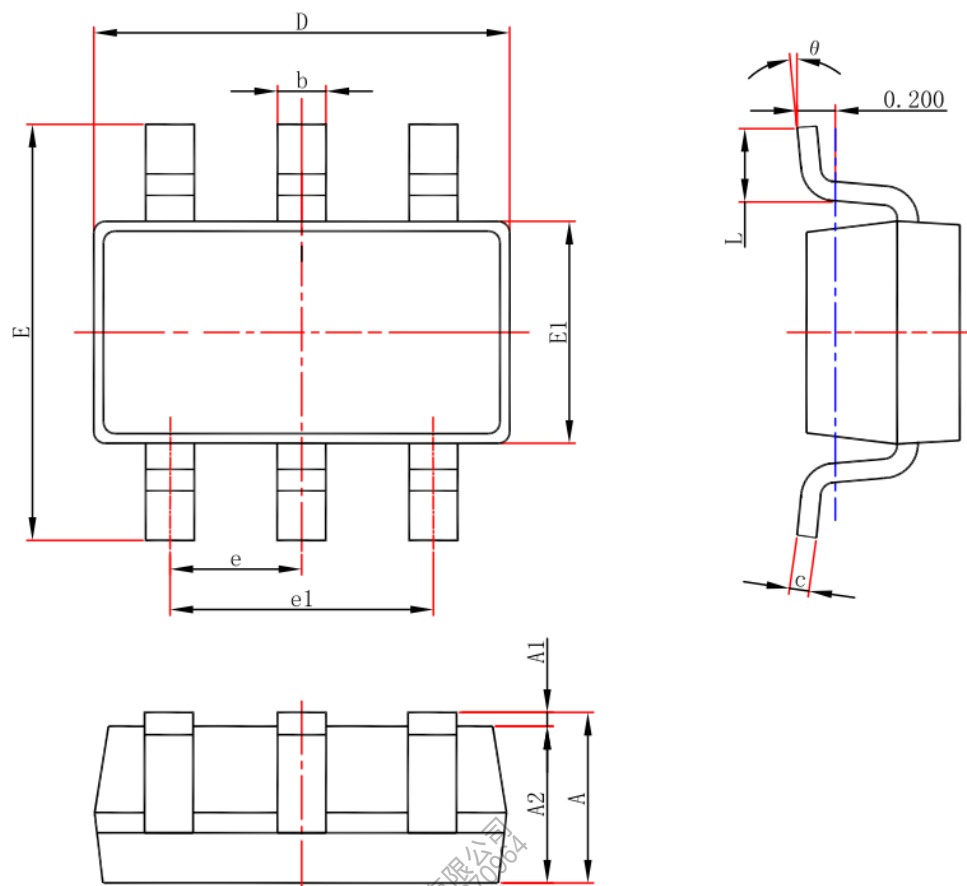
4.5、Test Data

Supply voltage	Input		Load		V_{EXT}
V_{CC}	V_I	$t_r = t_f$	C_L	R_L	t_{PLH}, t_{PHL}
1.65V to 1.95V	V_{CC}	$\leq 2.0ns$	30pF	1k Ω	open
2.3V to 2.7V	V_{CC}	$\leq 2.0ns$	30pF	500 Ω	open
2.7V	2.7V	$\leq 2.5ns$	50pF	500 Ω	open
3.0V to 3.6V	2.7V	$\leq 2.5ns$	50pF	500 Ω	open
4.5V to 5.5V	V_{CC}	$\leq 2.5ns$	50pF	500 Ω	open



5、Package Information

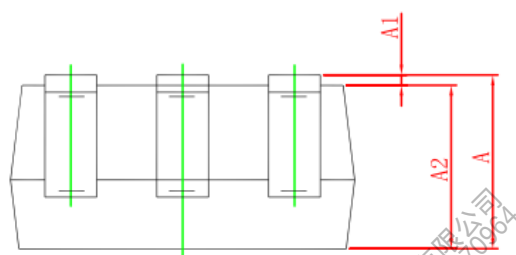
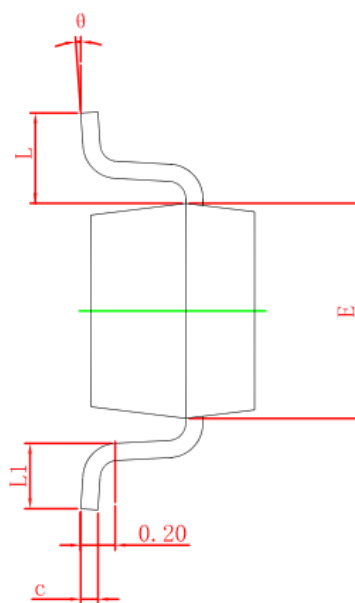
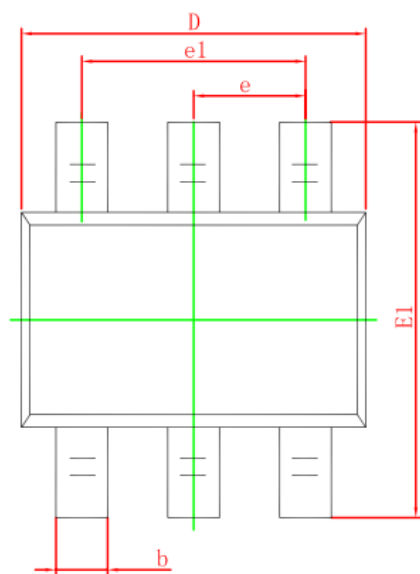
5.1、SOT-23-6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



5.2、SOT-363



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°



6、Statements And Notes

6.1、The name and content of Hazardous substances or Elements in the product

Part name	Hazardous substances or Elements									
	Lead and lead compounds	Mercury and mercury compounds	Cadmium and cadmium compounds	Hexavalent chromium compounds	Polybrominated biphenyls	Polybrominated biphenyl ethers	Dibutyl phthalate	Butylbenzyl phthalate	Di-2-ethylhexyl phthalate	Diisobutyl phthalate
Lead frame	○	○	○	○	○	○	○	○	○	○
Plastic resin	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
The lead	○	○	○	○	○	○	○	○	○	○
Plastic sheet installed	○	○	○	○	○	○	○	○	○	○
explanation	○: Indicates that the content of hazardous substances or elements in the detection limit of the following the SJ/T11363-2006 standard. ×: Indicates that the content of hazardous substances or elements exceeding the SJ/T11363-2006 Standard limit requirements.									

6.2、Notion

Recommended carefully reading this information before the use of this product;

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