

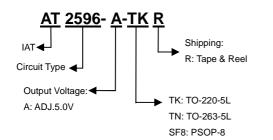
# FEATURES

- Standard PSOP-8/TO-220-5L /TO-263-5L Package
- Adjustable Output Versions
- Adjustable Version Output Voltage Range 1.23V to 37V
- V<sub>OUT</sub> Accuracy is to ± 3% Under Specified Input Voltage the Output Load Conditions
- Input Voltage Range Up to 40V
- Requires Only 4 External Components with High Efficiency
- TTL Shutdown Capability, Low Power Standby Mode
- Built-in Thermal Shutdown, Current Limit
  Protection
- Uses Standard Inductors
- 150 KHz Fixed Frequency Internal Oscillator

## **APPLICATION**

- Pre-Regulator for Linear Regulators
- High-Efficiency Step-Down Buck Regulator
- On-Card/ Board Switching Regulators
- Positive to Negative Converter (Buck)

## **ORDER INFORMATION**



### DESCRIPTION

Immense Advance Tech.

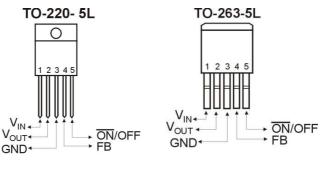
The AT2596 series of regulators are monolithic integrated circuits that provide all the active functions for a step-down (buck) switching regulator, capable of driving a 3A load with excellent line and load regulation.

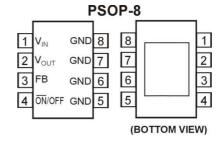
Requiring a minimum number of external components, these regulators are simple to use and include internal frequency compensation, and a fixed-frequency oscillator.

A standard series of inductors are available from several different manufacturers optimized for use with the AT2596 series. This feature greatly simplifies the design of switch-mode power supplies.

The output voltage is guaranteed to  $\pm 3\%$  tolerance under specified input voltage and output load conditions. The oscillator frequency is guaranteed to  $\pm 15\%$ . External shutdown is included, featuring typically 100µA standby current. Self protection features include a two stage frequency reducing current limit for the output switch and an over temperature shutdown for complete protection under fault conditions.

### PIN CONFIGURATIONS (TOP VIEW)







## **PIN DESCRIPTIONS**

Pin Name	Pin Description
V <sub>IN</sub>	Input supply voltage.
V <sub>OUT</sub>	Switching output.
FB	Output voltage feedback.
ON/OFF	ON/OFF Shutdown. Active is "Low" or floating.
GND	Ground.

# **TYPICAL APPLICATION CIRCUITS**

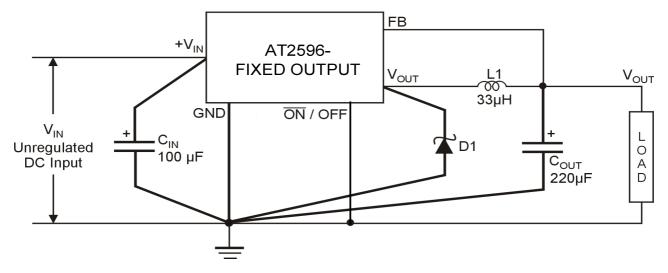


Figure 1. Typical application circuit for AT2596-Fixed version

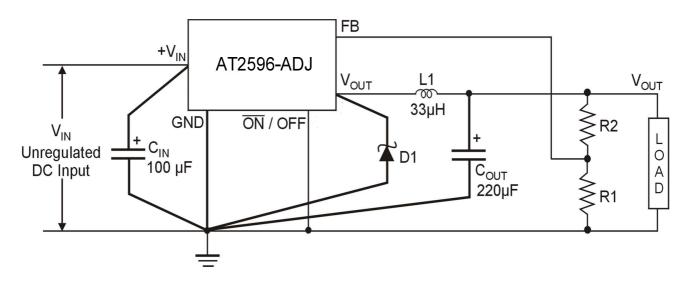


Figure 2. Typical application circuit for AT2596-ADJ version



# **BLOCK DIAGRAM**

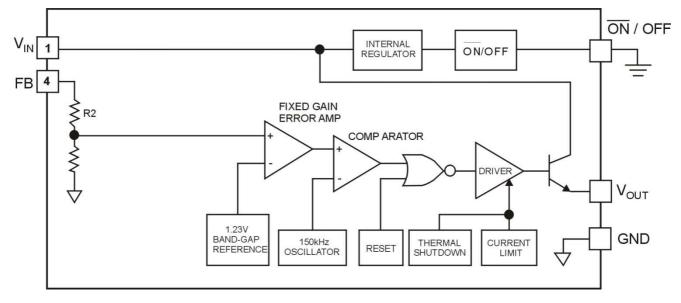


Figure 3. Functional Block Diagram of AT2596



### ABSOLUTE MAXIMUM RATINGS (Note 1)

Para	imeter	Symbol	Max Value	Unit
Maximum supply Voltage	9	V <sub>CC</sub>	45	V
ON/OFF Pin Input Voltag	ge / Feed Back Pin Voltage	V ON/OFF/VFB	-0.3 to + 25	V
Output Voltage to Groun	d (Steady State)	Vout	-0.3 to + 25	V
Maximum Junction Tem	perature	TJ	150	C
Storage Temperature Ra	inge	T <sub>STG</sub>	-65 to +150	C
Lead Temperature (Soldering, 5sec)		T <sub>LEAD</sub>	260	C
Power Dissipation	PSOP-8 (Note 2)	D	2770	m)///
P <sub>D</sub> @ T <sub>A</sub> =25℃	TO-263-5L (Note4)	- P <sub>D</sub> -	5000	— mW
Thermal Resistance	PSOP-8 (Note3)	_	36	~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~
Junction to Ambient	TO-263-5L (Note4)	R <sub>θJA</sub>	20	- ℃W
Thermal Resistance	PSOP-8	_	5.5	~ ~ ~ ~ ~
Junction to Case	TO-263-5L	R <sub>θJC</sub>	2	- ℃W
ESD Rating (Human Bo	dy Model) (Note 4)	V <sub>ESD</sub>	2	kV

# **RECOMMENDED OPERATING CONDITIONS** (Note 6)

Parameter	Symbol	<b>Operation Conditions</b>	Unit
Supply Voltage	V <sub>IN</sub>	4.5 ~ 40	V
Operating Junction Temperature Range	TJ	-40 to +125	ດ ເ
Operating Ambient Temperature Range	T <sub>A</sub>	-40 to +85	ດູ ເ

Note 1: Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Note 2: Thermal Resistance is specified with the component mounted on a low effective thermal conductivity test board in free air at  $T_A=25$  °C.

Note 3: 2 square inch of FR-4, double sided, 1 oz. minimum copper weight.

**Note 4:** Junction to ambient thermal resistance with the TO-263-5L package tab soldered to a double sided printed circuit board with 3 in<sup>2</sup> of (1 oz). Copper area on the AT2596 side of the board, and approximately 16 in<sup>2</sup> of copper on the other side of the p-c board.

**Note 5:** Devices are ESD sensitive. Handling precaution recommended.

Note 6: The device is not guaranteed to function outside its operating conditions.



# **ELECTRICAL CHARACTERISTICS**

#### AT2596-ADJ

Specifications with standard type face are for  $T_J=25$ °C, and those with **boldface type** apply over **Full Operating Temperature Range**, Test Circuit Figure 4. Unless otherwise specified,  $V_{IN}=12$ V.  $I_{LOAD}=200$ mA

Parameter	Symbol	Conditions	Min	<b>Typ</b> (Note 7)	Max	Unit
		V <sub>IN</sub> =12V, I <sub>LOAD</sub> =0.5A, V <sub>OUT</sub> =5V	1.193	1.23	1.267	V
Feedback Voltage	$V_{FB}$	$4.5 \le V_{\text{IN}} \le 40V, 0.2A \le I_{\text{LOAD}} \le 3A,$	1.18	1.23	1.28	V
		V <sub>OUT</sub> =5V	1.10	1.20	1.20	v
Efficiency	η	V <sub>IN</sub> =12V, I <sub>LOAD</sub> =3.0A ,V <sub>OUT</sub> =5V		80		%

#### AT2596-5.0V

Specifications with standard type face are for  $T_J=25^{\circ}$ C, and those with **boldface type** apply over **Full Operating Temperature Range**, Test Circuit Figure 4.

Parameter	Symbol	Condition	Min	<b>Typ</b> (Note 7)	Max	Unit
		V <sub>IN</sub> =12V, I <sub>LOAD</sub> =0.5A	4.850	5.0	5.150	V
Output Voltage	V <sub>OUT</sub>	$7V \leq V_{IN} \leq 40V, 0.2A \leq I_{LOAD} \leq 3A$	4.800 <b>4.750</b>	5.0	5.200 <b>5.250</b>	V
Efficiency	η	V <sub>IN</sub> =12V, I <sub>LOAD</sub> =3.0A		80		%

#### All Output Voltage Versions

Specifications with standard type face are for  $T_J=25$ °C, and those with **boldface type** apply over **Full Operating Temperature Range**, Unless otherwise specified,  $V_{IN}=12V$  for the 3.3V, 5.0V and Adjustable version.  $I_{LOAD}=200$ mA

Parameter	Symbol	Condition	Min	Typ (Note 7)	Max	Unit
Feedback Bias Current	۱ <sub>b</sub>	V <sub>FB</sub> =1.3V (Adjustable Version Only)		15	50 <b>100</b>	nA
Oscillator Frequency	Fosc	(Note 8)	127	150	173	KHz
Saturation Voltage	V <sub>SAT</sub>	I <sub>OUT</sub> =2A (Notes 9 ,10)		1.16	1.4 <b>1.5</b>	V
Max Duty Cycle(ON)	DC	(Note 10)		100		0/
Min Duty cycle(OFF)	DC	(Note 11)		0		%
Current Limit	I <sub>CL</sub>	Peak Current (Notes 9, 10)	3.4	4.5	6.0	А
Output Lookogo		Output =0V (Notes 9, 11)			25	μA
Output Leakage Current	١L	Output= -1V(V <sub>IN</sub> =40V)(Notes 9,11)		1	10	mA
Quiescent Current	l <sub>Q</sub>	(Note 11)		5	10	mA
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#### All Output Voltage Versions

Specifications with standard type face are for  $T_J=25$ °C, and those with **boldface type** apply over **Full Operating Temperature Range**, Unless otherwise specified,  $V_{IN}=12V$  for the 3.3V, 5.0V and Adjustable version.  $I_{LOAD}=200$ mA

Parameter	Symbol	Conditions	Min	<b>Typ</b> (Note 7)	Max	Unit
Standby Quiescent	I	$\overline{ON}OFF$ Pin = 5V (OFF),		100	200	
Current	ISTBY	V <sub>IN</sub> =40V		100	250	μA
OWOFF Pin Logic	V <sub>IL</sub>	Low (ON)		1.3	0.6	V
Input Level	V <sub>IH</sub>	High (OFF)	2.0	1.3		v
ON/OFF Pin Input	IIН	$V_{\overline{ON}/OFF} = 2.5V (OFF)$		5	15	μA
Current	١ <sub>١L</sub>	$V_{\overline{ON}/OFF} = 0.5V$ (ON)		0.02	5	μA

Note7: Typical numbers are at 25℃ and represent the most likely norm.

Note 8 :External components such as the catch diode, inductor, input and output capacitors, and voltage programming resistors can affect switching regulator system performance.

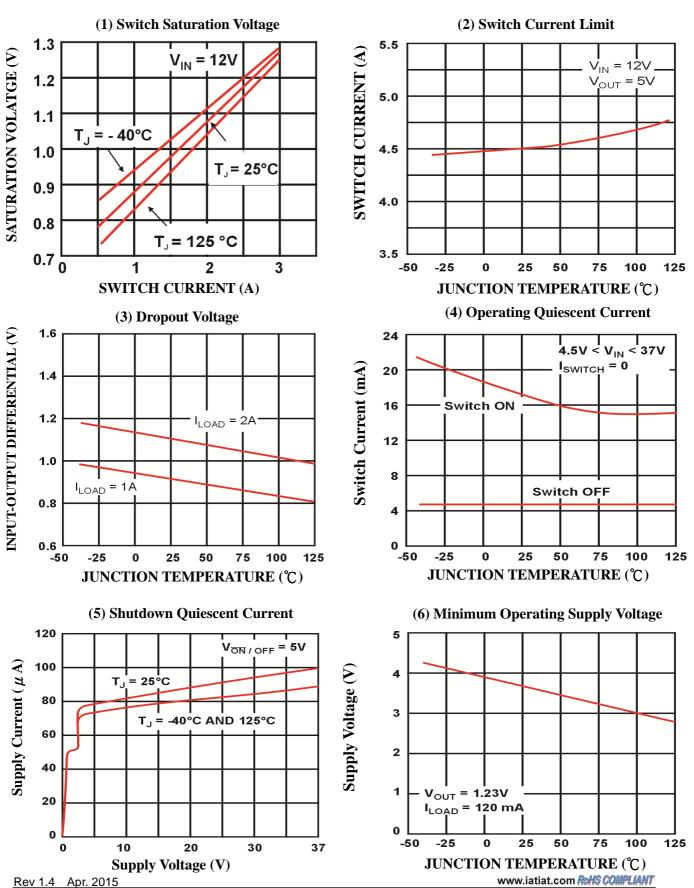
Note 9: No diode, inductor or capacitor connected to output pin.

Note 10: Feedback pin removed from output and connected to 0V to force the output transistor switch ON.

Note 11: Feedback pin removed from output and connected to 12V, to force the output transistor switch OFF.

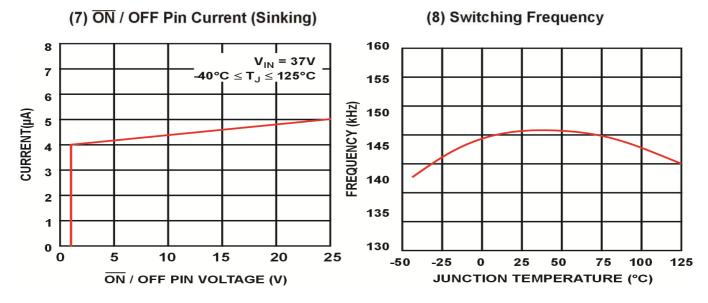


# **TYPICAL OPERATING CHARACTERISTICS**



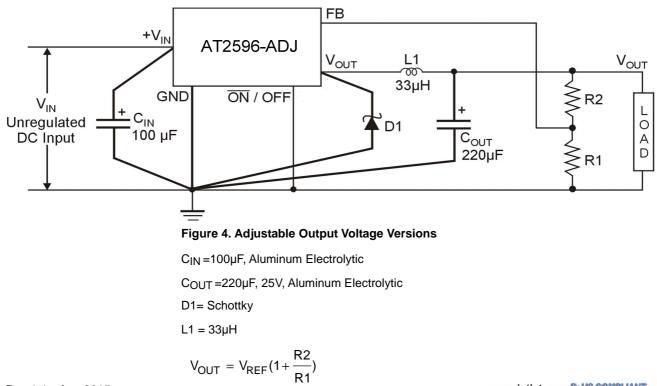


# TYPICAL OPERATING CHARACTERISTICS (CONTINUED)



### **TEST CIRCUIT AND LAYOUT GUIDELINES**

Careful layout is important with any switching regulator. Rapidly switching currents associated with wiring inductance generate voltage transients which can cause problems. To minimize inductance and ground loops, the lengths of the leads indicated by heavy lines in Figure 4 below should be kept as short as possible. Single point grounding (as indicated in figure 4) or ground plane construction should be used for best results. When using the Adjustable version, place the programming resistors as close as possible to AT2596, to keep the sensitive feedback wiring short.



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#### Input Capacitor (C<sub>IN</sub>)

To maintain stability, the regulator input pin must be by-passed with at least a  $47\mu$ F electrolytic capacitor. The capacitor's leads must be kept short, and located near the regulator.

If the operating temperature range includes temperatures below -25°C, the input capacitor value may need to be larger. With most electrolytic capacitors, the capacitance value decreases and the ESR increase with lower temperatures and age. Paralleling a ceramic or solid tantalum capacitor will increase the regulator stability at cold temperatures. For maximum capacitor operating lifetime, the capacitor's RMS ripple current rating should be greater than

$$\begin{split} & 1.2 \times (\frac{t_{ON}}{T}) \times I_{LOAD} = V_{REF}(1 + \frac{R2}{R1}) \\ & \text{Where } \frac{t_{ON}}{T} = \frac{V_{OUT}}{V_{IN}} \text{ for a buck regulator} \\ & \text{and } \frac{t_{ON}}{T} = \frac{\left|V_{OUT}\right|}{\left|V_{OUT}\right| + V_{IN}} \text{ for a buck - boost regulator.} \end{split}$$

#### **Inductor Selection**

All switching regulators have two basic modes of operation: continuous and discontinuous. The difference between the two types relates to the inductor current, whether it is flowing continuously, or if it drops to zero for a period of time in the normal switching cycle. Each mode has distinctively different operating characteristics, which can affect the regulator performance and requirements.

AT2596 can be used for both continuous and discontinuous modes of operation.

The peak-to-peak inductor ripple current will be approximately 20% to 30% of the maximum DC current.

With relatively heavy load currents, the circuit operates in the continuous mode (inductor current always flowing), but under light load conditions, the circuit should be forced to the discontinuous mode (inductor current falls to zero for a period of time). This discontinuous mode of operation is perfectly acceptable. For light loads (less than approximately 200mA) it may be desirable to operate the regulator in the discontinuous mode, primarily because of the lower inductor values required for the discontinuous mode.

The inductor chooses should be values suitable for continuous mode operation, but if the inductor value chosen is prohibitively high, the designer should investigate the possibility of discontinuous operation. Inductors are available in different styles such as pot core, toriod, E-frame, bobbin core, etc., as well as different core materials, such as ferrites and powdered iron. The least expensive, the bobbin core type, consists of wire wrapped on a ferrite rod core. This type of construction makes for an inexpensive inductor, but since the magnetic flux is not completely contained within the core, it generates more electromagnetic interference (EMI). This EMI can cause problems in sensitive circuits, or can give incorrect scope readings because of induced voltages in the scope probe.

The inductors listed in the selection chart include ferrite pot core construction for AIE, powdered iron toroid for Pulse Engineering, and ferrite bobbin core for Renco.



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### **APPLICATION INFORMATION (CONTINUED)**

An inductor should not be operated beyond its maximum rated current because it may saturate. When an inductor begins to saturate, the inductance decreases rapidly and the inductor begins to look mainly resistive (the DC resistance of the winding). This will cause the switch current to rise very rapidly. Different inductor types have different saturation characteristics, and this should be kept in mind when selecting an inductor.

The inductor manufacturer's data sheets include current and energy limits to avoid inductor saturation.

#### **Inductor Ripple Current**

When the switcher is operating in the continuous mode, the inductor current waveform ranges from a triangular to a sawtooth type of waveform (depending on the input voltage). For a given input voltage and output voltage, the peak-to-peak amplitude of this inductor current waveform remains constant. As the load current rises or falls. The average DC value of this waveform is equal to the DC load current (in the buck regulator configuration).

If the load current drops to a low enough level, the bottom of the sawtooth current waveform will reach zero, and the switcher will change to a discontinuous mode of operation. This is a perfectly acceptable mode of operation. Any buck switching regulator (no matter how large the inductor value is ) will be forced to run discontinuous if the load current is light enough.

#### **Output Capacitor**

An output capacitor is required to filter the output voltage and is needed for loop stability. The capacitor should be located near the AT2596 using short pc board traces. Standard aluminum electrolytics are usually adequate, but low ESR types are recommended for low output ripple voltage and good stability. The ESR of a capacitor depends on many factors, some which are: the value, the voltage rating, physical size and the type of construction. In general, low value or low voltage (less than 12V) electrolytic capacitors usually have higher ESR numbers.

The amount of output ripple voltage is primarily a function of the ESR (Equivalent Series Resistance) of the output capacitor and the amplitude of the inductor ripple current ( $\Delta$  I<sub>IND</sub>).

The lower capacitor values  $(220\mu$ F-680 $\mu$ F) will allow typically 50mV to 150mV of output ripple voltage, while larger-value capacitors will reduce the ripple to approximately 20mV to 50mV.

Output Ripple Voltage=  $(\Delta I_{IND})(ESR \text{ of } C_{OUT})$ To further reduce the output ripple voltage, several standard electrolytic capacitors may be paralleled, or a higher-grade capacitor may be used.

Such capacitors are often called "high-frequency," "low-inductance," or "low-ESR." These will reduce the output ripple to 10mV or 20mV. However, when operating in the continuous mode, reducing the ESR below  $0.05\Omega$  can cause instability in the regulator.

Tantalum capacitors can have a very low ESR, and should be carefully evaluated if it is the only output capacitor.



#### **APPLICATION INFORMATION (CONTINUED)**

Because of their good low temperature characteristics, a tantalum can be used in parallel with aluminum electrolytics, with the tantalum making up 10% or 20% of the total capacitance. The capacitor's ripple current rating at 150KHz should be at least 50% higher than the peak-to-peak inductor ripple current.

#### **Catch Diode**

Buck regulators require a diode to provide a return path for the inductor current when the switch is off. This diode should be located close to the AT2596 using short leads and short printed circuit traces.

Because of their fast switching speed and low forward voltage drop, Schottky diodes provide the best efficiency, especially in low output voltage switching regulators (less than 5V). Fast-Recovery, High-Efficiency, or Ultra-Fast Recovery diodes are also suitable, but some types with an abrupt turn-off characteristic may cause instability and EMI problems. A fast-recovery diode with soft recovery characteristics is a better choice. Standard 60Hz diodes (e.g., 1N4001 or 1N5400, etc.) are also **not suitable**.

#### **Output Voltage Ripple and Transients**

The output voltage of a switching power supply will contain a sawtooth ripple voltage at the switcher frequency, typically about 1% of the output voltage, and may also contain short voltage spikes at the peaks of the sawtooth waveform.

The output ripple voltage is due mainly to the inductor sawtooth ripple current multiplied by the ESR of the output capacitor.

The voltage spikes are present because of the fast switching action of the output switch, and the

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parasitic inductance of the output filter capacitor.

To minimize these voltage spikes, special low inductance capacitors can be used, and their lead lengths must be kept short. Wiring inductance, stray capacitance, as well as the scope probe used to evaluate these transients, all contribute to the amplitude of these spikes.

An additional small LC filter ( $20\mu$ H & $100\mu$ F) can be added to the output to further reduce the amount of output ripple and transients. A 10 x reduction in output ripple voltage and transients is possible with this filter.

#### **Feedback Connection**

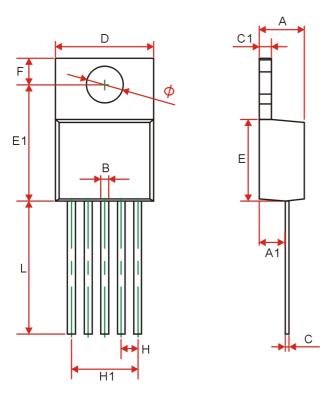
When using AT2596 adjustable version, physically locate both output voltage programming resistors near the AT2596 to avoid picking up unwanted noise. Avoid using resistors greater than  $100K\Omega$  because of the increased chance of noise pickup.

#### **ON/OFF** Input

For normal operation, the ON/OFF pin should be grounded or driven with a low-level TTL voltage (typically below 1.6V). To put the regulator into standby mode, drive this pin with a high-level TTL or CMOS signal. The  $\overline{ON}$ /OFF pin can be safely pulled up to +V<sub>IN</sub> without a resistor in series with it. The pin should not be left open.

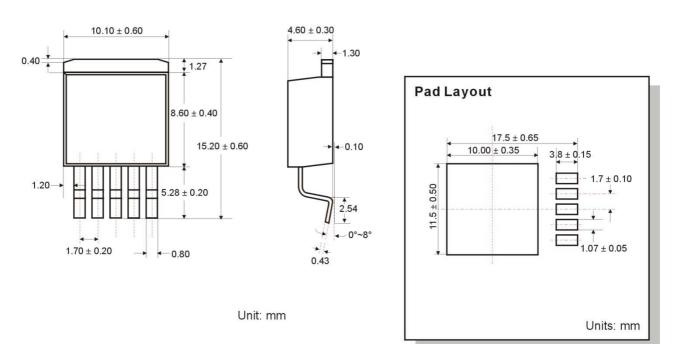


### PACKAGE OUTLINE DIMENSIONS TO-220-5L PACKAGE OUTLINE DIMENSIONS



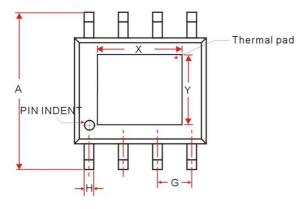
	DIMENSIONS			
REF.	Millimeters			
	Min.	Max.		
А	4.40	4.80		
A1	2.52	2.82		
В	0.76	1.00		
С	0.31	0.53		
C1	1.17	1.37		
D	9.80	10.40		
E	8.20	9.00		
E1	11.76	12.36		
Н	1.70	) TYP.		
H1	6.70	6.90		
F	2.59	2.89		
L	13.50	14.25		
φ	3.71	3.96		

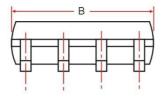
## **TO-263-5L PACKAGE OUTLINE DIMENSIONS**





#### PACKAGE OUTLINE DIMENSIONS **PSOP-8 PACKAGE OUTLINE DIMENSIONS**

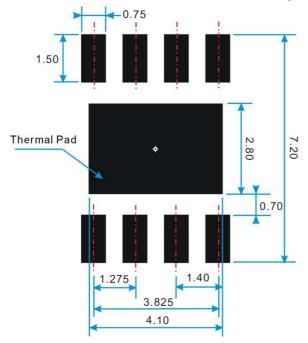




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	DIMEN	SIONS
REF.	Millir	neters
	Min.	Max.
A	5.79	6.20
В	4.80	5.00
С	3.80	4.00
D	0.	8°
E	0.40	1.27
F	0.15	0.26
М	0	0.25
Н	0.31	0.51
L	1.30	1.75
G	1.27	TYP.
Х	3.30	) TYP.
Y	2.50	) TYP.

C-

#### **PSOP-8 PACKAGE FOOTPRINT (mm)**



#### Note :

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