

Digital Attenuator, 31 dB, 5-Bit, TTL Driver DC - 2.0 GHz

AT15-0001

V4.00

Features

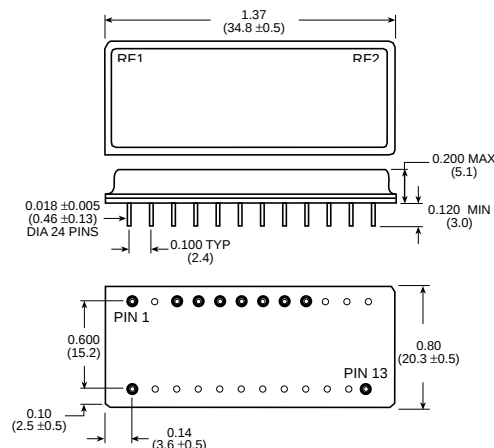
- Attenuation: 1-dB Steps to 31 dB
- Low DC Power Consumption
- Integral TTL Driver
- 50Ω Nominal Impedance

Description

M/A-COM's AT15-0001 is a GaAs FET 5-bit digital attenuator with a 1-dB minimum step size and 31 dB total attenuation. This attenuator and integral TTL driver is in a 24-pin dual in-line package. The AT15-0001 is ideally suited for use where accuracy, fast switching, very low power consumption and low intermodulation products are required. Typical applications include dynamic range setting in precision receiver circuits and other gain/leveling control circuits.

Environmental screening is available. Contact the factory for information.

DI-3



Pin Configuration

1	2	3	4	5	6	7	8	9	10-12	13	14-23	24
+5 V	GND	-12 V	GND	C16	C1	C2	C4	C8	GND	RF2	GND	RF1

Dimensions in () are in mm. Unless otherwise noted:
.xxx = ±0.010 (.xx = ±0.25)

Typical Electrical Specifications¹, T_A = -55°C to +85°C

Parameter	Test Conditions	Units	Minimum	Typical	Maximum
Reference Insertion Loss	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz	dB dB dB			6.0 6.5 7.3
Attenuation Accuracy ^{2,3}	Any Single Bit Any Combination of Bits DC - 1.0 GHz DC - 2.0 GHz DC - 1.0 GHz DC - 2.0 GHz	dB dB dB dB	± (0.2 dB + 2% of attenuation setting in dB) ± (0.25 dB + 2% of attenuation setting in dB) ± (0.2 dB + 2% of attenuation setting in dB) ± (0.3 dB + 2% of attenuation setting in dB)		
VSWR	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz				1.3:1 1.4:1 1.7:1
Trise, Tfall Ton, Toff Transients	10% RF to 90% RF 50% Control to 90%/10% RF In-band	nS nS mV		5 30 220	
1 dB Compression	Input Power 0.05 GHz Input Power 0.5 - 2.0 GHz	dBm dBm		+20 +27	
Input IP ₃	For two-tone input power up to +5 dBm 0.05 GHz 0.5 - 2.0 GHz	dBm dBm		+40 +40	
Input IP ₂	For two-tone input power up to +5 dBm 0.05 GHz 0.5 - 2.0 GHz	dBm dBm		+60 +75	
V _{CC} V _{EE}		V V	4.5 -16.5	5.0	5.5 -10.8
I _{CC}	V _{CC} = 4.5 to 5.5 V V _{ctl} = 0 to 0.8 V, or V _{CC} - 2.1 V to V _{CC}	mA			10
I _{EE}	V _{EE} = -16.5 to -10.8 V V _{EE} = -12.0 V	mA mA			8.0 5.0
V _{ctl}	Logic 0 (TTL) Logic 1 (TTL)	V V	0.0 2.0		0.8 5.0
Input Leakage Current	Low 0 to 0.8 V High 2.0 to 5.0 V	μA μA			1.0 1.0

1. All specifications apply when operated with bias voltages of +5 V for V_{CC} and -16.5 V to -10.8 V for V_{EE}, and 50Ω impedance at all ports unless otherwise specified.
2. Above reference insertion loss.
3. This attenuator is guaranteed monotonic.
4. Replaces AT-104.

Specifications Subject to Change Without Notice.

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Absolute Maximum Ratings¹

Parameter	Absolute Maximum
Maximum Input Power	
DC - 0.5 GHz	+27 dBm
0.5 - 2.0 GHz	+34 dBm
Supply Voltages	
V _{CC}	-0.5 V to +7.0 V
V _{EE}	-18 V to +0.5 V
Control Voltage	-0.5 V to V _{CC} + 0.5 V
Operating Temperature	-55°C to +125°C
Storage Temperature	-65°C to +150°C

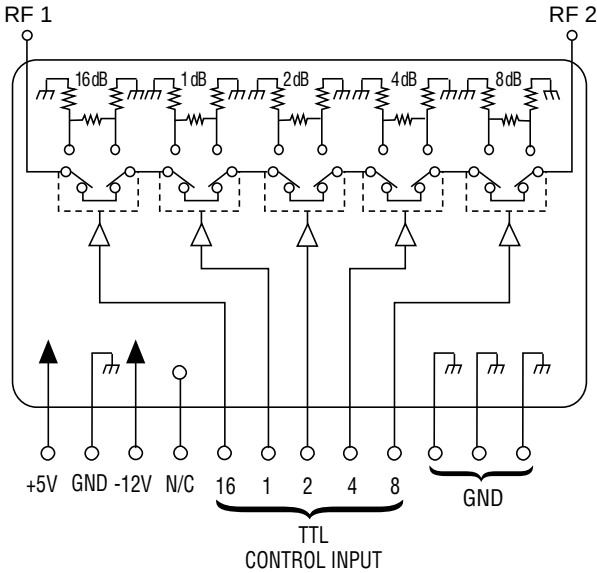
1. Operation of this device above any one of these parameters may cause permanent damage.

Truth Table

Control Inputs					Attenuation
C16	C8	C4	C2	C1	
0	0	0	0	0	Reference
0	0	0	0	1	1 dB
0	0	0	1	0	2 dB
0	0	1	0	0	4 dB
0	1	0	0	0	8 dB
1	0	0	0	0	16 dB
1	1	1	1	1	31 dB

0 = TTL Low
1 = TTL High

Functional Schematic

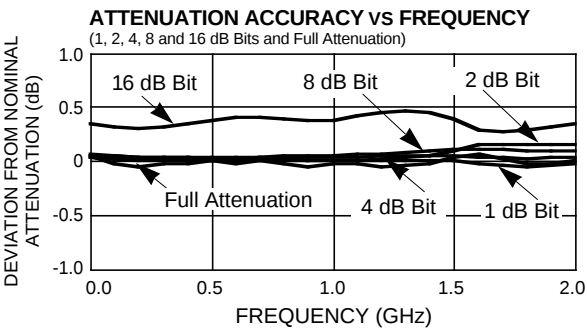
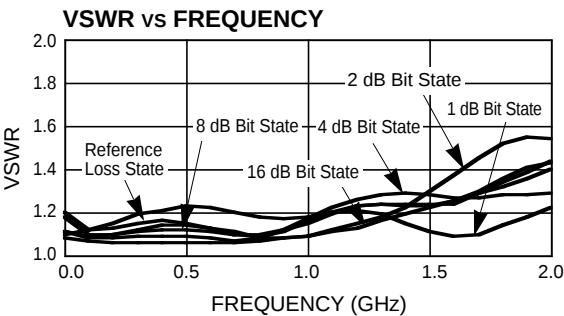
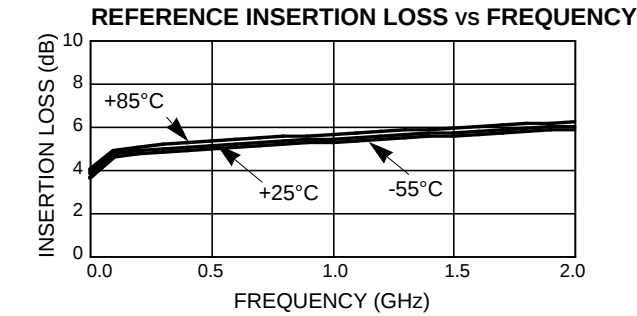


Ordering Information²

Part Number	Package
AT15-0001	Dual Inline

2. Contact the factory for standard or custom screening requirements.

Typical Performance



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