

ARF450





RF POWER MOSFET

N-CHANNEL ENHANCEMENT MODE

150V 500W 120MHz

The ARF450 is a matched pair of RF power transistors in a common source configuration. It is designed for push-pull or parallel operation in scientific, commercial, medical and industrial RF power amplifier applications up to 120 MHz.

- Specified 150 Volt, 81.36 MHz Characteristics:
 - Output Power = 500 Watts.

Gain = 13dB (Class C)

Efficiency = 75%

- High Performance Push-Pull RF Package.
- Very High Breakdown for Improved Ruggedness.
- Low Thermal Resistance.
- Nitride Passivated Die for Improved Reliability.

MAXIMUM RATINGS

All Ratings: T_C = 25°C unless otherwise specified.

Symbol	Parameter	ARF450	UNIT
V _{DSS}	Drain-Source Voltage	450	Volts
V _{DGO}	Drain-Gate Voltage	450	VOILS
I _D	Continuous Drain Current @ T _C = 25°C	11	Amps
V _{GS}	Gate-Source Voltage	±30	Volts
P _D	Total Device Dissipation @ T _C = 25°C	650	Watts
T_J , T_{STG}	Operating and Storage Junction Temperature Range	-55 to 200	°C
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage (V _{GS} = 0V, I _D = 250 μA)	500			\/alta
V _{DS(ON)}	On State Drain Voltage (1) (I _{D(ON)} = 5.5A, V _{GS} = 10V)			5	Volts
	Zero Gate Voltage Drain Current (V _{DS} = V _{DSS} , V _{GS} = 0V)			25	
I _{DSS}	Zero Gate Voltage Drain Current (V _{DS} = 0.8 V _{DSS} , V _{GS} = 0V, T _C = 125°C)			250	μA
I _{GSS}	Gate-Source Leakage Current (V _{GS} = ±30V, V _{DS} = 0V)			±100	nA
g _{fs}	Forward Transconductance (V _{DS} = 25V, I _D = 5.5A)	3	5.8		mhos
g _{fs1} /g _{fs2}	Forward Transconductance Ratio (V _{DS} = 25V, I _D = 5.5A)	0.9		1.1	
V _{GS(TH)}	Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 50mA)	3		5	\/al\a
$\Delta V_{GS(TH)}$	Delta Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 50mA)			0.1	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic (per package unless otherwise noted)		TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case (per section)			0.54	°C 447
$R_{\theta CS}$	Case to Sink (Use High Efficiency Thermal Joint Compound and Planar Heat Sink Surface.)		0.1		°C/W

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - http://www.advancedpower.com

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Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		980	1200	
C _{oss}	Output Capacitance	V _{DS} = 150V		87	120	pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		25	40	
t _{d(on)}	Turn-on Delay Time	V _{GS} = 15V		5	10	
t _r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		3.0	7	ns
t _{d(off)}	Turn-off Delay Time	I _D = I _{D[Cont.]} @ 25°C		15	25	113
t _f	Fall Time	$R_{G}^{}$ = 1.6 Ω		3	7	

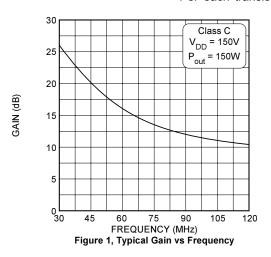
FUNCTIONAL CHARACTERISTICS (Push-Pull Configuration)

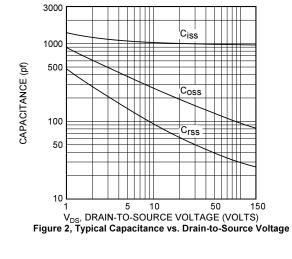
Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
G _{PS}	Common Source Amplifier Power Gain	f = 81.36 MHz	12	13		dB
η	Drain Efficiency	$V_{GS} = 0V$ $V_{DD} = 150V$	70	75		%
Ψ	Electrical Ruggedness VSWR 10:1	P _{out} = 500W	No Degradation in Output Power			

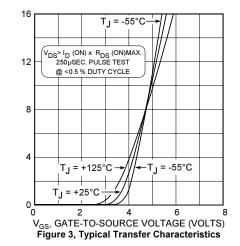
¹ Pulse Test: Pulse width < 380 µS, Duty Cycle < 2%.

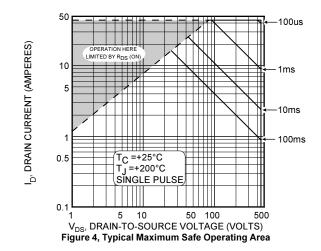
APT Reserves the right to change, without notice, the specifications and information contained herein.

Per each transistor side unless otherwise specified.









ID, DRAIN CURRENT (AMPERES)

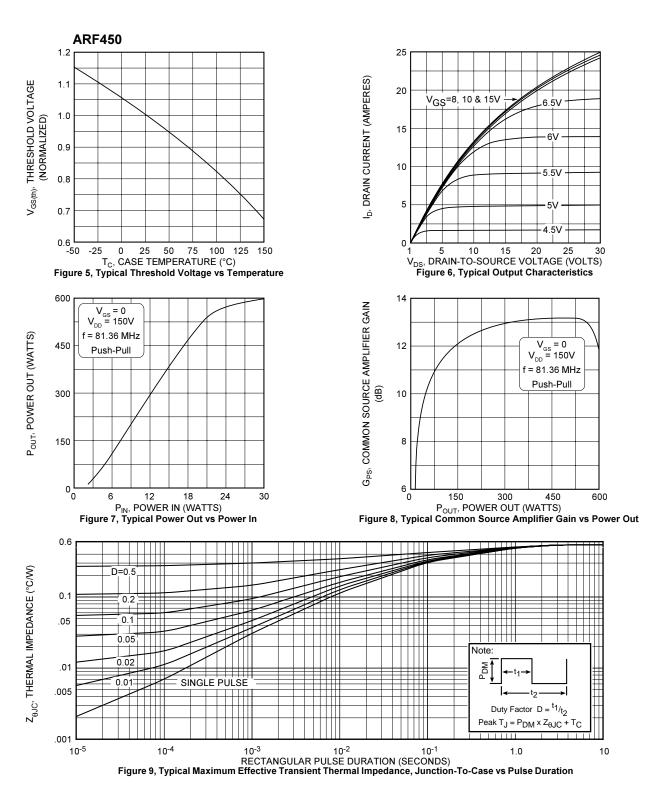
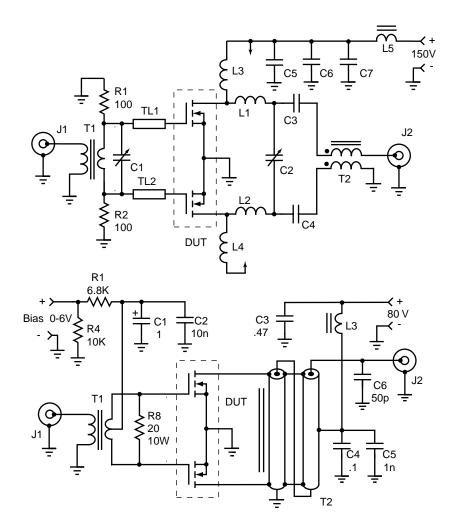


Table 1 - Typical Class C Large Signal Input-Output Impedance (per section)

Freq. (MHz)	Z _{in} (Ω)	Z _{oL} (Ω)
2.0	23.00 - j 7.0	93.0 - j 10
13.5	4.30 - j 9.1	63.0 - j 43
27.0	1.00 - j 4.2	32.0 - j 43
40.0	0.42 - j 1.7	17.5 - j 34
65.0	0.35 + j 1.1	7.7 - j 22
80.0	0.56 + j 2.5	5.1 - j 16
100.0	0.90 + j 3.8	3.4 - j 12

 Z_{in} - gate shunted by 25Ω

Z_{OL} - conjugate of optimum load impedance for 150W at 150V

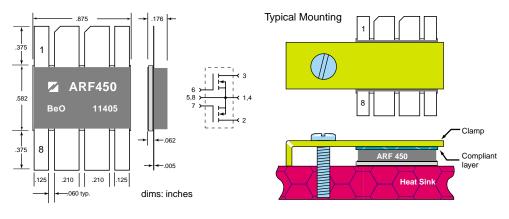


C1 75-380 pF ARCO 465
C2 25-115 pF ARCO 462
C3 -C5 2 nF NPO 500V chip
C6 10 nF 500V chip
C7 .47 uF Ceramic 500V
L1-L2 50 nH 3t # 14 ga .3" dia
L3-L4 .68 uH 12t #24 enam
L5 2t #20 on bead approx 2 uH
R1-R2 100 ohm 1 W
T1 9:1 RF transformer
T2 1:1 coax balun
TL1-TL2 Printed line 1" long

81.36 MHz Test Circuit

T1 9:1 RF Transformer on type 43 beads
T2 4:1 RF Transformer. Made from two pieces
of 25 ohm semi-rigid coax with type 43
ferrite bead loading.

30 MHz Linear Test Circuit



HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and mounting surface is beryllium oxide. Beryllium oxide dust is highly toxic when inhaled. Care must be taken during handling and mounting to avoid damage to this area. These devices must never be thrown away with general industrial or domestic waste.

Thermal Considerations and Package Mounting:

The rated 650W power dissipation is only available when the package mounting surface is at 25°C and the junction temperature is 200°C. The thermal resistance between junctions and case mounting surface is 0.27 °C/W. When installed, an additional thermal impedance of 0.05 °C/W between the package base and the mounting surface is typical. Insure that the mounting surface is smooth and flat. Thermal joint com-

pound must be used to reduce the effects of small surface irregularities. The heatsink should incorporate a copper heat spreader to obtain best results.

The package is designed to be clamped to a heatsink. A clamped joint maintains the required mounting pressure while allowing for thermal expansion of both the device and the heat sink. An L-clamp, a compliant layer of plastic or rubber, and a 6-32 (M3.5) screw can provide the minimum 35 lb required mounting force. T = 4 in-lb.

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