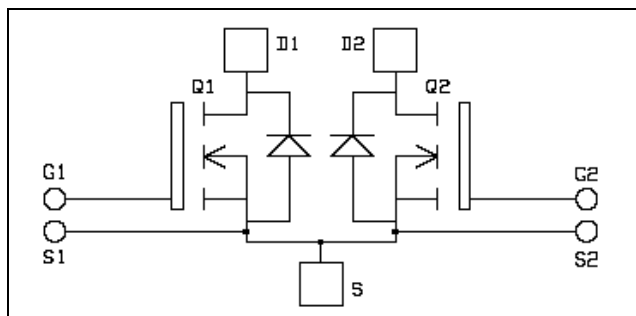


***Dual common source
MOSFET Power Module***

$$V_{DSS} = 500V$$

$$R_{DSon} = 17m\Omega \text{ max @ } T_j = 25^{\circ}C$$

$$I_D = 180A \text{ @ } T_c = 25^{\circ}C$$



Application

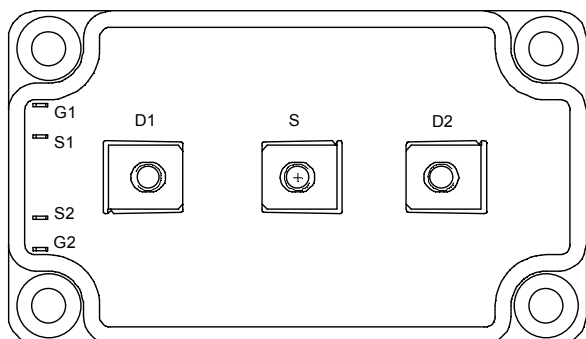
- AC Switches
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- Power MOS 7[®] MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V_{DSS}	Drain - Source Breakdown Voltage	500	V
I_D	Continuous Drain Current	$T_c = 25^{\circ}C$ 180	A
		$T_c = 80^{\circ}C$ 135	
I_{DM}	Pulsed Drain current	720	
V_{GS}	Gate - Source Voltage	± 30	V
R_{DSon}	Drain - Source ON Resistance	17	m Ω
P_D	Maximum Power Dissipation	$T_c = 25^{\circ}C$ 1250	W
I_{AR}	Avalanche current (repetitive and non repetitive)	51	A
E_{AR}	Repetitive Avalanche Energy	50	mJ
E_{AS}	Single Pulse Avalanche Energy	3000	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{GS} = 0V, I_D = 500\mu A$	500			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V, T_j = 25^\circ\text{C}$			200	μA
		$V_{GS} = 0V, V_{DS} = 400V, T_j = 125^\circ\text{C}$			1000	
$R_{DS(on)}$	Drain - Source on Resistance	$V_{GS} = 10V, I_D = 90A$			17	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 10mA$	3		5	V
I_{GSS}	Gate - Source Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$			± 200	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		28		nF
C_{oss}	Output Capacitance	$V_{DS} = 25V$		5.6		
C_{rss}	Reverse Transfer Capacitance	$f = 1MHz$		0.36		
Q_g	Total gate Charge	$V_{GS} = 10V$		560		nC
Q_{gs}	Gate - Source Charge	$V_{Bus} = 250V$		160		
Q_{gd}	Gate - Drain Charge	$I_D = 180A$		280		
$T_{d(on)}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{GS} = 15V$ $V_{Bus} = 333V$ $I_D = 180A$ $R_G = 0.5\Omega$		21		ns
T_r	Rise Time			38		
$T_{d(off)}$	Turn-off Delay Time			75		
T_f	Fall Time			93		
E_{on}	Turn-on Switching Energy ❶	Inductive switching @ 25°C $V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 180A, R_G = 0.5\Omega$		4140		μJ
E_{off}	Turn-off Switching Energy ❷			3380		
E_{on}	Turn-on Switching Energy ❶	Inductive switching @ 125°C $V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 180A, R_G = 0.5\Omega$		6224		μJ
E_{off}	Turn-off Switching Energy ❷			4052		

Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I_S	Continuous Source current (Body diode)	$T_c = 25^\circ\text{C}$			180	A
		$T_c = 80^\circ\text{C}$			135	
V_{SD}	Diode Forward Voltage	$V_{GS} = 0V, I_S = -180A$			1.3	V
dv/dt	Peak Diode Recovery ❸				8	V/ns
t_{rr}	Reverse Recovery Time	$I_S = -180A, V_R = 250V$ $di/dt = 400A/\mu s$		680		ns
Q_{rr}	Reverse Recovery Charge	$I_S = -180A, V_R = 250V$ $di/dt = 400A/\mu s$		68		μC

❶ E_{on} includes diode reverse recovery.

❷ In accordance with JEDEC standard JESD24-1.

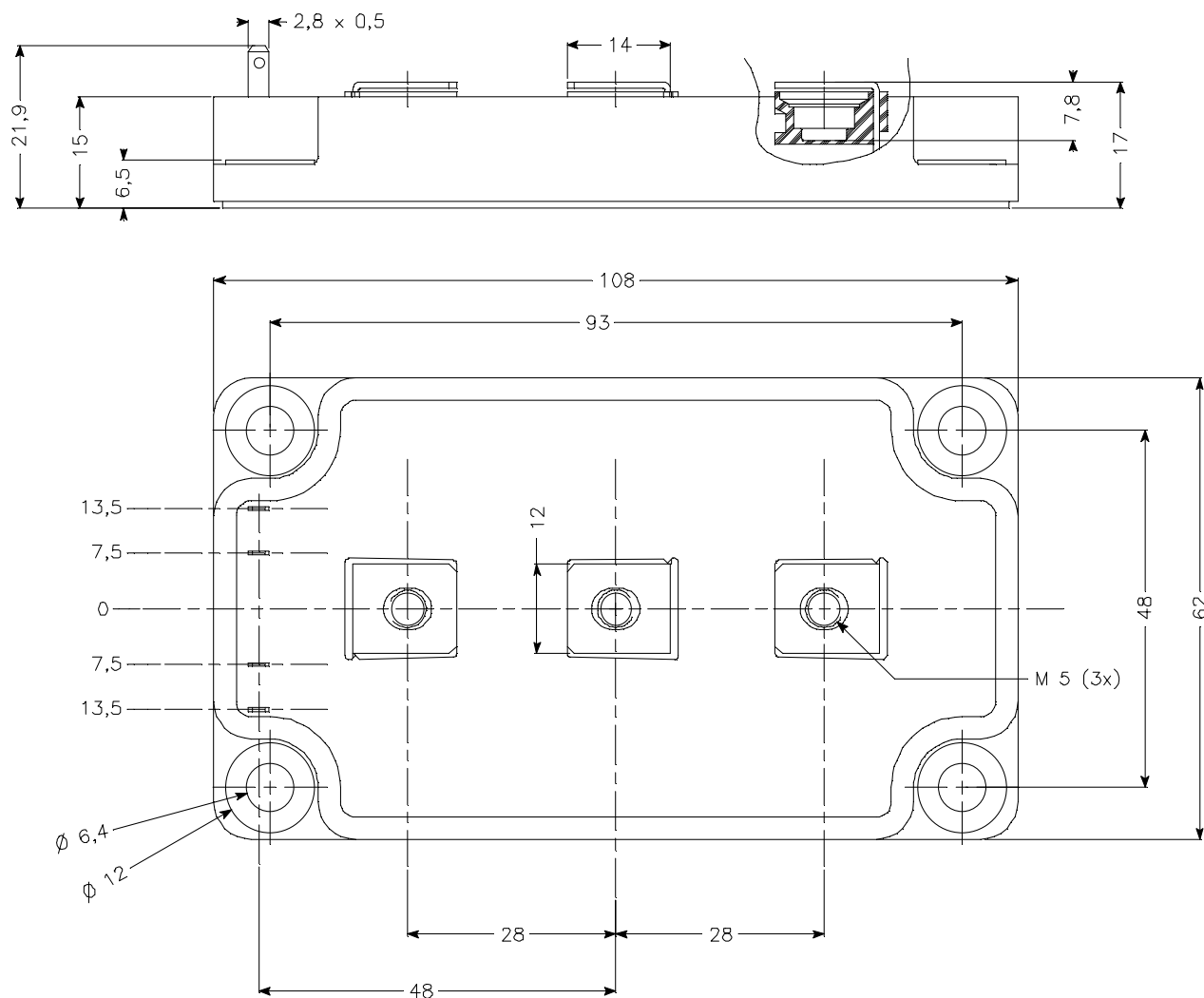
❸ dv/dt numbers reflect the limitations of the circuit rather than the device itself.

$I_S \leq -180A$ $di/dt \leq 700A/\mu s$ $V_R \leq V_{DSS}$ $T_j \leq 150^\circ\text{C}$

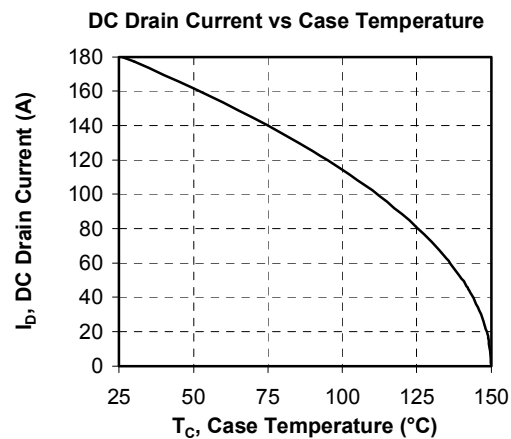
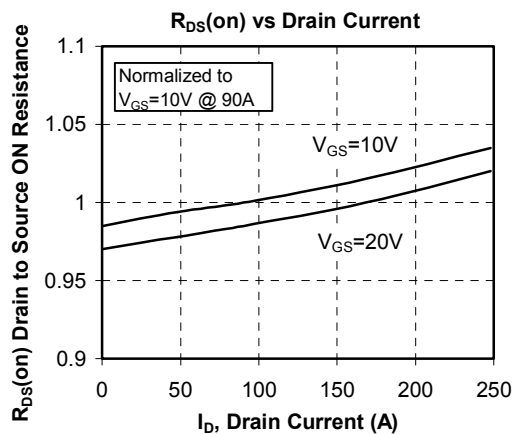
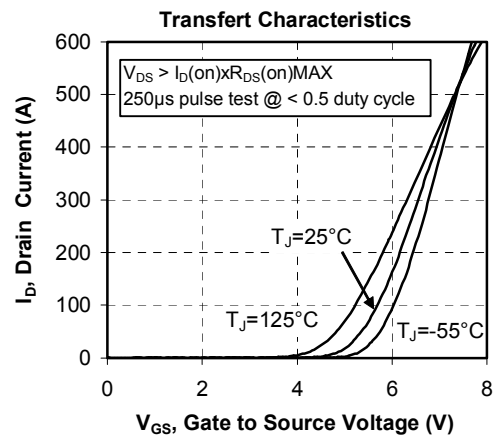
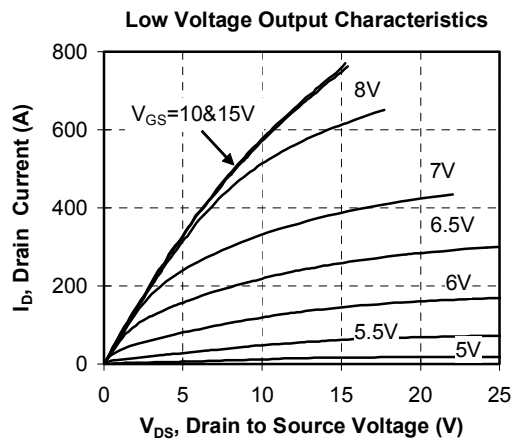
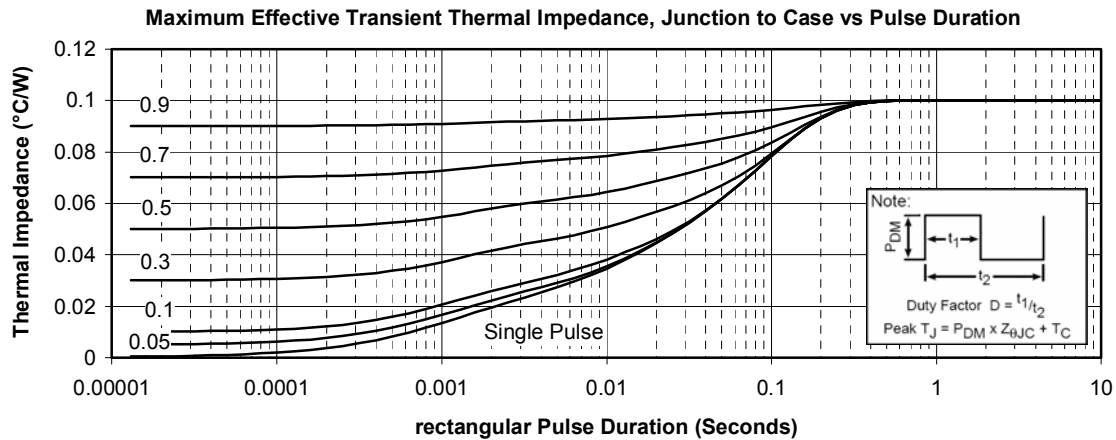
Thermal and package characteristics

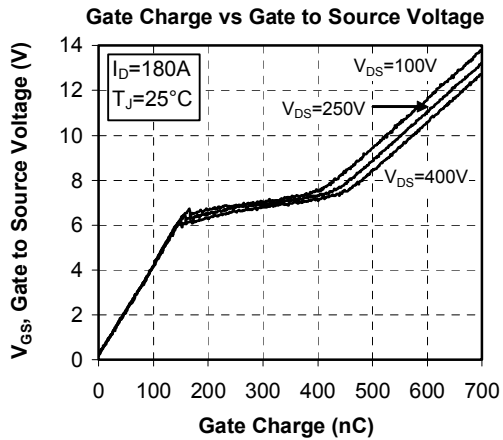
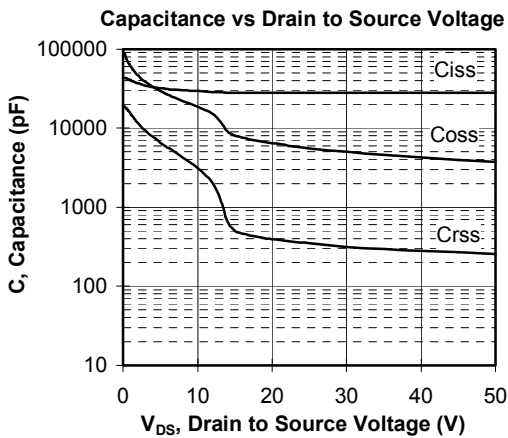
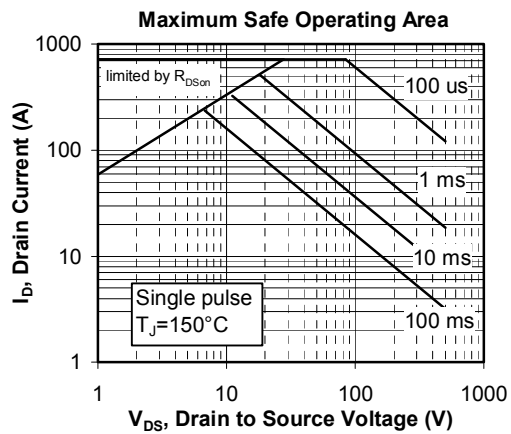
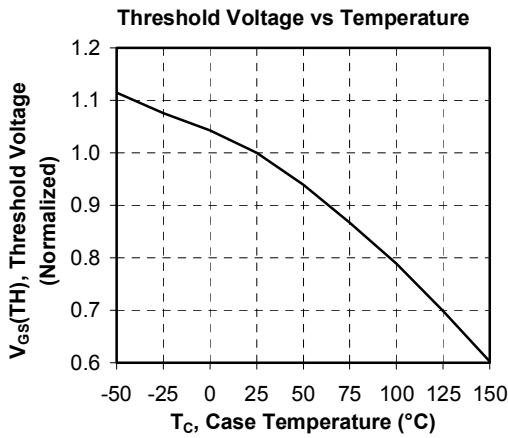
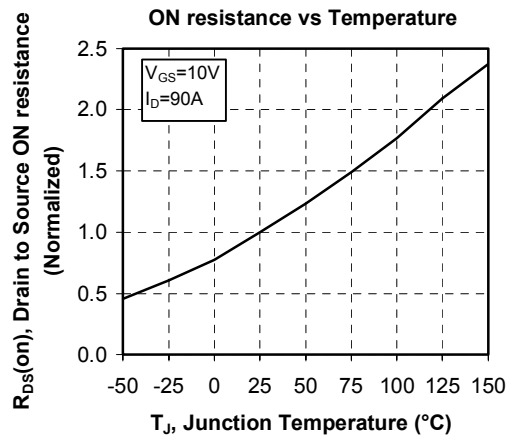
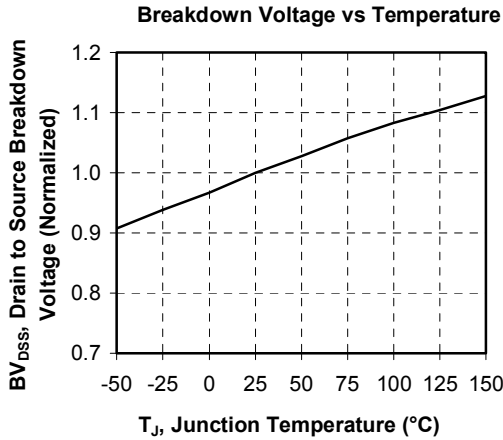
Symbol	Characteristic	Min	Typ	Max	Unit
R _{thJC}	Junction to Case			0.1	°C/W
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, I isol < 1mA, 50/60Hz	2500			V
T _J	Operating junction temperature range	-40		150	°C
T _{STG}	Storage Temperature Range	-40		125	
T _C	Operating Case Temperature	-40		100	
Torque	Mounting torque	To heatsink	M6	3	N.m
		For terminals	M5	2	
Wt	Package Weight			280	g

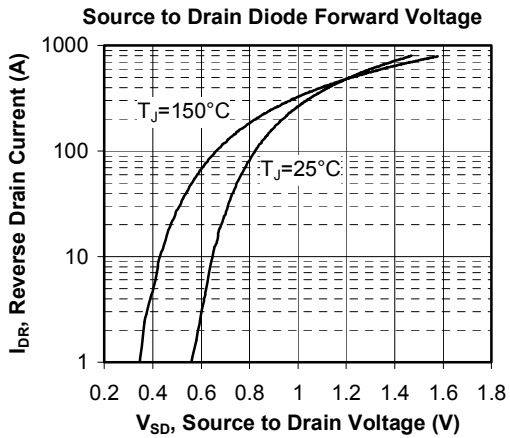
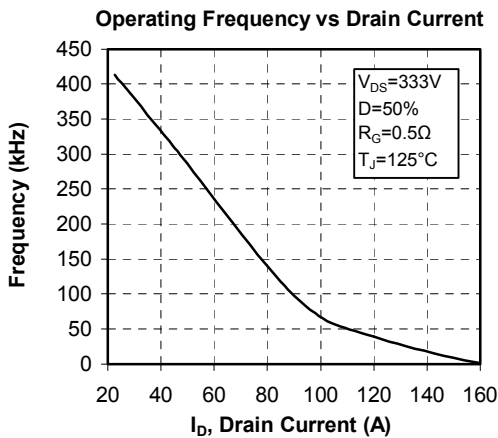
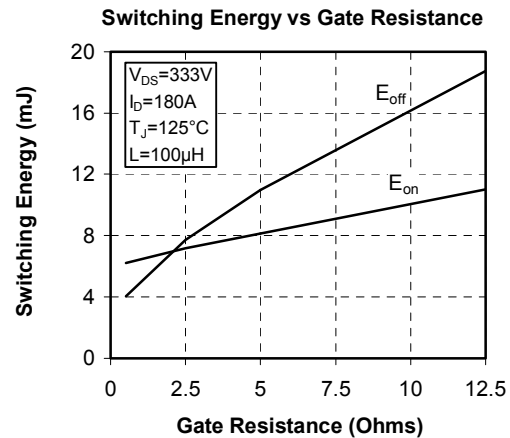
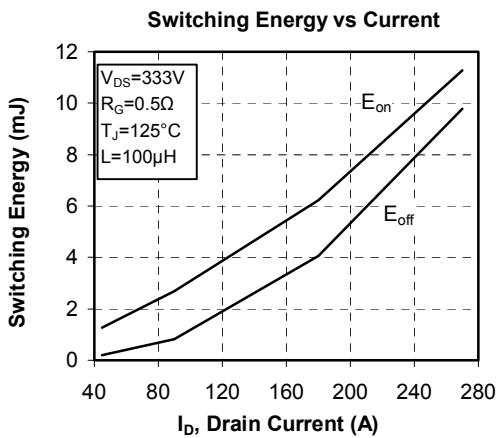
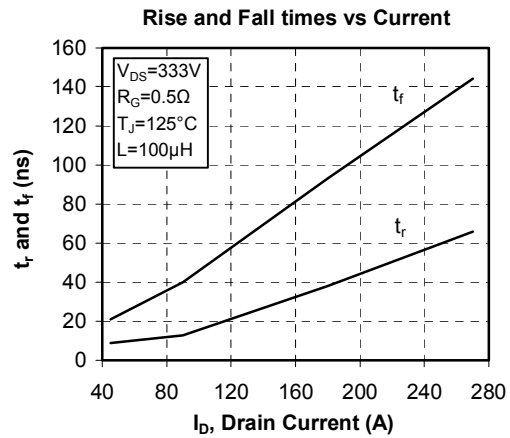
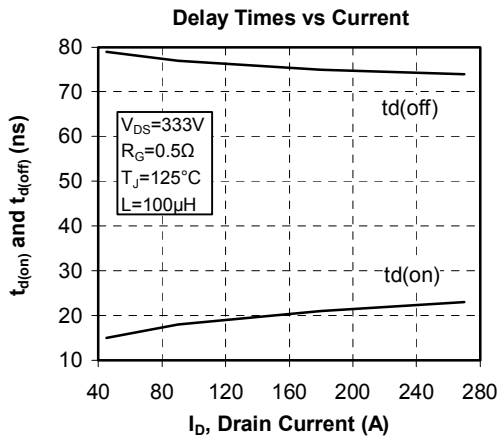
Package outline



Typical Performance Curve







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APT's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S. and Foreign patents pending. All Rights Reserved.