

isc N-Channel MOSFET Transistor

APT50M80LVFR

FEATURES

- Drain Current $-I_D = 58A @ T_C = 25^\circ C$
- Drain Source Voltage-
: $V_{DS} = 500V (Min)$
- Static Drain-Source On-Resistance
: $R_{DS(on)} = 0.08 \Omega (Max)$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

DESCRIPTION

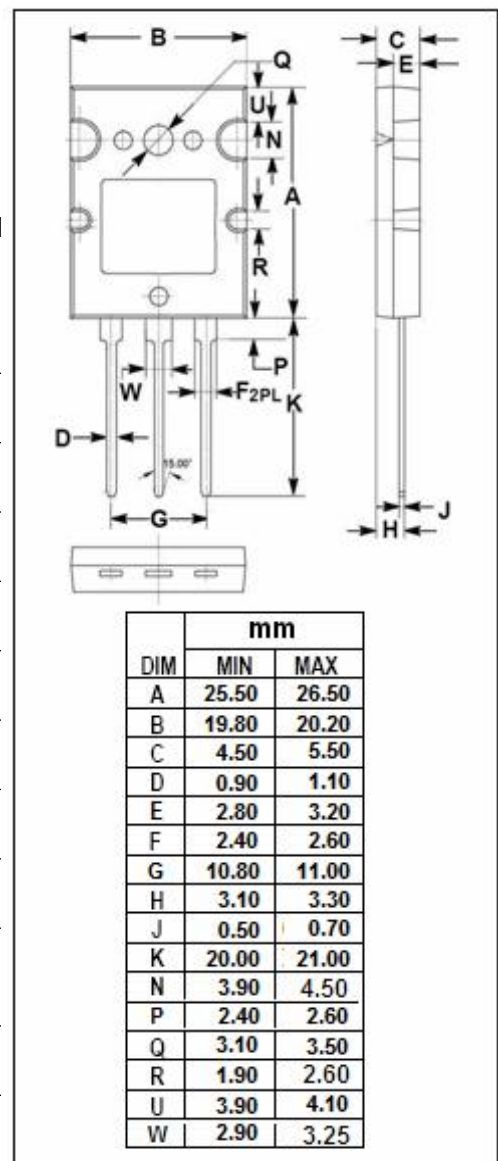
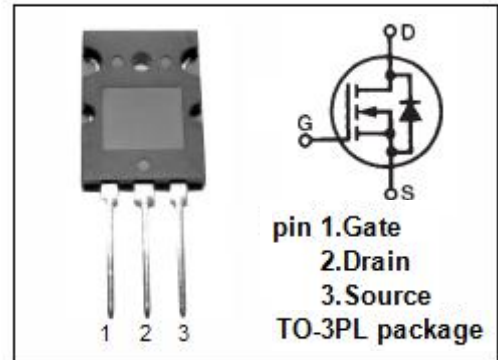
- Designed for use in switch mode power supplies and general purpose applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage-Continuous	± 30	V
I_D	Drain Current-Continuous	58	A
I_{DM}	Drain Current-Single Pluse	232	A
P_D	Total Dissipation @ $T_C = 25^\circ C$	625	W
T_J	Max. Operating Junction Temperature	-55~150	$^\circ C$
T_{stg}	Storage Temperature	-55~150	$^\circ C$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th j-c}$	Thermal Resistance, Junction to Case	0.20	$^\circ C/W$



isc N-Channel MOSFET Transistor**APT50M80LVFR****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0; I _D = 0.25mA	500		V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} ; I _D = 2.5mA	2	4	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} = 10V; I _D =29A		0.08	Ω
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±30V; V _{DS} = 0		±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500V; V _{GS} = 0 V _{DS} = 400V; V _{GS} = 0@T _C =125°C		250 1000	μA
V _{SD}	Forward On-Voltage	I _S =-58A; V _{GS} = 0		1.3	V

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