

plerow[™] APM2600-P29

Low Noise & High OIP3 Medium Power Amplifier Module

Features

- · S₂₁ = 28.7 dB @ 2500 MHz
- = 27.3 dB @ 2700 MHz
- · NF fo 2.5 dB over Frequency
- · Unconditionally Stable
- Single 5V Supply
- · High OIP3 @ Low Current

Description

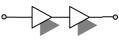
The plerow[™] APM-Series is an internally matched amplifier mini-module for such application band in SMD package with the output P1dB of 29 dBm. It is compactly designed for low current consumption and high OIP3. Integrating all the components for biasing and matching within the module enhances production yield and throughput as well. It passes through the stringent DC, RF, and reliability tests. Not sample test but 100% quality control test is made before packing.



Specifications (in Production)

Typ. @ T = 25°C, V_s = 5 V, Freq. = 2600 MHz, $Z_{o.sys}$ = 50 ohm

		-			
Parameter	Unit	Specifications			
		Min	Тур	Max	
Frequency Range	MHz	2500		2700	
Gain	dB	27	28		
Gain Flatness	dB		± 0.7	± 0.8	
Noise Figure	dB		2.5	2.6	
Output IP3 ⁽¹⁾	dBm	44	47		
S11 / S22 ⁽²⁾	dB			-18 / -12	
Output P1dB	dBm	28	29		
Switching Time (3)	μsec		-	1	
Supply Current	mA		460	500	
Supply Voltage	V		5		
Impedance	Ω	50			
Max. RF Input Power	dBm	C.W 23 ~ 25 (before fail)			
Package Type & Size	mm	Surface Mount Type, 13Wx13Lx3.8H			



2-stage Single Type

More Information

Website: www.asb.co.kr E-mail: sales@asb.co.kr

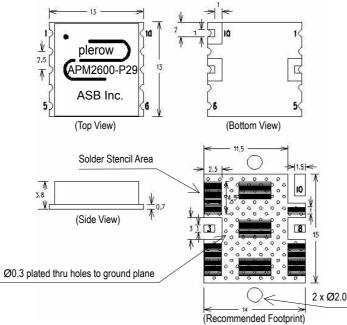
Tel: (82) 42-528-7223 Fax: (82) 42-528-7222

ASB Inc., 4th Fl. Venture Town Bldg., 367-17 Goijeong-Dong, Seo-Gu, Daejon 302-716, Korea

Operating temperature is -40°C to +85°C.

1) OIP3 is measured with two tones at an output power of 15 dBm / tone separated by 1 MHz.
2) S11/S22 (max) is the worst value within the frequency band.
3) Switching time means the time that takes for output power to get stabilized to its final level after switching DC voltage from 0 V to V_S.

Outline Drawing (Unit: mm)



Pin Number	Function		
3	RF In		
8	RF Out		
10	Vs		
Others	Ground		

Note: 1. The number and size of ground via holes in a circuit board is critical for thermal RF grounding considerations.

2. We recommend that the ground via holes be placed on the bottom of all ground pins for better RF and thermal performance, as shown in the drawing at the left side.

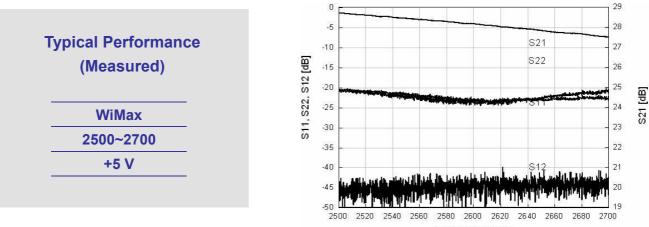
2 x Ø2.0 plated thru holes to screw on heat sinker

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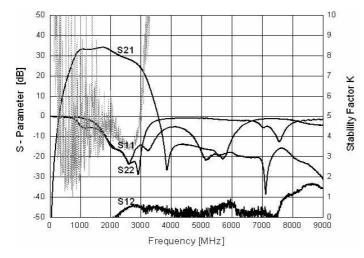
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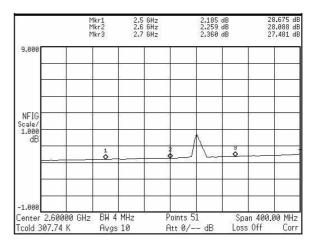
Frequency [MHz]

S-parameters

Stability Factor (K)



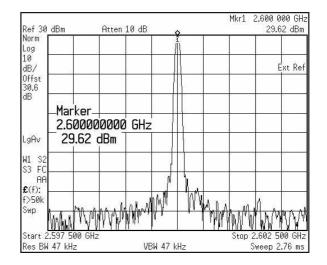
Noise Figure



OIP3

Center 2		en 16 dB 🔥			Mkr1	2.599 50	
+Samp			ŝ		_		
10 JB/			0			E:	kt Rei
Dffst 30.6 JB	Ă			A		<u>k</u>	
Start 2.597 5 #Res BW 30 k		#VB	W 3 kHz			2.602 50 Sweep 1	

P1dB



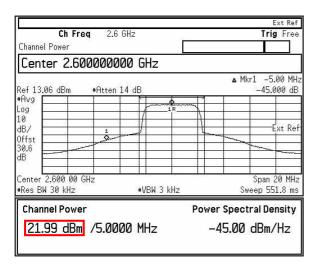


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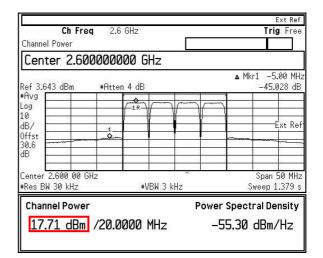
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Output Channel Power

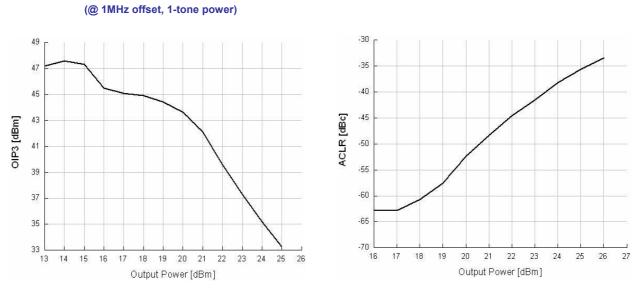
(@ ACLR=-45dBc, +/-5MHz Offset)



OIP3 vs Output Power



ACLR vs Channel Power



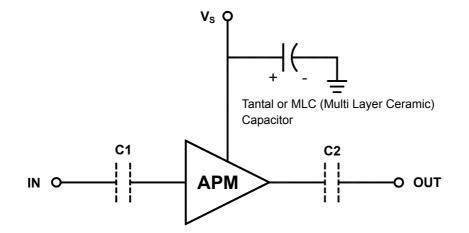
** Test Source : Agilent E4433B (3GPP W-CDMA Test Model-1 64DPCH)

www.DataSheet4U.com



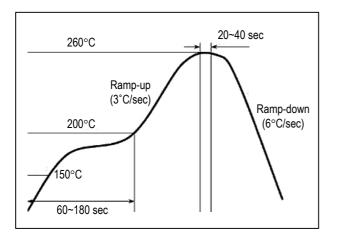
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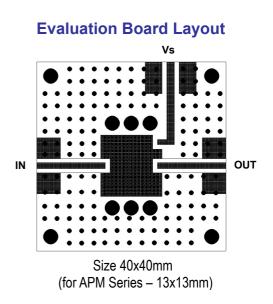
Application Circuit



- The tantal or MLC (Multi Layer Ceramic) capacitor is optional and for bypassing the AC noise introduced from the DC supply. The capacitance value may be determined by customer's DC supply status. The capacitor should be placed as close as possible to V_s pin and be connected directly to the ground plane for the best electrical performance.
- 2) DC blocking capacitors are always necessarily placed at the input and output port for allowing only the RF signal to pass and blocking the DC component in the signal. The DC blocking capacitors are included inside the APM module. Therefore, C1 & C2 capacitors may not be necessary, but can be added just in case that the customer wants. The value of C1 & C2 is determined by considering the application frequency.

Recommended Soldering Reflow Process



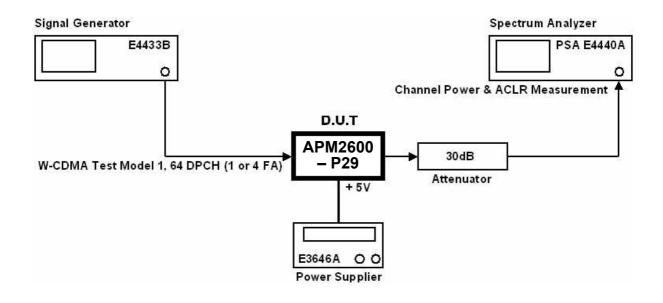


February 2009

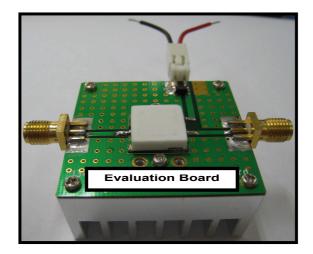


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Channel Power vs. ACLR Test Configuration



Evaluation Board attached with Heat Sink



* In order to prevent damage of D.U.T (APM-Series) from heating, you must to use a properly sized heat sink for testing a module.