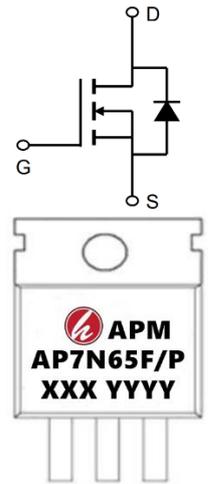


## 650V N-Channel Enhancement Mode MOSFET

### Description

The AP7N65F/P is silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.



### General Features

$V_{DS} = 650V$   $I_D = 7A$

$R_{DS(ON)} < 1.2\Omega$  @  $V_{GS}=10V$  (Type:  $1.0\Omega$ )

### Application

Uninterruptible Power Supply(UPS)

Power Factor Correction (PFC)



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP7N65F	TO-220F-3L	AP7N65F XXX YYYY	1000
AP7N65P	TO-220-3L	AP7N65P XXX YYY	1000

### Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value		Unit
		TO-220F	TO-220	
$V_{DS}$	Drain-Source Voltage ( $V_{GS} = 0V$ )	650		V
$I_D$	Continuous Drain Current	7		A
$I_{DM}$	Pulsed Drain Current (note1)	28		A
$V_{GS}$	Gate-Source Voltage	$\pm 30$		V
$E_{AS}$	Single Pulse Avalanche Energy (note2)	247		mJ
$I_{AR}$	Avalanche Current (note1)	7		A
$E_{AR}$	Repetitive Avalanche Energy note1)	18		mJ
$P_D$	Power Dissipation ( $T_C = 25^\circ C$ )	32.9		W
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	-55~+150		$^\circ C$
$R_{thJC}$	Thermal Resistance, Junction-to-Case	3.8		$^\circ C/W$
$R_{thJA}$	Thermal Resistance, Junction-to-Ambient	13.3		$^\circ C/W$

## 650V N-Channel Enhancement Mode MOSFET

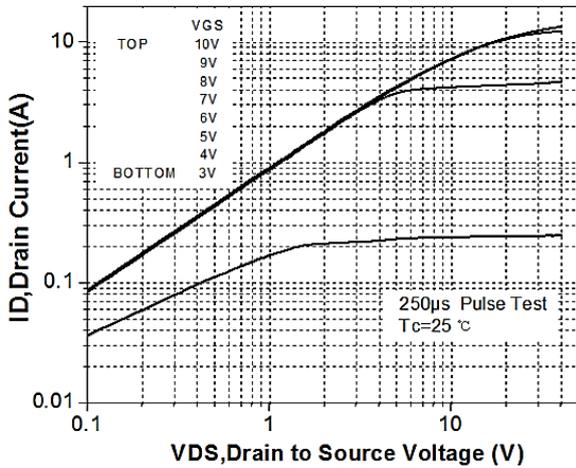
### Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	650	685	--	V
IDSS	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V, T <sub>J</sub> =25°C	--	--	1	μA
IGSS	Gate-Source Leakage	V <sub>GS</sub> = ±30V	--	--	±100	nA
VGS(th)	Gate-Source Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	--	4.0	V
RDS(on)	Drain-Source On-Resistance (Note3)	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5A	--	1.0	1.2	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1.0MHz	--	1000	--	pF
C <sub>oss</sub>	Output Capacitance		--	101	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	1.5	--	
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> =520V, I <sub>D</sub> = 7A, V <sub>GS</sub> = 10V	--	22	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	4.3	--	
Q <sub>gd</sub>	Gate-Drain Charge		--	13	--	
td(on)	Turn-on Delay Time	V <sub>DD</sub> =325V, I <sub>D</sub> = 7A, R <sub>G</sub> = 25Ω	--	12	--	ns
t <sub>r</sub>	Turn-on Rise Time		--	26	--	
td(off)	Turn-off Delay Time		--	29	--	
t <sub>f</sub>	Turn-off Fall Time		--	27	--	
I <sub>S</sub>	Continuous Body Diode Current	T <sub>C</sub> = 25 °C	--	--	7.0	A
I <sub>SM</sub>	Pulsed Diode Forward Current		--	--	28	A
V <sub>SD</sub>	Body Diode Voltage	T <sub>J</sub> = 25°C, I <sub>SD</sub> = 7A, V <sub>GS</sub> = 0V	--	--	1.4	V
trr	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 7A, di <sub>F</sub> /dt = 100A/μs	--	389	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	2.04	--	μC

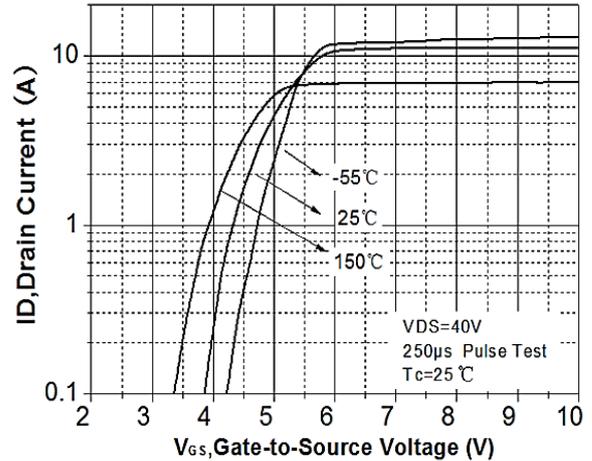
#### Note :

- 1、 The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2、 The EAS data shows Max. rating . I<sub>AS</sub> = 4.5A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 Ω, Starting T<sub>J</sub> = 25 °C
- 3、 The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

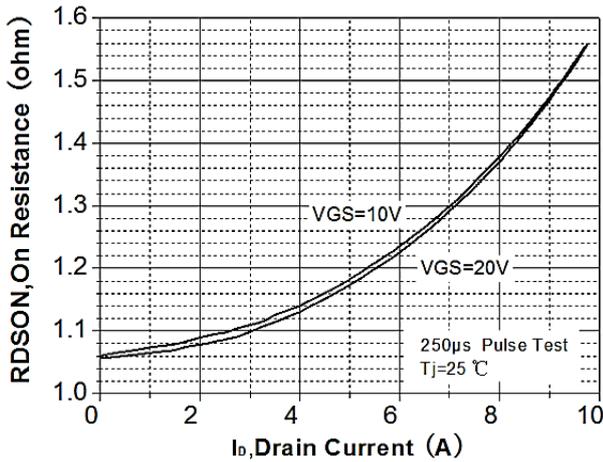
**Typical Characteristics**



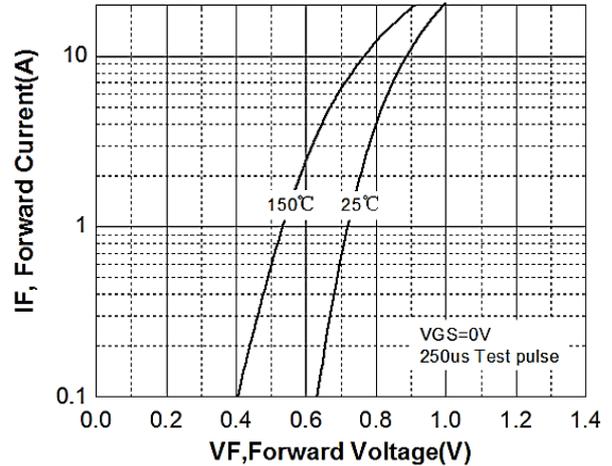
**Figure 1. On-Region Characteristics**



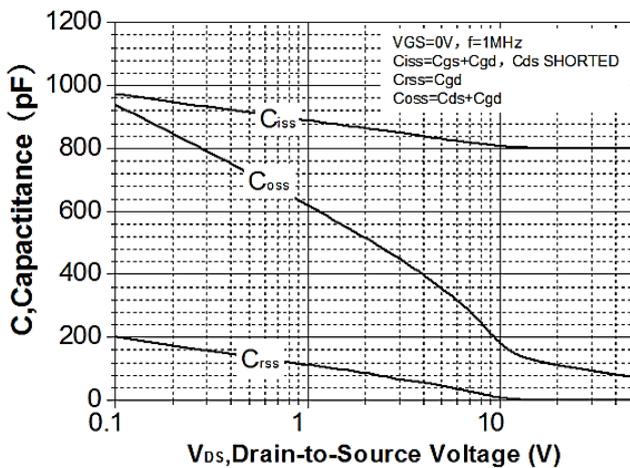
**Figure 2. Transfer Characteristics**



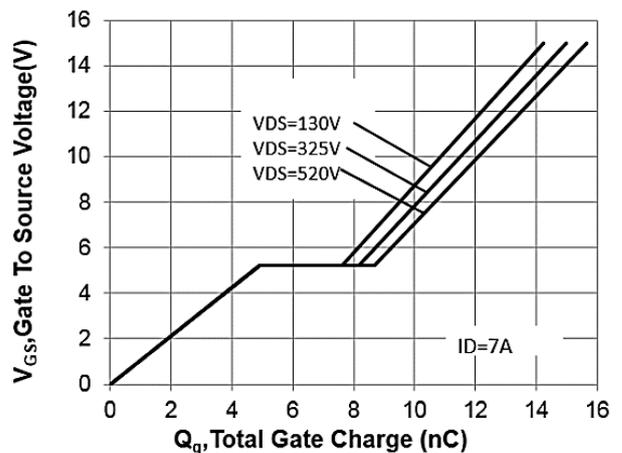
**Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage**



**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**

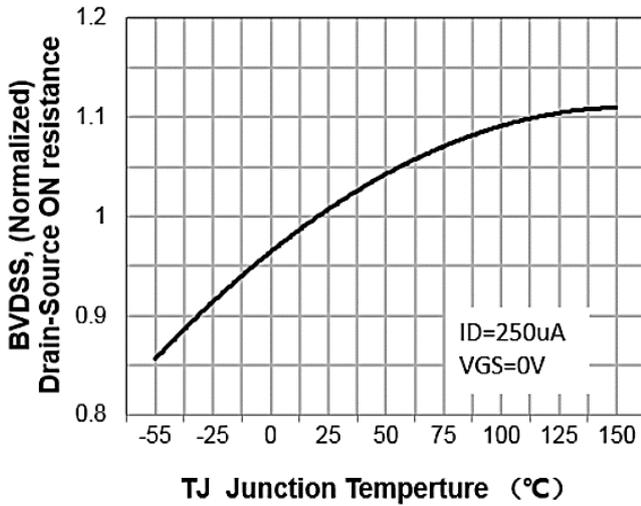


**Figure 5. Capacitance Characteristics**

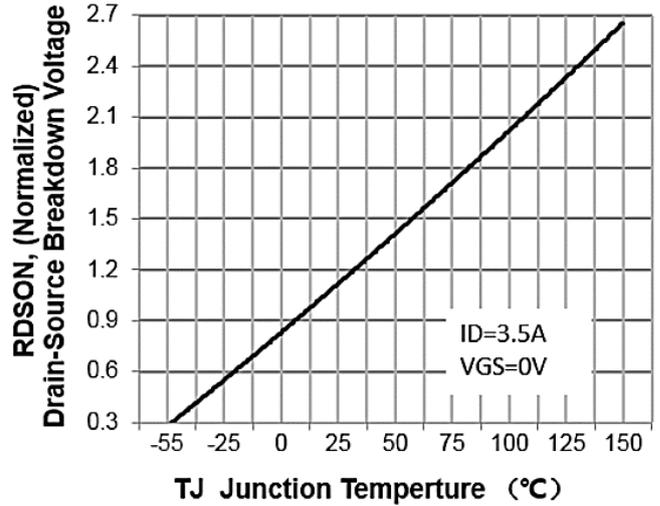


**Figure 6. Gate Charge Characteristics**

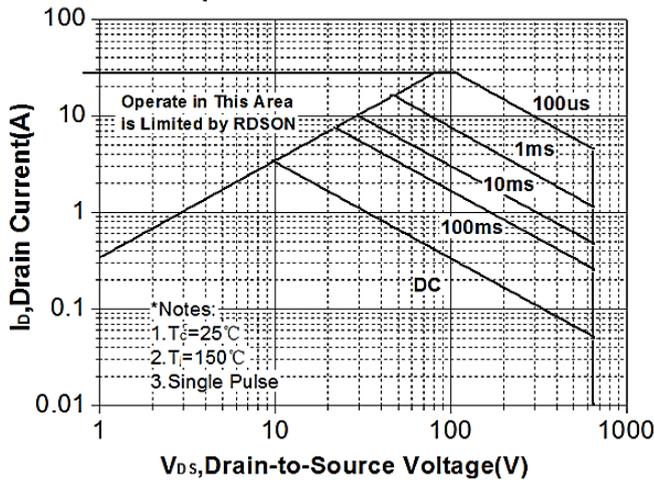
**650V N-Channel Enhancement Mode MOSFET**



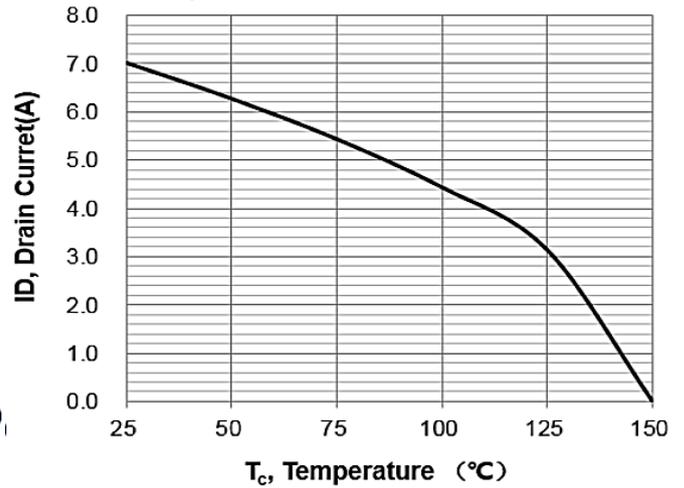
**Figure 7. Breakdown Voltage Variation vs Temperature**



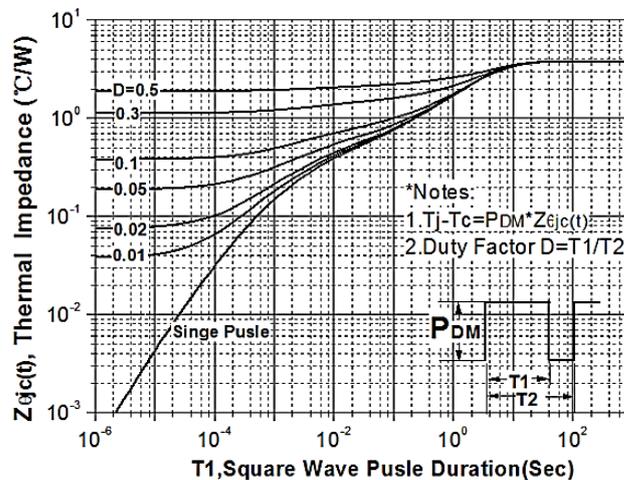
**Figure 8. On-Resistance Variation vs Temperature**



**Figure 9. Maximum Safe Operating Area**



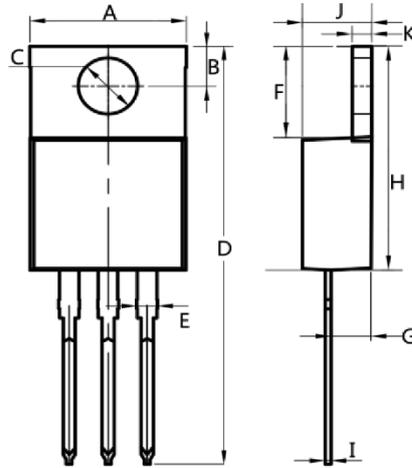
**Figure 10. Maximum Drain Current vs Case Temperature**



**Figure 11. Transient Thermal Response Curve**

## 650V N-Channel Enhancement Mode MOSFET

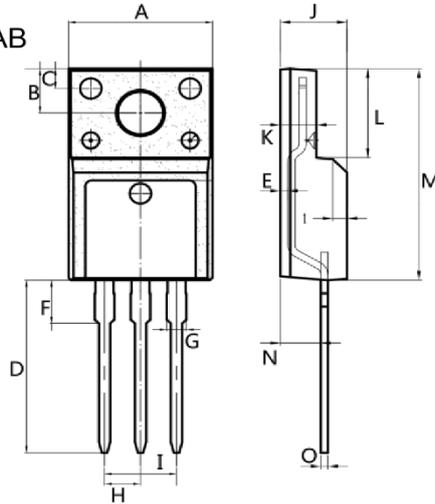
TO-220AB



Dim.	Min.	Max.
A	10.0	10.4
B	2.5	3.0
C	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
H	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4

All Dimensions in millimeter

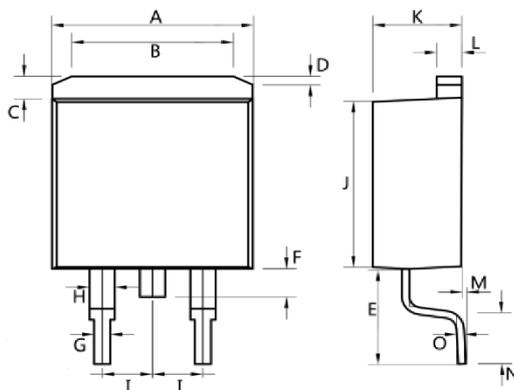
ITO-220AB



Dim.	Min.	Max.
A	9.9	10.3
B	2.9	3.5
C	1.15	1.45
D	12.75	13.25
E	0.55	0.75
F	3.1	3.5
G	1.25	1.45
H	Typ 2.54	
I	Typ 5.08	
J	4.55	4.75
K	2.4	2.7
L	6.35	6.75
M	15.0	16.0
N	2.75	3.15
O	0.45	0.60

All Dimensions in millimeter

TO-263



Dim.	Min.	Max.
A	10.0	10.5
B	7.25	7.75
C	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.75	0.95
H	1.15	1.35
I	Typ 2.54	
J	8.4	8.6
K	4.4	4.6
L	1.25	1.45
M	0.02	0.1
N	2.4	2.8
O	0.35	0.45

All Dimensions in millimeter

**650V N-Channel Enhancement Mode MOSFET****Attention**

1, Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.

2, APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.

3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the APM Microelectronics product that you intend to use.

**650V N-Channel Enhancement Mode MOSFET**

<b>Edition</b>	<b>Date</b>	<b>Change</b>
Rve1.0	2018/1/31	Initial release
Rve1.1	2021/1/05	Reduce RDS(on)

**Copyright Attribution“APM-Microelectronice”**

