

Description

The AP420N03SLG5 uses advanced **APM-SGT V** technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

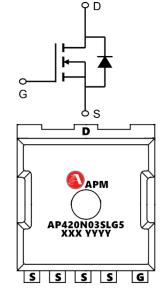
 $V_{DS} = 30V I_{D} = 420A$

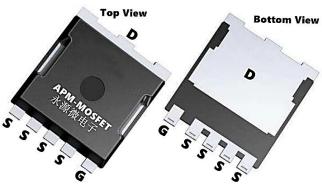
 $R_{DS(ON)} < 0.8 m\Omega$ @ $V_{GS}=10V$ (Type: $0.55 m\Omega$)

Application

Buck

Boost





Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP420N03SLG5	STOLL-6L	AP420N03SLG5 XXX YYYY	2000

Absolute Maximum Ratings (T_C=25[°]Cunless otherwise noted)

Symbol	Parameter	Max.	Units
VDSS	Drain-Source Voltage	30	V
VGSS	Gate-Source Voltage	±20	V
ID@TC=25°C	Continuous Drain Current, VGS @ 10V1	420	Α
ID@TC=100°C	Continuous Drain Current, VGS @ 10V1	307	Α
IDM	Pulsed Drain Current	1312	А
EAS	Single Pulsed Avalanche Energy	845	mJ
IAS	Avalanche Current	125	А
PD@TC=25°C	Power Dissipation	160	W
TJ TSTG	Operating Junction Temperature Range	-55 to 150	°C
R _θ JA	Thermal Resistance Junction-Ambient ¹	25	°C/W
RθJC	Thermal Resistance, Junction to Case	0.78	°C/W

AP420N03SLG5

30V N-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	30	-	-	V
IGSS	Gate-body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
IDOO	Zero Gate Voltage Drain Current T _J =25°C	.,	-	-	1	•
IDSS	Zero Gate Voltage Drain Current T _J =100°C V _{DS} = 30V, V _{GS} = 0V	$V_{DS} = 30V$, $V_{GS} = 0V$	-	-	100	μA
VGS(th)	Gate-Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2	1.6	2.5	V
DDC()	Dunin Course On Besister and	V _{GS} = 10V, I _D = 20A	-	0.55	0.80	mΩ
RDS(on)	Drain-Source On-Resistance ⁴	V _{GS} = 4.5V, I _D =10A	-	0.90	1.2	
gfs	Forward Transconductance ⁴	V _{DS} = 10V, I _D = 20A	-	110	-	S
Ciss	Input Capacitance		-	6790	-	
Coss	Output Capacitance	V_{DS} = 15V, V_{GS} =0V, f =1MHz	-	2450	-	pF
Crss	Reverse Transfer Capacitance	1 – 11VII 12	-	220	-	
Rg	Gate Resistance	f = 1MHz	-	2.2	-	Ω
Qg	Total Gate Charge		-	109.3	-	
Qgs	Gate-Source Charge	$V_{GS} = 10V, V_{DS} = 15V,$ $I_{D} = 20A$	-	20.8	-	nC
Qgd	Gate-Drain Charge	ID- ZOA	-	15.2	-	
td(on)	Turn-On Delay Time		-	12	-	
t _r	Rise Time	$V_{GS} = 10V, V_{DD} = 15V,$	-	12.3	-	
td(off)	Turn-Off Delay Time	$R_G = 3\Omega$, $I_D = 20A$	-	88.4	-	ns
t _f	Fall Time		-	42.8		
trr	Body Diode Reverse Recovery Time	I _F =20A, dl/dt=100A/µs	-	72	-	ns
Qrr	Body Diode Reverse Recovery Charge	1F-20A, α//α(-100A/μ3	-	36	-	nC
VSD	Diode Forward Voltage ⁴	$I_S = 20A$, $V_{GS} = 0V$	-	-	1.2	V
IS	Continuous Source Current	T _C =25°C	-	-	240	Α

Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2_{\times} The data tested by pulsed , pulse width $\, \leqq \, 300 \text{us}$, duty cycle $\, \leqq \, 2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD =25V,VGS =10V,L=0.1mH,IAS =125A
- 4. The power dissipation is limited by 150 $^{\circ}\mathrm{C}$ junction temperature
- 5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



Typical Characteristics

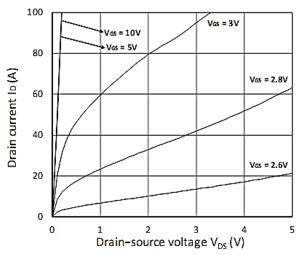
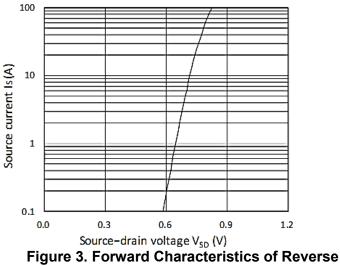


Figure 1. Output Characteristics



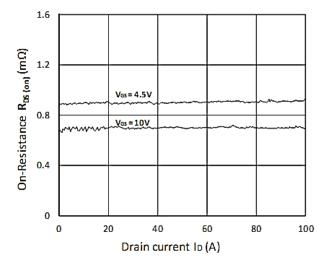


Figure 5. RDS(ON) vs. ID

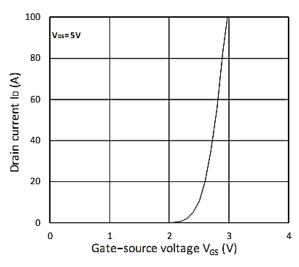


Figure 2. Transfer Characteristics

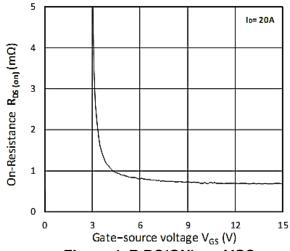


Figure 4. R DS(ON) vs. VGS

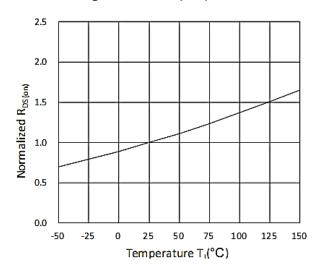
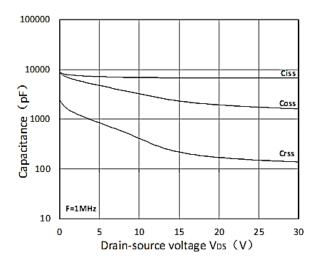


Figure 6. Normalized RDS(on) vs. Temperature





Vos = 15V



8 lo= 20A | Son of the state of

Figure 7. Capacitance Characteristics

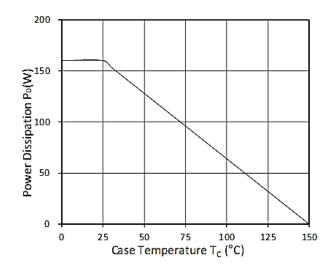


Figure 8. Gate Charge Characteristics

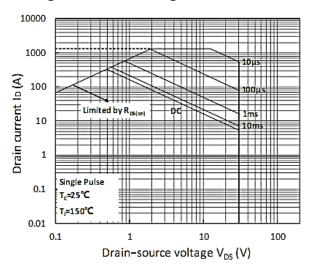
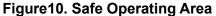


Figure 9. Power Dissipation



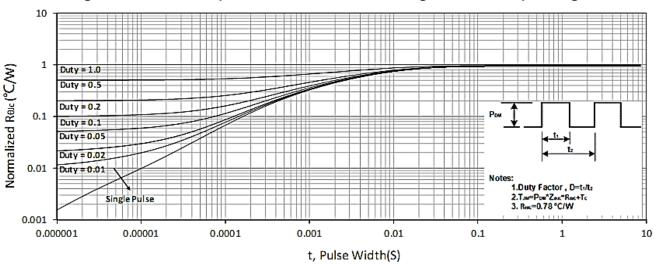
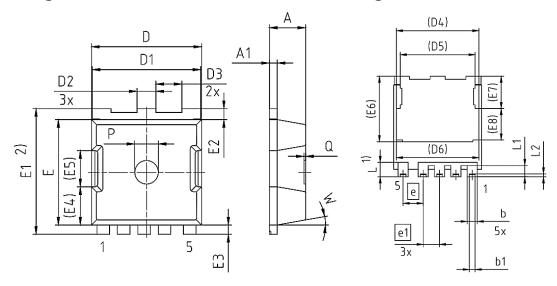


Figure 9 Normalized Maximum Transient Thermal Impedance



Package Mechanical Data-STOLL-6L-JJ Single



Symbol	Dimensions In Millimeters			
Syllibol	Min.	Max.		
Α	2.2	2.4		
A1	0.40	0.60		
b	0.70	0.90		
b1	0.42	0.50		
D	6.80	7.20		
D1	6.80	7.00		
D2	1.10	1.30		
D3	1.55	1.75		
D4	6.56			
D5	5.96			
D6	5.60			
E	6.50	6.90		
E1	7.80	8.20		
E2	0.60	0.80		
E3	0.50	0.70		
E4	2.	2.43		
E5	2.30			
E6	5.20			
E7	2.57			
E8	2.50			
е	1.60			
e1	1.30			
L	1.05	1.25		
L1	0.80	1.00		
L2	0.13	0.33		
Р	1.40	1.60		
Q	0.00	0.10		
W	8.50°	11.50°		



AP420N03SLG5

30V N-Channel Enhancement Mode MOSFET

Attention

- 1,Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.
- 2,APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.
- 3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. Whendesigning equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- 5,In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.
- 7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- 8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "DeliverySpecification" for the APM Microelectronics product that you Intend to use.





AP240N03NF

30V N-Channel Enhancement Mode MOSFET

Edition	Date	Change
REV1.0	2023/3/31	Initial release

Copyright Attribution"APM-Microelectronices"