

-100V P-Channel Enhancement Mode MOSFET

Description

The AP35P10D uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} = -100V I_D =-35A

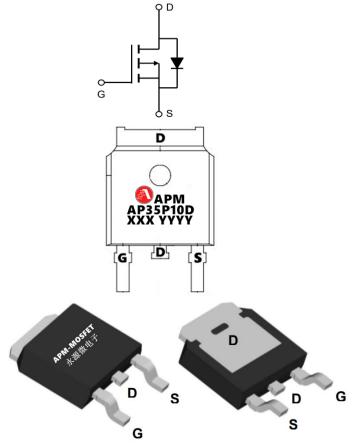
 $R_{DS(ON)} < 52m\Omega @ V_{GS} = 10V (Type: 40m\Omega)$

Application

Brushless motor

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS) 2500	
AP35P10D	TO-252-3L	AP35P10D XXX YYYY		
solute Maximur	n Ratings (Tc=25℃unless otherwise not	ed)		
Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	-100	V	
VGS	Gate-Source Voltage	±20	V	
I₀@Tc=25℃	Continuous Drain Current, V _{GS} @ -10V ¹	-35	А	
I₀@Tc=100℃	Continuous Drain Current, V _{GS} @ -10V ¹	-19	А	
IDM	Pulsed Drain Current ²	-110	А	
EAS	Single Pulse Avalanche Energy ³	360	mJ	
IAS	Avalanche Current	-35	А	
P _D @T _C =25℃	Total Power Dissipation ⁴	119	W	
TSTG	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
R _θ JA	Thermal Resistance Junction-Ambient ¹	62.5	°C/W	
ReJC Thermal Resistance Junction-Case ¹		1.35	°C/W	



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Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250µA	-100	-	-	V	
IGSS	Gate-body Leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA	
IDSS TJ=25°C		N 400XXXX 0XX	-	-	-1	μA	
IDSS TJ=100°C	Zero Gate Voltage Drain Current	V_{DS} = -100V, V_{GS} = 0V	-	-	-100		
VGS(th)	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D = -250µA		-1.7	-2.4	V	
RDS(on)	Drain-Source on-Resistance ⁴	V _{GS} = -10V, I _D = -20A	- 40 52		52		
	Drain-Source on-Resistance	V _{GS} = -4.5V, I _D = -10A	-	43	55	mΩ	
gfs	Forward Transconductance ⁴	ansconductance ⁴ $V_{DS} = -10V, I_D = -20A$		44	-	S	
Ciss	Input Capacitance		-	5870	-	pF	
Coss	Output Capacitance	V _{DS} = -50V, V _{GS} =0V, f =1MHz	-	135	-		
Crss	Reverse Transfer Capacitance	1 111112	-	106	-		
Rg	Gate Resistance	f =1MHz	-	9.5	-	Ω	
Qg	Total Gate Charge		-	126	-	nC	
Qgs	Gate-Source Charge	$V_{GS} = -10V, V_{DS} = -50V, I_{D} = -20A$	-	13.9	-		
Qgd	Gate-Drain Charge	20/1	-	13	-		
td(on)	Turn-on Delay Time		-	15	-		
tr	Rise Time	V _{GS} =-10V, V _{DD} = -50V, R _G =		42	-		
td(off)	Turn-off Delay Time	3 Ω, I _D = -20A	-	68	-	ns	
t _f	Fall Time		-	38	-		
VSD	Diode Forward Voltage ⁴	I _S = -20A, V _{GS} = 0V	-	-	-1.2	V	
IS	Continuous Source Current T _C =25°C	-	-	-	-30	Α	

Electrical Characteristics (TJ =25°C, unless otherwise noted)

Note :

 1_{\times} The data tested by surface mounted on a 1 inch 2 $\,$ FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%

3、The EAS data shows Max. rating . The test condition is V DD =-25V,V GS =-10V,L=0.1mH,IAS =-35A

 $4\,{\scriptstyle \smallsetminus}\,$ The power dissipation is limited by $150\,{\rm ^\circ C}$ junction temperature

5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.

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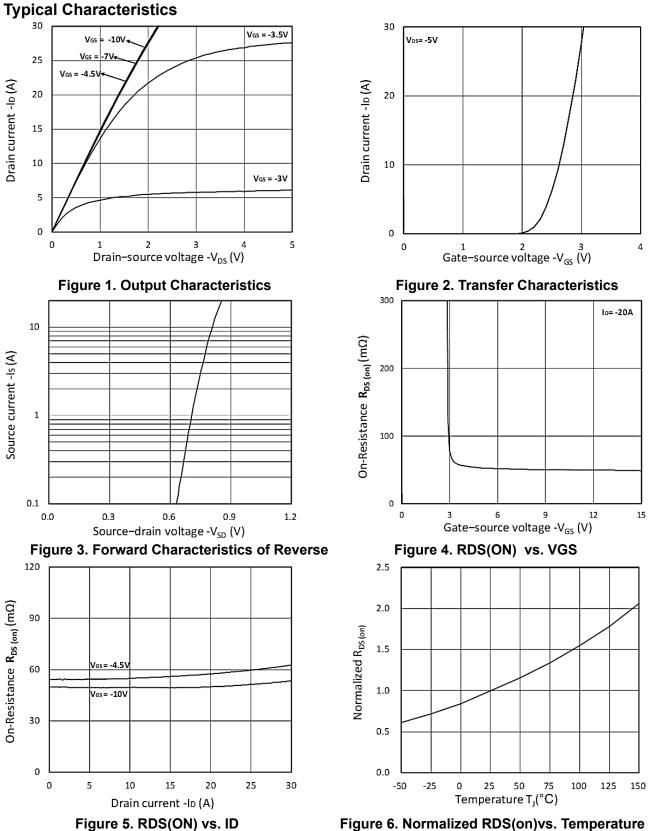


Figure 6. Normalized RDS(on)vs. Temperature



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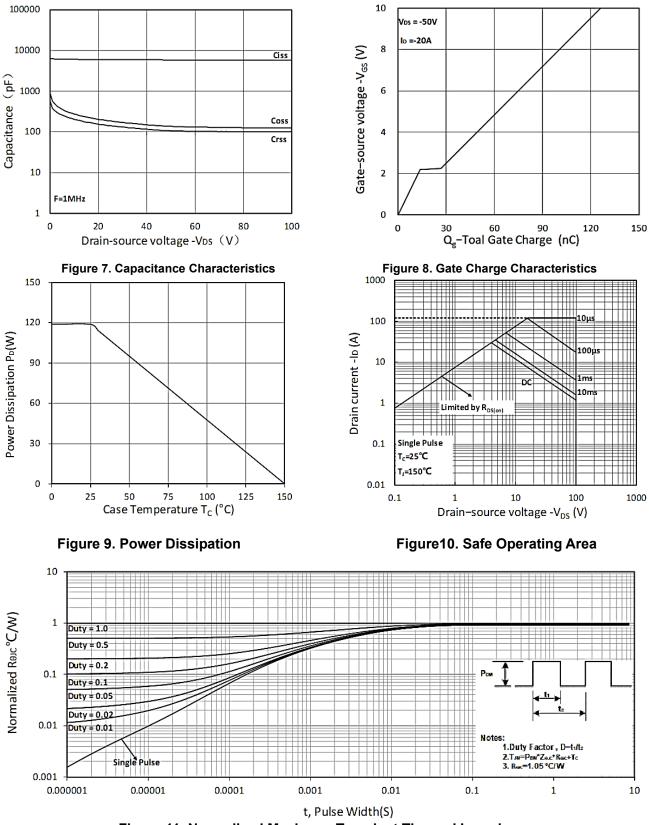
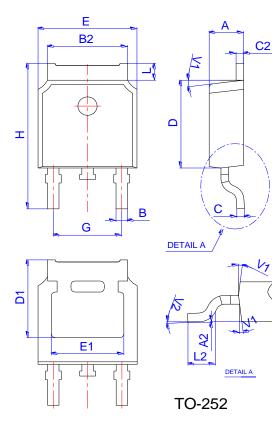


Figure 11. Normalized Maximum Transient Thermal Impedance



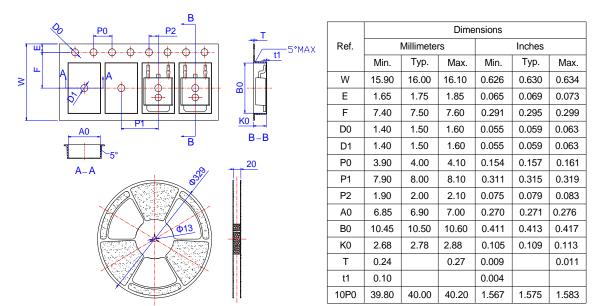
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Package Mechanical Data:TO-252-3L



	Dimensions					
Ref.	Millimeters		Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
В	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
С	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF		0.209REF			
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
Н	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Spectification-TO-252



AP35P10D REV1.1

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Edition	Date	Change
REV1.0	2023/4/13	Initial release
REV1.1	2023/11/21	Reduce RDS

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