

30V N-Channel Enhancement Mode MOSFET

Description

The AP3414MI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} = 30V I_D =18A

 $R_{DS(ON)} < 12m\Omega @ V_{GS}=10V$ (Type: 9.0m Ω)

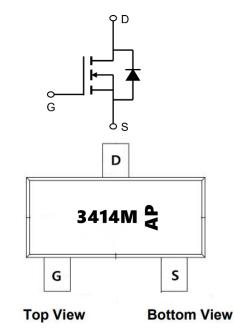
 $R_{\text{DS(ON)}} < 13 \text{m}\Omega \text{ (}V_{\text{GS}}\text{=}10 \text{V} \text{ (}\text{Type: }11 \text{m}\Omega\text{)}$

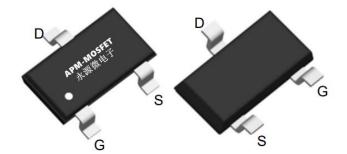
Application

VBUS

Wireless impact

Mobile phone fast charging





Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP3414MI	SOT23-3L	3414M AP	5000

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I⊳@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V ¹	18	А
I⊳@Tc=100°C	Continuous Drain Current, V _{GS} @ 10V ¹	9	А
IDM	Pulsed Drain Current ²	54	А
P₀@T₀=25℃	Total Power Dissipation ⁴	29	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient ¹	125	°C/W
R₀JC	Thermal Resistance Junction-Case ¹	4.32	°C/W



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Electrical Characteristics (Tc=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30	33		V
RDS(ON)	RDS(ON) Static Drain-Source On-Resistance ² V _{GS} =10V,	V _{GS} =10V , I _D =12A		9.0	12	mΩ
KD3(ON)		V _{GS} =4.5V , I _D =10A		11	13	11152
VGS(th)	Gate Threshold Voltage		1.0	1.6	2.5	V
∆VGS(th)	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D = 250 uA$		-5.8		mV/°C
IDSS	Drain-Source Leakage Current	V_{DS} =24V , V_{GS} =0V , T_{J} =25°C			1	uA
1000	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			5	
IGSS	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		9.8		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			12.8		nC
Qgs	Gate-Source Charge	V _{DS} =20V , V _{GS} =4.5V , I _D =12A		3.3		
Qgd	Gate-Drain Charge			6.5		
Td(on)	Turn-On Delay Time			4.5		
Tr	Rise Time	V _{DD} =12V , V _{GS} =10V ,		10.8		
Td(off)	Turn-Off Delay Time	R _G =3.3Ω I _D =5A		25.5		ns
Tf	Fall Time			9.6		
Ciss	Input Capacitance			1317		
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		163		pF
Crss	Reverse Transfer Capacitance			131		
IS	Continuous Source Current ^{1,6}				46	А
ISM	Pulsed Source Current ^{2,6}	$V_G=V_D=0V$, Force Current			92	А
VSD	Diode Forward Voltage ²	V _{GS} =0V , Is=1A , T」=25℃			1	V

Note :

 $1_{\mbox{\tiny V}}$ The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width $\leq 300 us$, duty cycle $\leq 2\%$

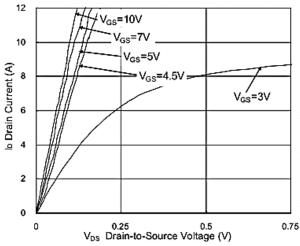
 $3\,{\scriptstyle \sim}\,$ The power dissipation is limited by 150°C junction temperature

4、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

N



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Typical Characteristics



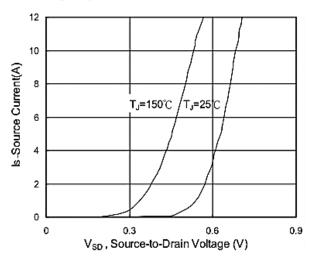


Fig.3 Forward Characteristics of Reverse

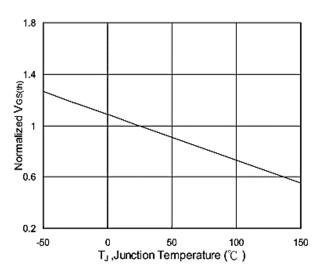


Fig.5 Normalized V_{GS(th)} vs. T_J

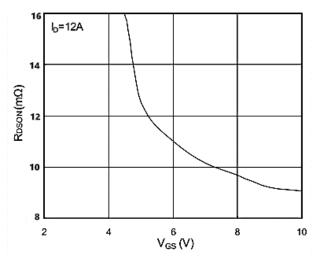


Fig.2 On-Resistance vs. G-S Voltage

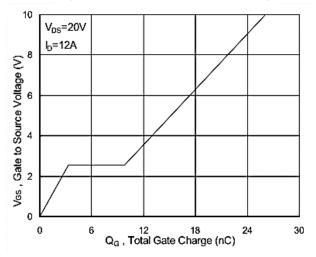
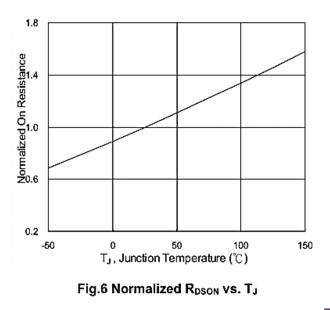


Fig.4 Gate-Charge Characteristics



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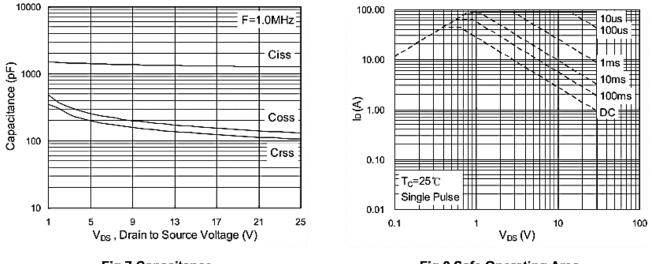


Fig.7 Capacitance

Fig.8 Safe Operating Area

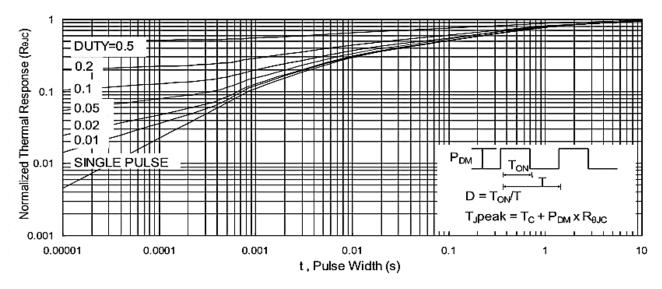


Fig.9 Normalized Maximum Transient Thermal Impedance

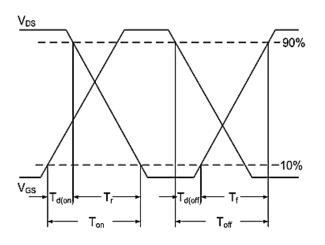


Fig.10 Switching Time Waveform

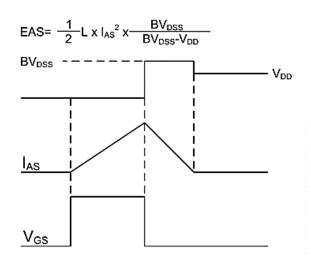
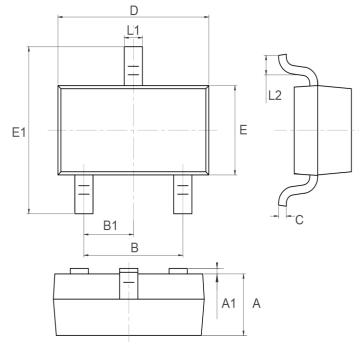


Fig.11 Unclamped Inductive Switching Waveform



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Package Mechanical Data-SOT23-3L-Single



Symbol	Dim in mm			
Symbol	Min	Тур	Мах	
A	1	1.1	1.2	
A1	0	0.05	0.1	
В	1.8	1.9	2	
B1	0.95TYP			
С	0.1	0.15	0.2	
D	2.82	2.92	3.02	
E	1.5	1.6	1.7	
E1	2.65	2.8	2.95	
L1	0.3	0.4	0.5	
L2	0.3	0.45	0.6	



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Edition	Date	Change
REV1.0	2023/10/10	Initial release

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