

## -100V P-Channel Enhancement Mode MOSFET

### Description

The AP30P10P uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### General Features

$V_{DS} = -100V$   $I_D = -30A$

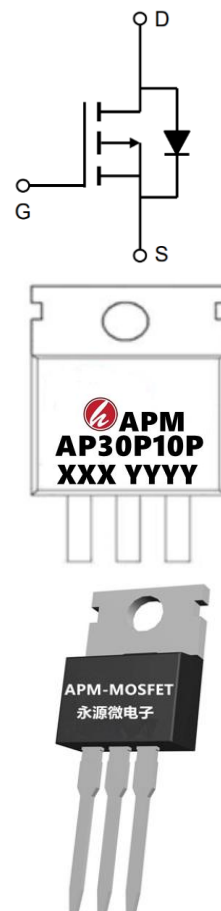
$R_{DS(ON)} < -110m\Omega$  @  $V_{GS} = -10V$

### Application

Battery protection

Load switch

Uninterruptible power supply



### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP30P10P	TO-220-3L	AP30P10P XXX YYYYY	1000

### Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-30	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-16	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-75	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	157.2	mJ
$I_{AS}$	Avalanche Current	25	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation <sup>4</sup>	96	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	1.3	$^\circ C/W$

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### Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)

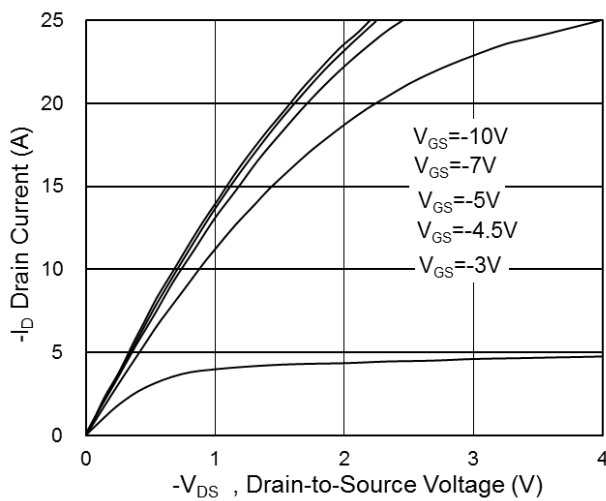
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=-250\mu A$	-100	---	---	V
RDS(ON)	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10V$ , $I_D=-10A$	---	78	95	m $\Omega$
		$V_{GS}=-4.5V$ , $I_D=-8A$	---	86	110	
VGS(th)	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu A$	-1.2	-1.7	-2.5	V
IDSS	Drain-Source Leakage Current	$V_{DS}=-100V$ , $V_{GS}=0V$ , $T_J=25^{\circ}\text{C}$	---	---	-50	$\mu A$
IGSS	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
gfs	Forward Transconductance	$V_{DS}=-10V$ , $I_D=-10A$	---	24	---	S
Qg	Total Gate Charge	$V_{DS}=-50V$ , $V_{GS}=-10V$ , $I_D=-20A$	---	44.5	---	nC
Qgs	Gate-Source Charge		---	9.13	---	
Qgd	Gate-Drain Charge		---	5.93	---	
Td(on)	Turn-On Delay Time	$V_{DD}=-50V$ , $V_{GS}=-10V$ , $R_G=3.3\Omega$ , $I_D=-10A$	---	12	---	ns
T <sub>r</sub>	Rise Time		---	27.4	---	
Td(off)	Turn-Off Delay Time		---	79	---	
T <sub>f</sub>	Fall Time		---	53.6	---	
Ciss	Input Capacitance	$V_{DS}=-20V$ , $V_{GS}=0V$ , $f=1\text{MHz}$	---	3029	---	pF
Coss	Output Capacitance		---	129	---	
Crss	Reverse Transfer Capacitance		---	76	---	
IS	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V$ , Force Current	---	---	-18	A
VSD	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V$ , $I_S=-1A$ , $T_J=25^{\circ}\text{C}$	---	---	-1.2	V
trr	Reverse Recovery Time	$I_F=-8A$ , $di/dt=-100A/\mu s$ , $T_J=25^{\circ}\text{C}$	---	38.7	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	22.4	---	nC

#### Note :

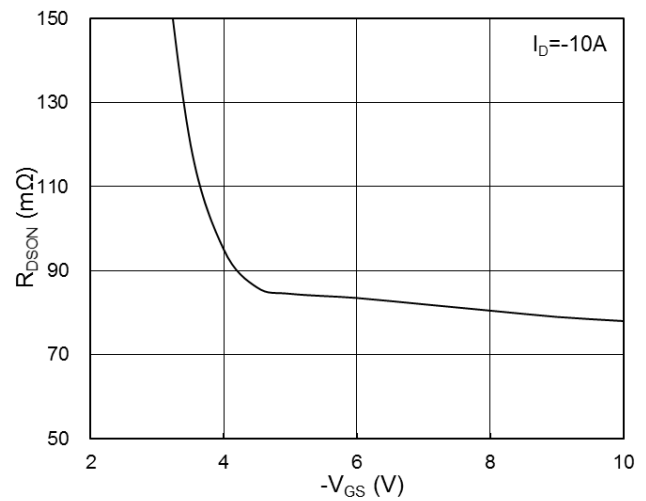
1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is  $V_{DD}=-72V$ ,  $V_{GS}=-10V$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=-25A$
4. The power dissipation is limited by  $150^{\circ}\text{C}$  junction temperature
5. The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation

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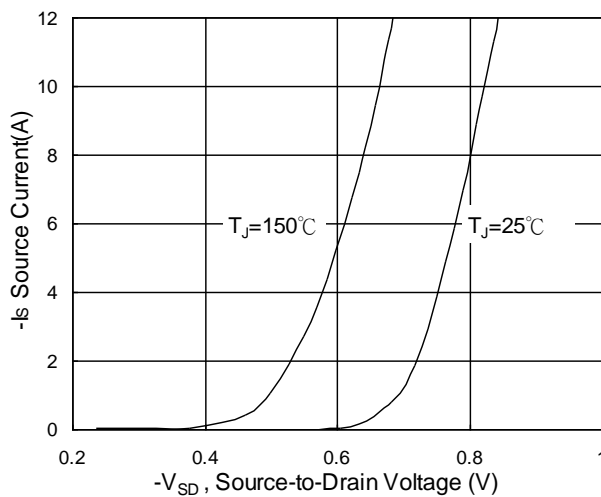
### Typical Characteristics



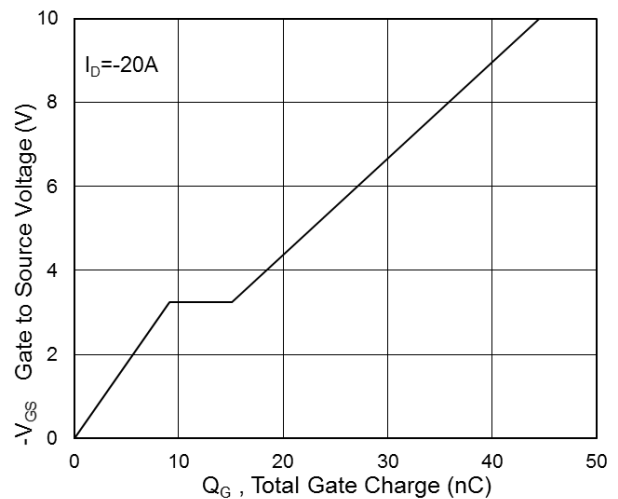
**Fig.1 Typical Output Characteristics**



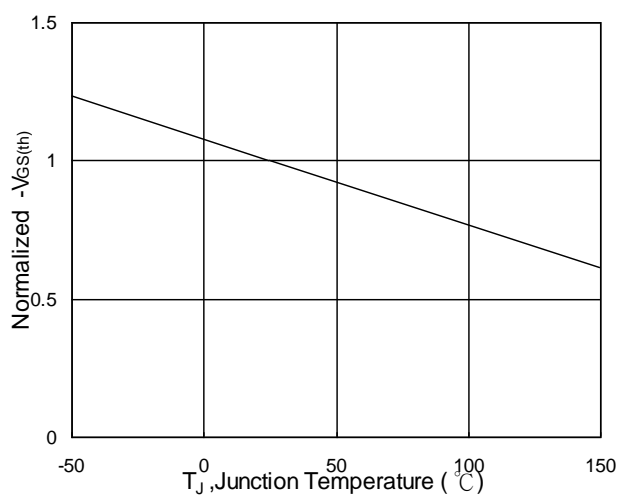
**Fig.2 On-Resistance vs G-S Voltage**



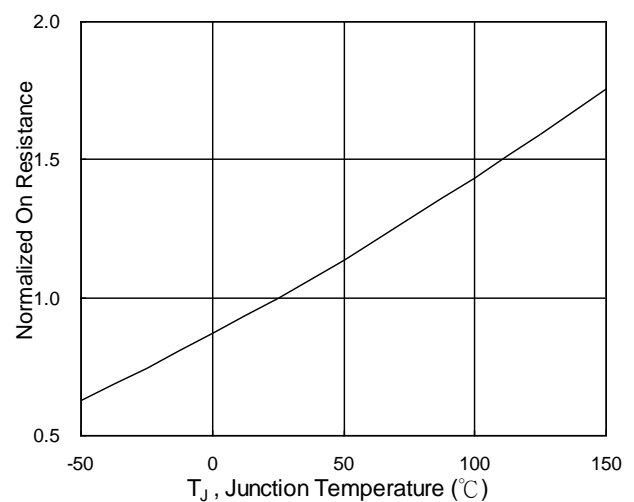
**Fig.3 Typical S-D Diode Forward Voltage**



**Fig.4 Gate-Charge Characteristics**

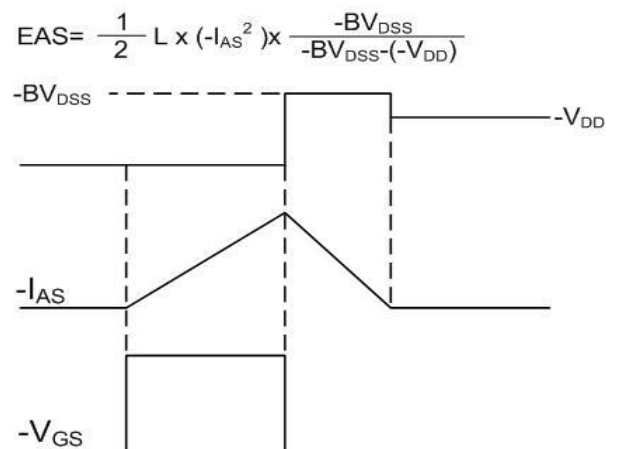
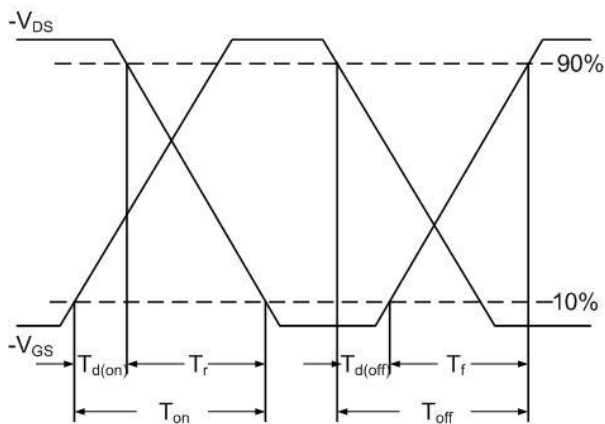
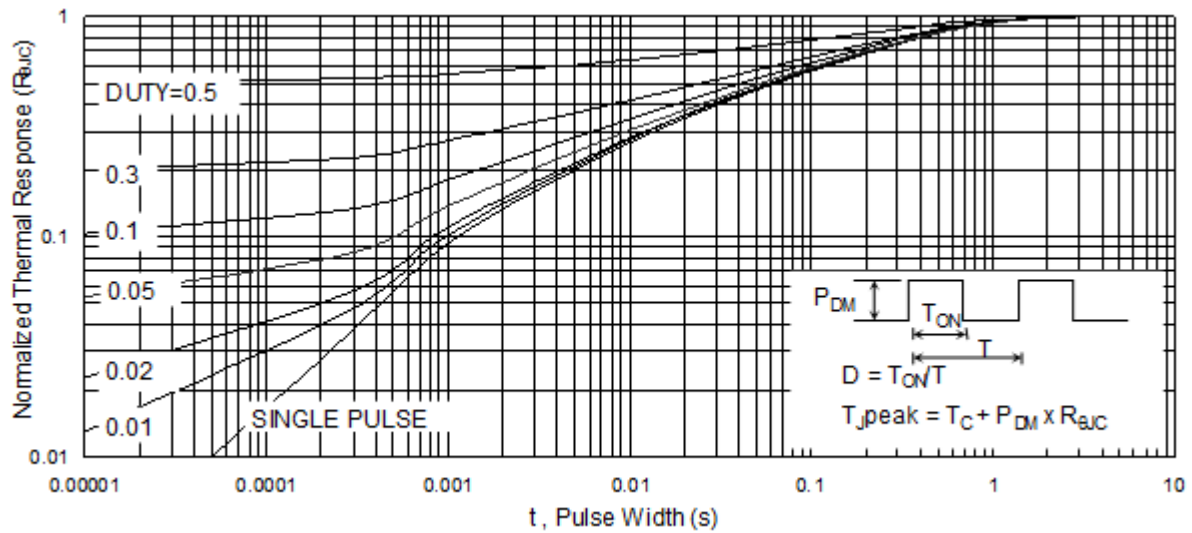
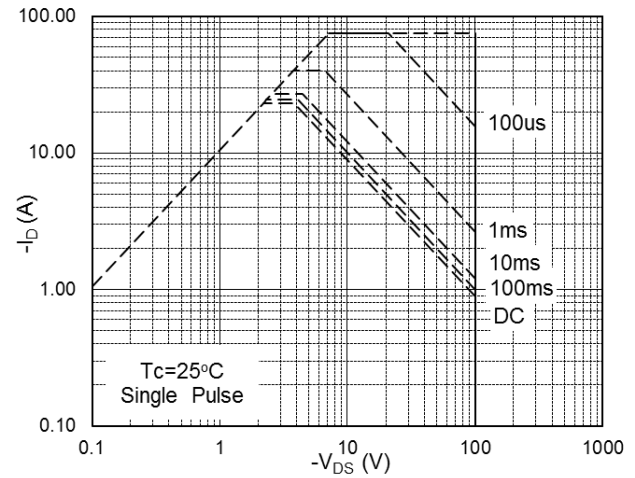
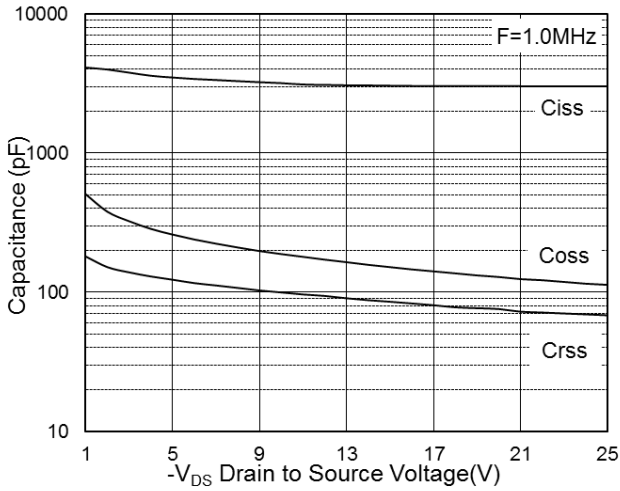


**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



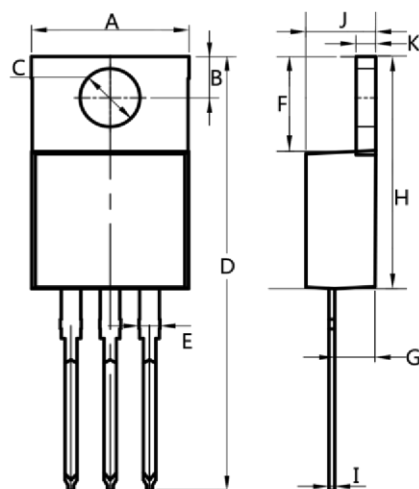
**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**

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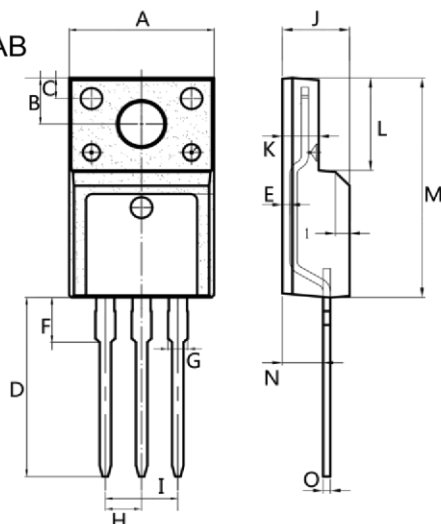
## -100V P-Channel Enhancement Mode MOSFET

TO-220AB



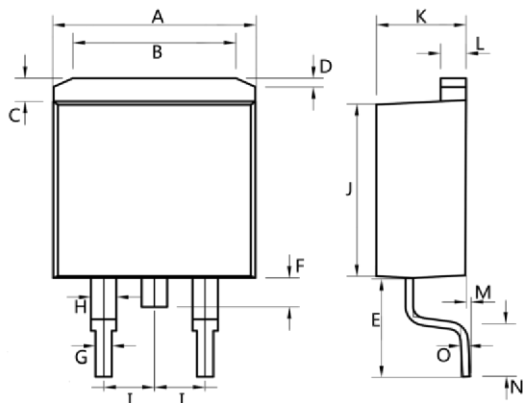
Dim.	Min.	Max.
A	10.0	10.4
B	2.5	3.0
C	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
H	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4
All Dimensions in millimeter		

ITO-220AB



Dim.	Min.	Max.
A	9.9	10.3
B	2.9	3.5
C	1.15	1.45
D	12.75	13.25
E	0.55	0.75
F	3.1	3.5
G	1.25	1.45
H	Typ 2.54	
I	Typ 5.08	
J	4.55	4.75
K	2.4	2.7
L	6.35	6.75
M	15.0	16.0
N	2.75	3.15
O	0.45	0.60
All Dimensions in millimeter		

TO-263



Dim.	Min.	Max.
A	10.0	10.5
B	7.25	7.75
C	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.75	0.95
H	1.15	1.35
I	Typ 2.54	
J	8.4	8.6
K	4.4	4.6
L	1.25	1.45
M	0.02	0.1
N	2.4	2.8
O	0.35	0.45
All Dimensions in millimeter		

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Edition	Date	Change
RVE1.0	2020/3/25	Initial release

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