

150V N-Channel Enhancement Mode MOSFET

Description

The AP30N15P/T uses advanced **APM-SGT II** technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 150V$ $I_D = 30A$

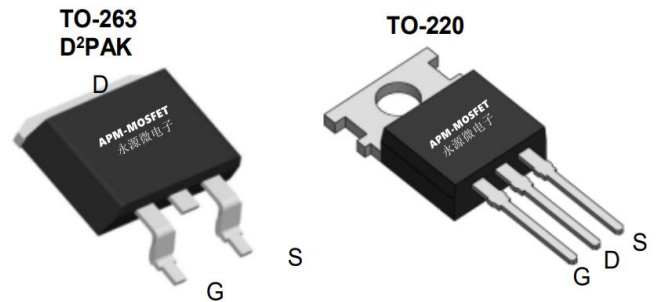
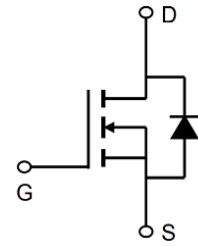
$R_{DS(ON)} < 78m\Omega @ V_{GS}=10V$ (Type: 63m Ω)

Application

Automotive lighting

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP30N15P	TO-220-3L	AP30N15P XXX YYYY	1000
AP30N15T	TO-263-3L	AP30N15T XXX YYYY	800

Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C=25^\circ C$	Drain Current, $V_{GS} @ 10V$	30	A
$I_D @ T_C=100^\circ C$	Drain Current, $V_{GS} @ 10V$	21	A
IDM	Pulsed Drain Current ¹	90	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation	60	W
TSTG	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Maximum Thermal Resistance, Junction-ambient	62.5	°C/W
$R_{\theta JC}$	Maximum Thermal Resistance, Junction-case	2.5	°C/W

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Electrical Characteristics@T_J=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	150	175	-	V
IGSS	Gate-body Leakage current	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
IDSS	Zero Gate Voltage Drain Current T _J = 25°C	V _{DS} = 150V, V _{GS} = 0V	-	-	1	μA
IDSS	Zero Gate Voltage Drain Current T _J = 100°C		-	-	100	μA
VGS(th)	Gate-Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2.0	3.0	4.5	V
RDS(on)	Drain-Source On-Resistance ²	V _{GS} = 10V, I _D = 10A	-	63	78	mΩ
RDS(on)	Drain-Source On-Resistance ²	V _{GS} = 4.5V, I _D = 8A	-	72	90	
gfs	Transconductance	V _{DS} = 5V, I _D = 10A	-	23	-	S
Ciss	Input Capacitance	V _{DS} = 75V, V _{GS} = 0V, f = 1MHz	-	630	-	pF
Coss	Output Capacitance		-	50	-	
Crss	Reverse Transfer Capacitance		-	13.5	-	
R _g	Gate Resistance	V _{GS} = 0V, V _{DS} Open, f = 1MHz	-	5	-	Ω
Q _g	Total Gate Charge	V _{GS} = 10V, V _{DD} = 75V, I _D = 10A	-	11	-	nC
Q _{gs}	Gate-Source Charge		-	1.2	-	
Q _{gd}	Gate-Drain Charge		-	4	-	
td(on)	Turn-On Delay Time	V _{GS} = 10V, V _{DD} = 75V, R _G = 10Ω, I _D = 10A	-	9.8	-	nS
t _r	Rise Time		-	6	-	
td(off)	Turn-Off Delay Time		-	15	-	
t _f	Fall Time		-	4.1	-	
VSD	Diode Forward Voltage ²	I _S = 10A, V _{GS} = 0V	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	V _R = 75V, I _F = 10A, dI/dt = 100A/μs	-	55	-	nS
Q _{rr}	Body Diode Reverse Recovery Charge		-	124	-	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、The EAS data shows Max. rating . The test condition is VDD=72V,VGS=10V,L=0.1mH,IAS=13A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

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Typical Characteristics

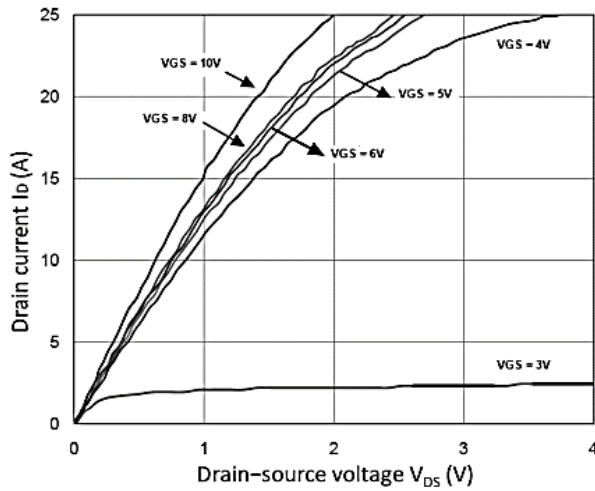


Figure 1. Output Characteristics

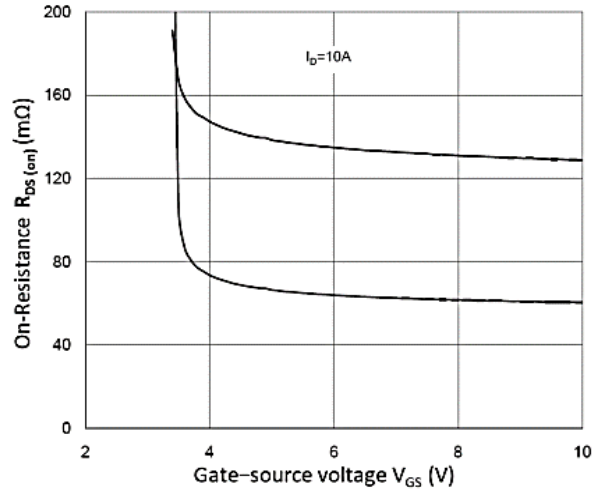


Figure 2. $R_{DS(on)}$ vs. V_{GS}

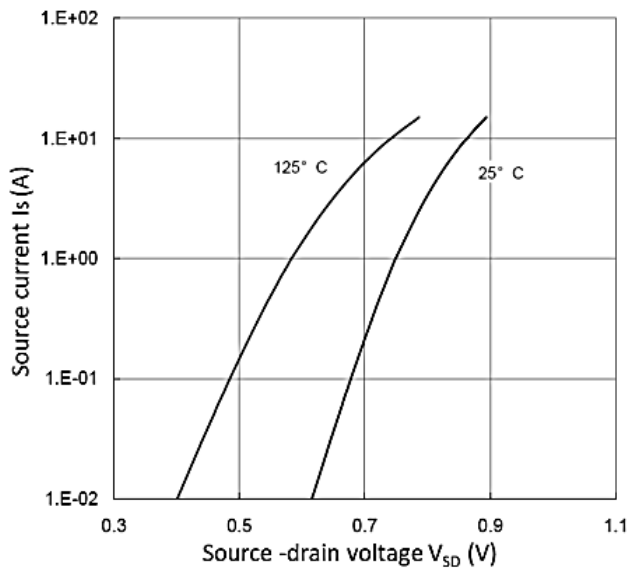


Figure 3. Forward Characteristics of Reverse

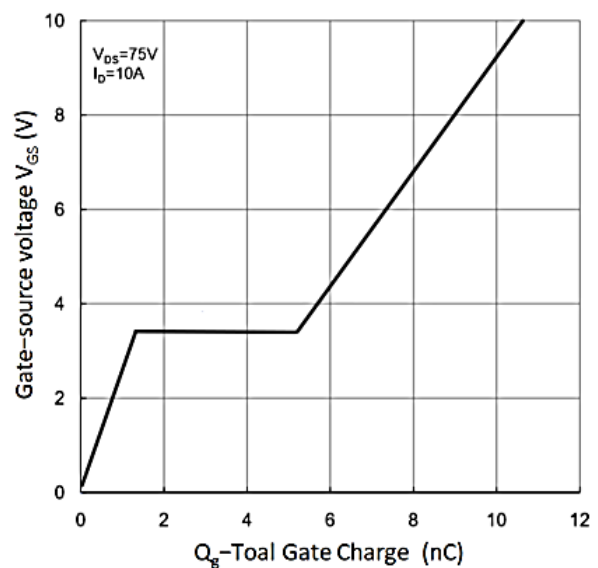


Figure 4. Gate Charge Characteristics

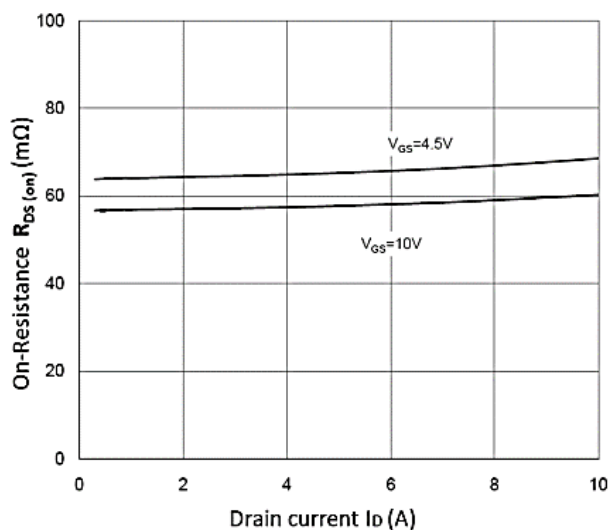


Figure 5. $R_{DS(ON)}$ vs. I_D

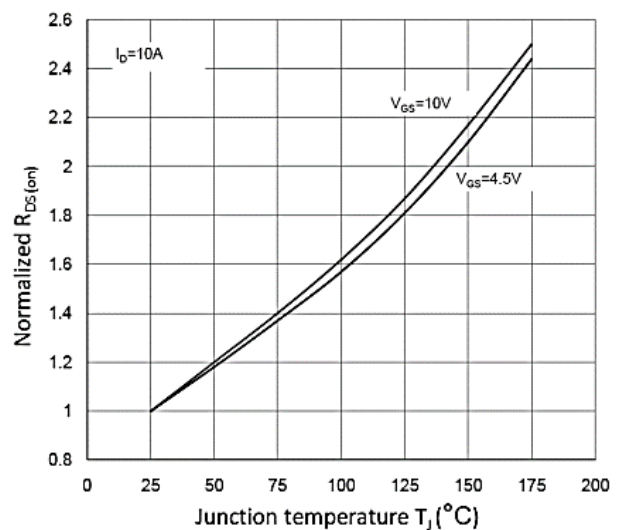


Figure 6. Normalized $R_{DS(on)}$ vs. T_J

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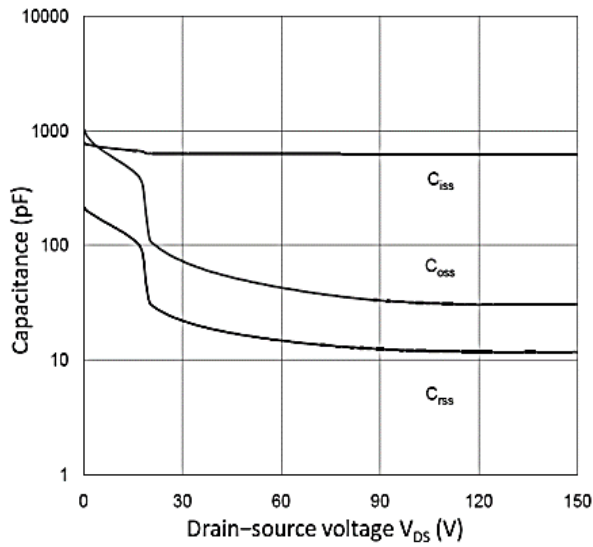


Figure 7. Capacitance Characteristics

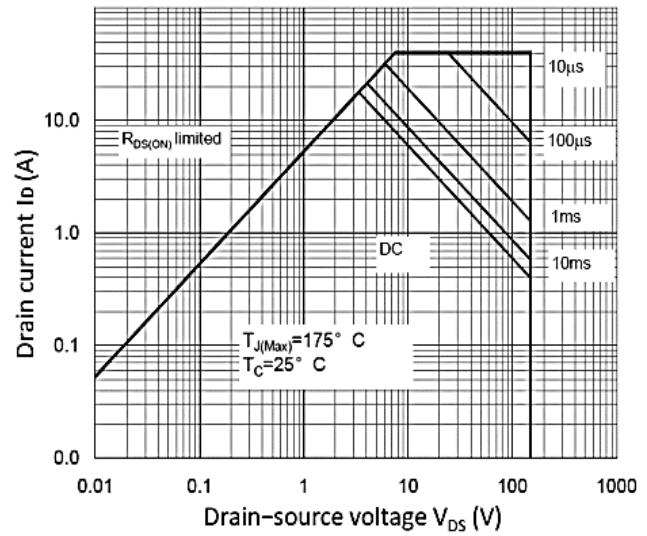


Figure 8. Safe Operating Area

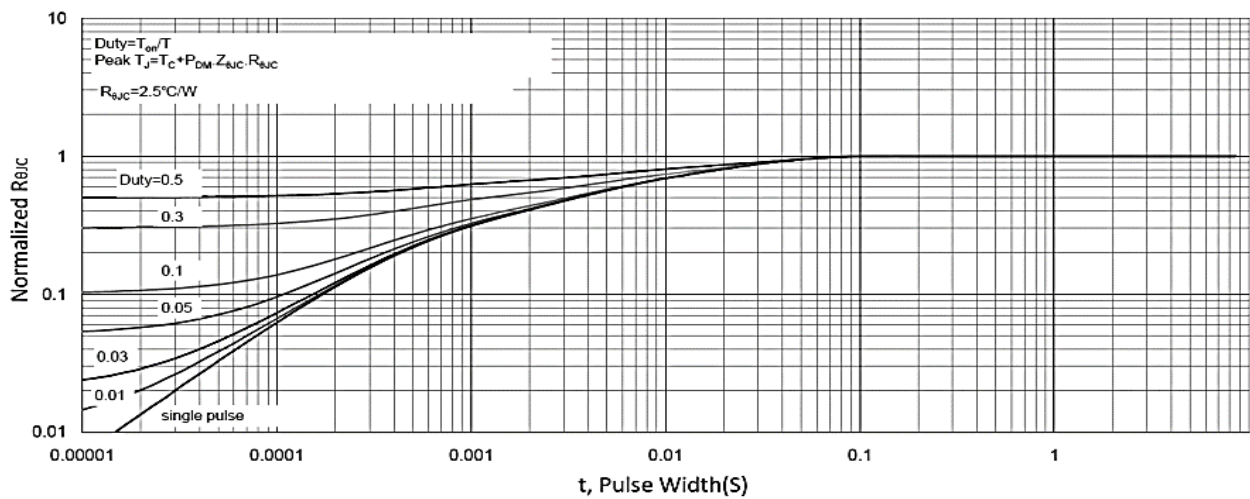


Figure 9. Normalized Maximum Transient Thermal Impedance

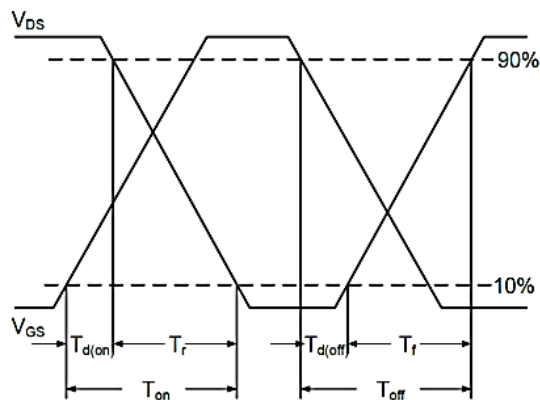


Figure 10. Switching Time Waveform

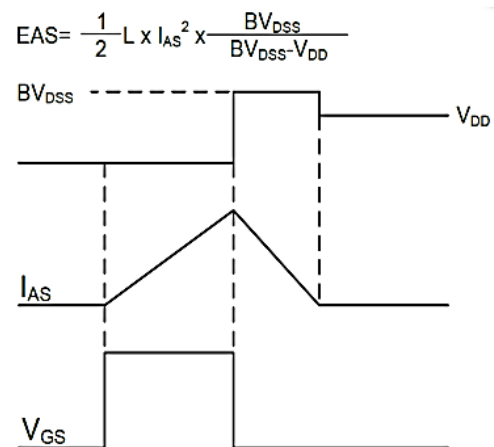
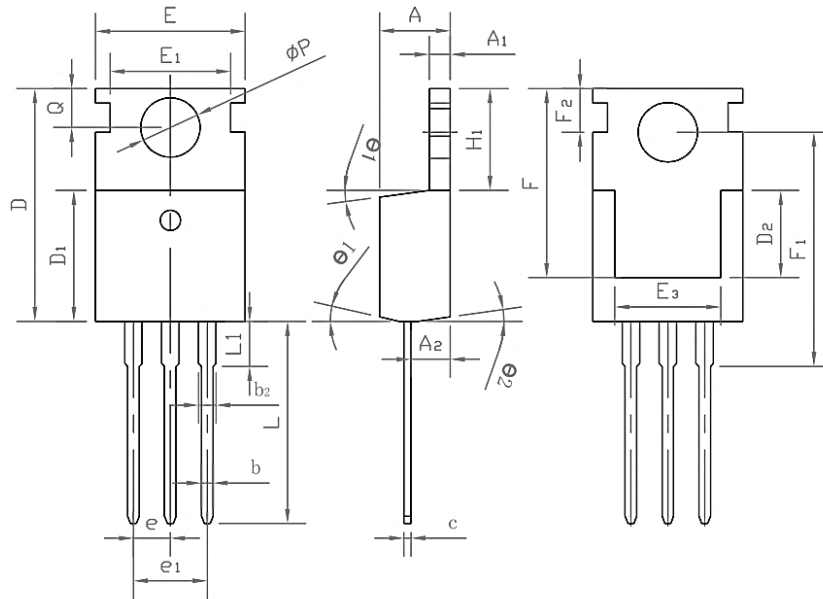


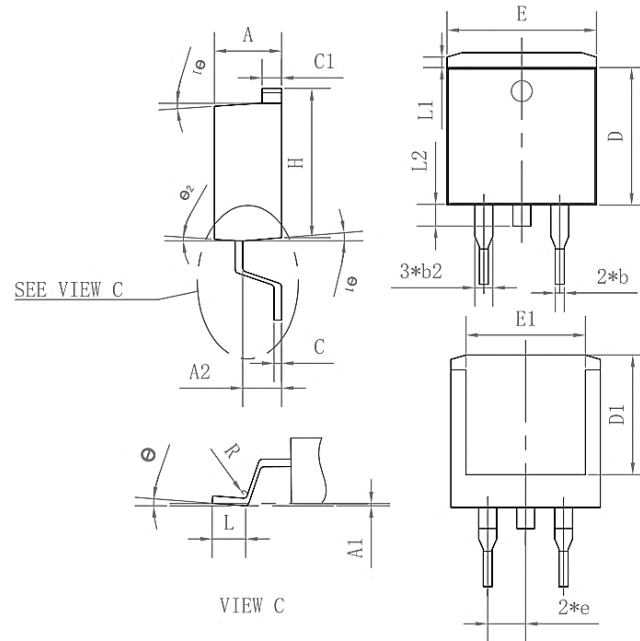
Figure 11. Unclamped Inductive Switching

150V N-Channel Enhancement Mode MOSFET Package Mechanical Data-TO-220-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
D	0.40	0.50	0.65
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.63	10.00	10.35
E3	7.00	8.00	8.40
e	0.37		
e1	0.10		
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
Φp	3.45	3.60	3.75
Q	2.60	2.80	3.00
θ1	4°	7°	10°
θ2	0°	3°	6°
F	13.30	13.50	13.70
F1	15.50	15.90	16.30
F2	2.80	3.00	3.20

150V N-Channel Enhancement Mode MOSFET Package Mechanical Data-TO-263-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
C	0.45	0.50	0.65
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
H	15.30	15.50	15.70
e	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
theta	0°	4°	8°
theta1	4°	7°	10°
theta2	0°	3°	6°

150V N-Channel Enhancement Mode MOSFET**Attention**

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Edition	Date	Change
Rve1.0	2021/10/29	Initial release

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