



**Advanced Power
Electronics Corp.**

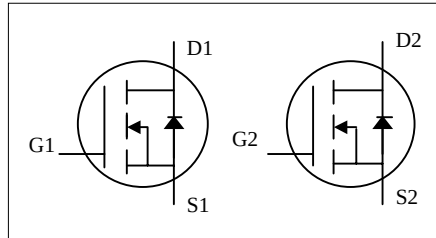
AP2622GY

Pb Free Plating Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ Low Gate Charge
- ▼ Small Package Outline
- ▼ Surface Mount Package
- ▼ RoHS Compliant

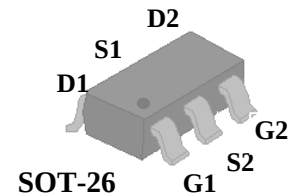


BV_{DSS}	50V
$R_{DS(ON)}$	1.8Ω
I_D	520mA

Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-26 package is universally used for all commercial-industrial applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	50	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ\text{C}$	Continuous Drain Current ³ , V_{GS} @ 10V	520	mA
$I_D @ T_A = 70^\circ\text{C}$	Continuous Drain Current ³ , V_{GS} @ 10V	410	mA
I_{DM}	Pulsed Drain Current ^{1,2}	1.5	A
$P_D @ T_A = 25^\circ\text{C}$	Total Power Dissipation	0.8	W
	Linear Derating Factor	0.006	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Thermal Resistance Junction-ambient ³ Max.	150	$^\circ\text{C}/\text{W}$

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	50	-	-	V
ΔBV _{DSS} /ΔT _j	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =1mA	-	0.06	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =500mA	-	-	1.8	Ω
		V _{GS} =4.5V, I _D =200mA	-	-	3.2	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1	-	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =500mA	-	600	-	mS
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =50V, V _{GS} =0V	-	-	10	uA
	Drain-Source Leakage Current (T _j =70°C)	V _{DS} =40V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V	-	-	±30	uA
Q _g	Total Gate Charge ²	I _D =500mA	-	1	1.6	nC
Q _{gs}	Gate-Source Charge	V _{DS} =40V	-	0.5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =4.5V	-	0.5	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =25V	-	12	-	ns
t _r	Rise Time	I _D =500mA	-	10	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =10V	-	56	-	ns
t _f	Fall Time	R _D =50Ω	-	29	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	32	50	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	8	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	6	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =0.6A, V _{GS} =0V	-	-	1.3	V

Notes:

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse width ≤300us , duty cycle ≤2%.
- 3.Surface mounted on 1 in² copper pad of FR4 board, t≤5sec ; 250°C/W when mounted on min. copper pad.

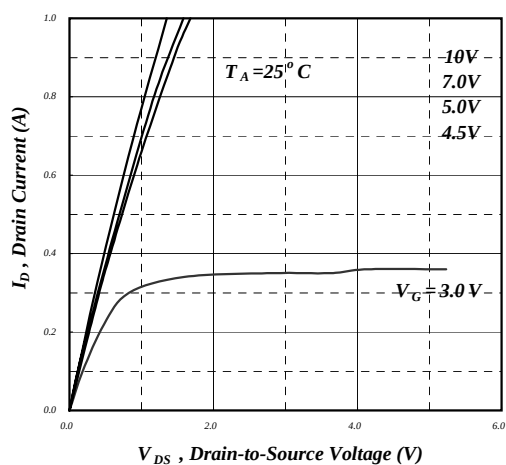


Fig 1. Typical Output Characteristics

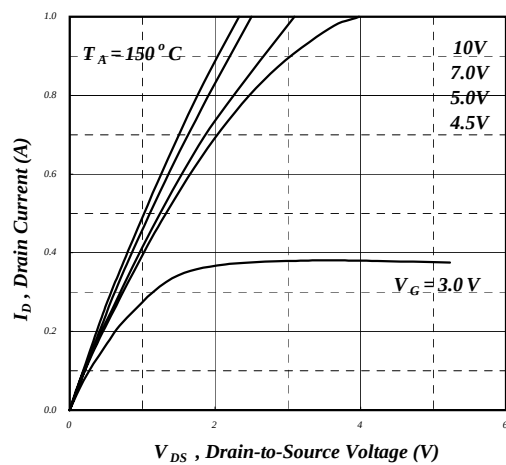


Fig 2. Typical Output Characteristics

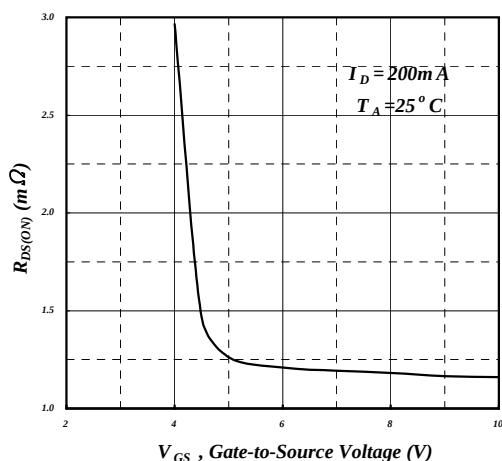


Fig 3. On-Resistance v.s. Gate Voltage

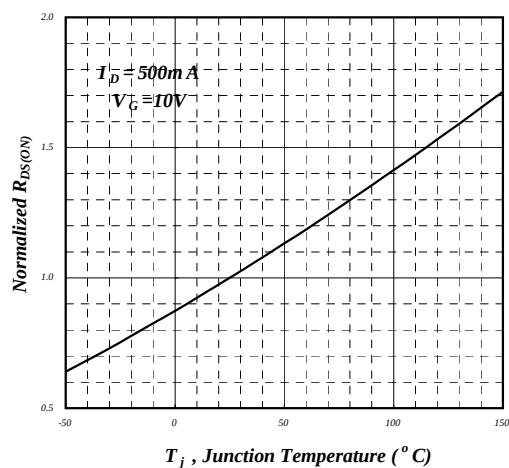


Fig 4. Normalized On-Resistance v.s. Junction Temperature

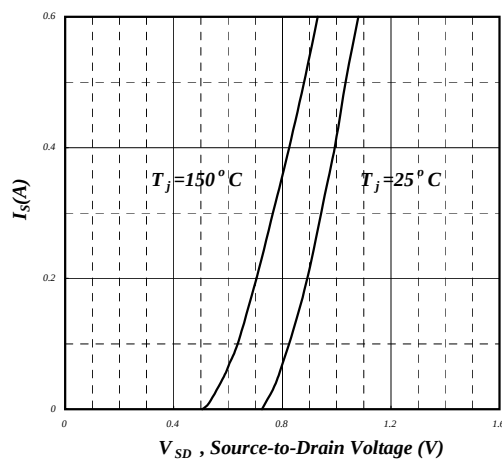


Fig 5. Forward Characteristic of Reverse Diode

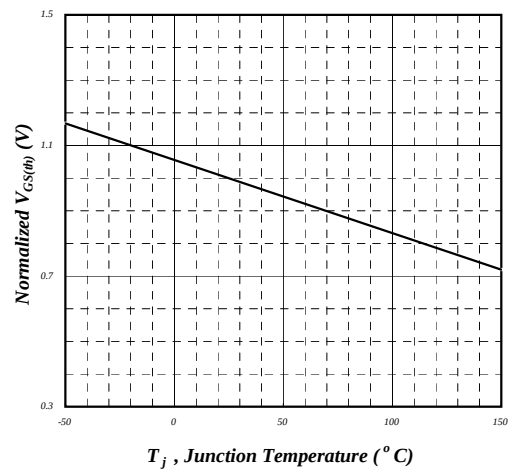


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

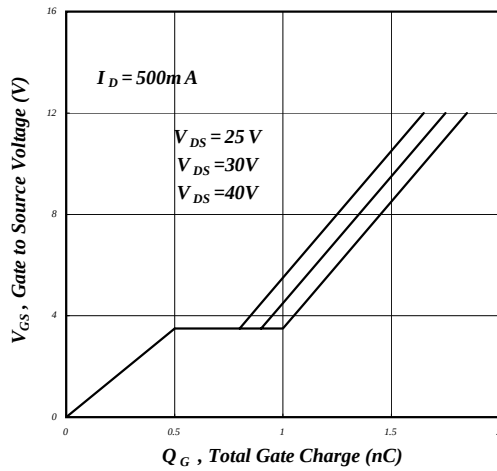


Fig 7. Gate Charge Characteristics

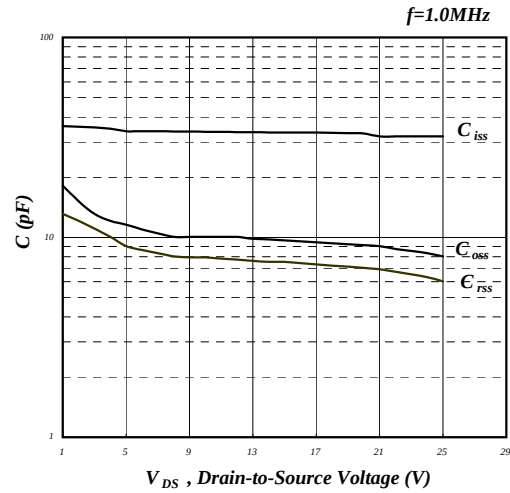


Fig 8. Typical Capacitance Characteristics

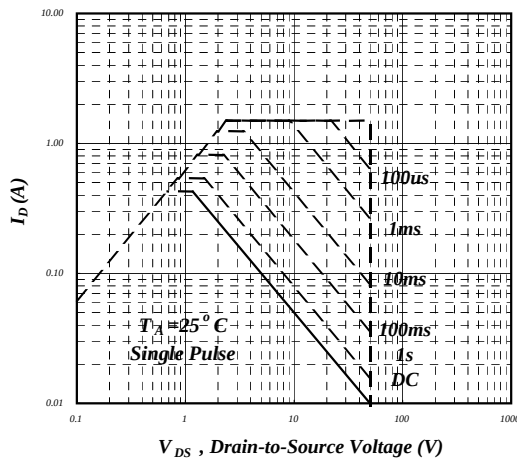


Fig 9. Maximum Safe Operating Area

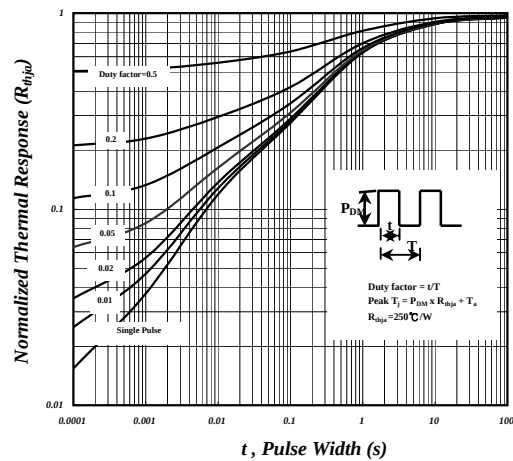


Fig 10. Effective Transient Thermal Impedance

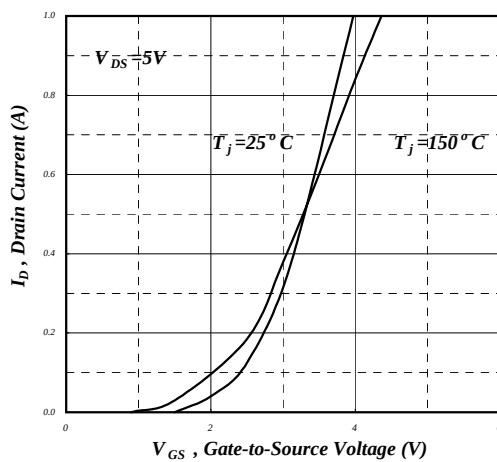


Fig 11. Transfer Characteristics

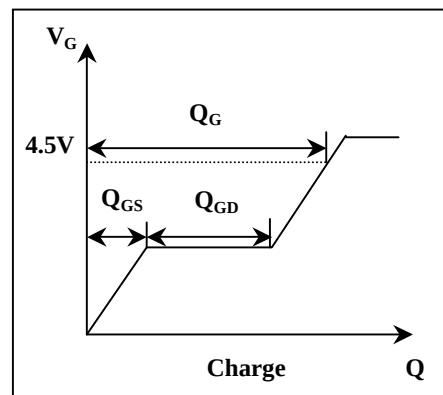


Fig 12. Gate Charge Waveform