

Description

The AP25N10D uses advanced APM-SGTII technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = 100V I_{D} = 25A$

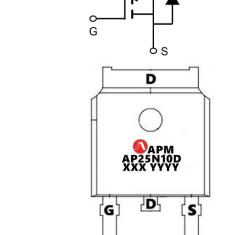
 $R_{DS(ON)} < 53m\Omega$ @ $V_{GS}=10V$ (Type: 43m Ω)

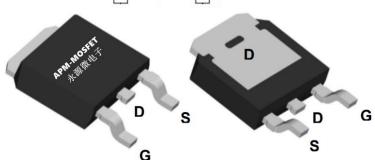
Application

DC/DC Converter

LED Backlighting

Power Management Switches





Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP25N10D	TO-252-3L	AP25N10D XXX YYYY	2500

Absolute Maximum Ratings (T_C=25°Cunless otherwise noted)

Symbol	ol Parameter Rating		Units
VDS	Drain-Source Voltage	100	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V	25	Α
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V	11.4	Α
IDM	Pulsed Drain Current	72	Α
EAS	Single Pulse Avalanche Energy	14.5	mJ
IAS	Avalanche Current	8.5	Α
P _D @T _C =25°C	Total Power Dissipation⁴	35.7	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-Ambient	3.5	°C/W
R₀JC	Thermal Resistance Junction-Case	62	°C/W





Electrical Characteristics (Tc=25℃ unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	100	108	-	V
IDSS	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
IGSS	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.7	2.5	V
DDC(on)	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =15A	-	43	52	mΩ
RDS(on)		V _{GS} =4.5V, I _D =10A	-	60	80	mΩ
g fs	Forward Threshold Voltage	V _{DS} =10V, I _D =20A	-	7.5	-	S
Rg	Gate Resistance	$V_{DS} = V_{GS} = 0V$, $f = 1.0MHz$	-	1.75	-	Ω
Ciss	Input Capacitance		-	390	-	pF
Coss	Output Capacitance	V_{DS} =50V, V_{GS} =0V, f = 1.0MHz	-	94	-	pF
Crss	Reverse Transfer Capacitance	1 1.500112	-	3.3	-	pF
Qg	Total Gate Charge	.,,,	-	8.2	-	
Qgs	Gate-Source Charge	V_{DS} =50V, I_{D} =10A, V_{GS} =10V	-	1.4	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	2.1	-	
td(on)	Turn-On Delay Time		-	4.2	-	
t_{r}	Turn-On Rise Time	$V_{DS} = 50V, I_{D} = 10A,$	-	4.9	-	ns
td(off)	Turn-Off Delay Time	$R_G = 3\Omega$, $V_{GS}=10V$	-	13	-	113
t _f	Turn-Off Fall Time		-	4.8	-	
Is	Continuous Source Current		-	-	25	Α
VSD	Diode Forward Voltage	I _S =10A . V _{GS} = 0V	-	0.75	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =10A, dI _{SD} /dt=100A/μs	-	2.2	-	ns
Qrr	Reverse Recovery Charge	ISD-TOA, UISD/UI-TOOA/μS	-	34.4	-	nC

Notes:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2 The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.5mH, I_{AS} =8.5A
- 4. The power dissipation is limited by 150°C junction temperature
- 5 The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

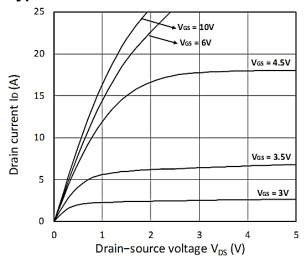


Figure 1. Output Characteristics

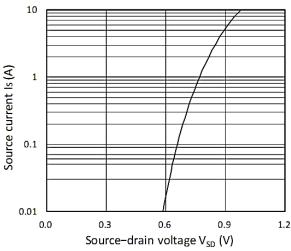


Figure 3. Forward Characteristics of Reverse

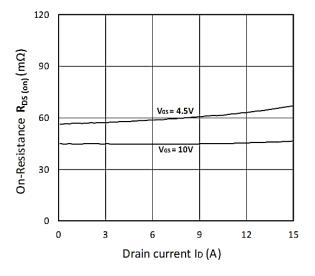


Figure 5. RDS(ON) vs. ID

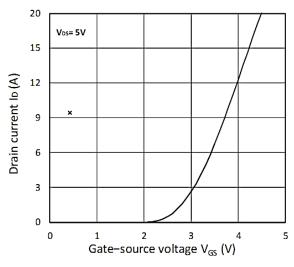


Figure 2. Transfer Characteristics

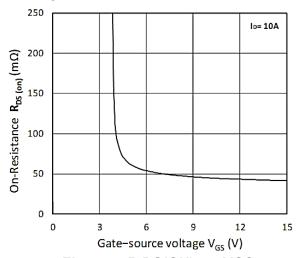


Figure 4. R DS(ON) vs. VGS

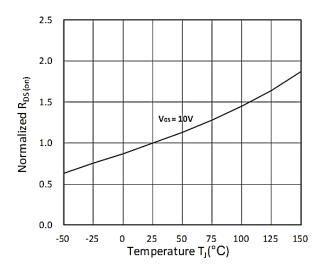
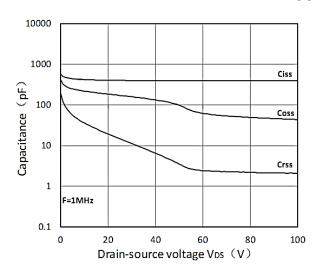


Figure 6. Normalized RDS(on) vs. Temperature





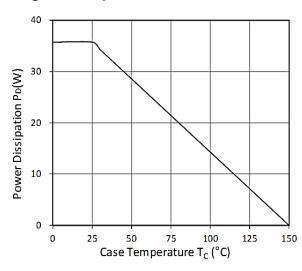


10 V_{os} = 50V l_{o= 10A}

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Figure 7. Capacitance Characteristics





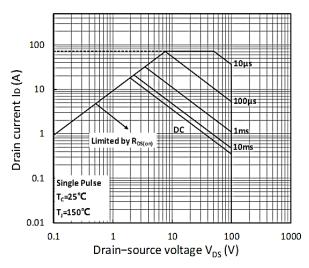


Figure 9. Power Dissipation

Figure 10. Safe Operating Area

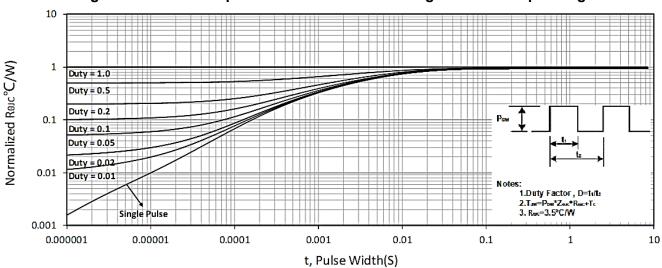
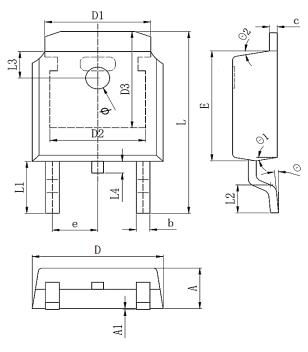


Figure 9 Normalized Maximum Transient Thermal Impedance



Package Mechanical Data-TO-252-3L



Cumhal	Dim in mm			
Symbol	Min	Тур	Max	
A	2.1	2.3	2.5	
A1	0	0.064	0.128	
b	0.64	0.75	0.86	
С	0.45	0.52	0.6	
D	6.4	6.6	6.8	
D1	5.33REF			
D2	4.83REF			
D3	5.25REF			
E	5.9	6.1	6.3	
е	2.286TYP			
L	9.8 10.1 10.4		10.4	
L1	2.888REF			
L2	1.4	1.5	1.7	
L3	1.65REF			
L4	0.6	0.8	1	
ф	1.1	1.2	1.3	
θ	0°		10°	
θ1	5°		10°	
θ2	5°		10°	



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Edition	Date	Change
REV1.0	2023/11/10	Initial release

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