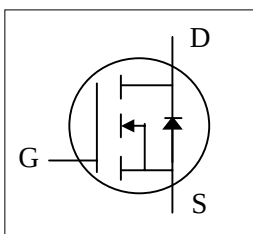




- ▼ Lower On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant



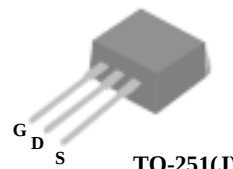
BV_{DSS}	150V
$R_{DS(ON)}$	105m Ω
I_D	22A

Description

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP20N15GJ) is available for low-profile applications.



TO-252(H)



TO-251(J)

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^{\circ}\text{C}$	Continuous Drain Current, V_{GS} @ 10V	22	A
$I_D@T_C=100^{\circ}\text{C}$	Continuous Drain Current, V_{GS} @ 10V	14	A
I_{DM}	Pulsed Drain Current ¹	88	A
$P_D@T_C=25^{\circ}\text{C}$	Total Power Dissipation	96	W
	Linear Derating Factor	0.77	W/ $^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case	Max. 1.3	$^{\circ}\text{C}/\text{W}$
Rthj-a	Thermal Resistance Junction-ambient	Max. 110	$^{\circ}\text{C}/\text{W}$



AP20N15GH/J

Electrical Characteristics@T_J=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =1mA	150	-	-	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =1mA	-	0.1	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =14A	-	-	105	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	2	-	4	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =14A	-	15	-	S
I _{DSS}	Drain-Source Leakage Current (T _J =25°C)	V _{DS} =150V, V _{GS} =0V	-	-	25	uA
	Drain-Source Leakage Current (T _J =150°C)	V _{DS} =120V, V _{GS} =0V	-	-	100	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =14A	-	18	28	nC
Q _{gs}	Gate-Source Charge	V _{DS} =120V	-	6	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	5.7	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =75V	-	12	-	ns
t _r	Rise Time	I _D =14A	-	30	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =10Ω, V _{GS} =10V	-	17	-	ns
t _f	Fall Time	R _D =5.4Ω	-	2	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1070	1720	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	240	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	8	-	pF
R _g	Gate Resistance	f=1.0MHz	-	1.5	2.3	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =14A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _S =14A, V _{GS} =0V,	-	130	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	700	-	nC

Notes:

1.Pulse width limited by safe operating area.

2.Pulse width ≤300us , duty cycle ≤2%.