



AOT424

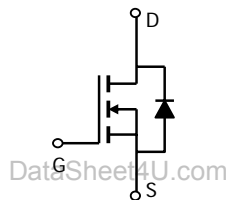
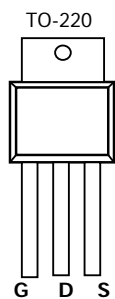
N-Channel Enhancement Mode Field Effect Transistor

General Description

The AOT424 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and low gate resistance. This device is ideally suited for use as a low side switch in CPU core power conversion. Standard Product AOT424 is Pb-free (meets ROHS & Sony 259 specifications). AOT424L is a Green Product ordering option. AOT424 and AOT424L are electrically identical.

Features

V_{DS} (V) = 30V
 I_D = 110A (V_{GS} = 10V)
 $R_{DS(ON)} < 4m\Omega$ (V_{GS} = 10V)
 $R_{DS(ON)} < 5.5m\Omega$ (V_{GS} = 4.5V)



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}^G$	A
Current ^{B,G}		$T_C=100^\circ\text{C}^B$	
Pulsed Drain Current	I_{DM}	200	
Avalanche Current ^C	I_{AR}	30	A
Repetitive avalanche energy $L=0.1\text{mH}^C$	E_{AR}	112	mJ
Power Dissipation ^B	P_D	$T_C=25^\circ\text{C}$	W
		$T_C=100^\circ\text{C}$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	$t \leq 10\text{s}$	14.2	$^\circ\text{C/W}$
Maximum Junction-to-Ambient ^A		Steady-State	39	$^\circ\text{C/W}$
Maximum Junction-to-Case ^C	$R_{\theta JC}$	0.8	1.5	$^\circ\text{C/W}$

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Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V T _J =55°C			1 5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} = ±20V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1	2	3	V
I _{D(ON)}	On state drain current	V _{GS} =10V, V _{DS} =5V	110			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =30A T _J =125°C		3 4.7	4 6	mΩ
		V _{GS} =4.5V, I _D =30A		4.3	5.5	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =30A		106		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V		0.72	1	V
I _S	Maximum Body-Diode Continuous Current				85	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance			3200	3840	pF
C _{oss}	Output Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		590		pF
C _{rss}	Reverse Transfer Capacitance			414		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		0.54	0.7	Ω
SWITCHING PARAMETERS						
Q _g (10V)	Total Gate Charge	DataSheet4U.com		59.6	72	nC
Q _g (4.5V)	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =30A		30.4	37	nC
Q _{gs}	Gate Source Charge			9.5		nC
Q _{gd}	Gate Drain Charge			19.8		nC
t _{D(on)}	Turn-On Delay Time			12.5		ns
t _r	Turn-On Rise Time	V _{GS} =10V, V _{DS} =15V, R _L =0.5Ω,		35.5		ns
t _{D(off)}	Turn-Off Delay Time	R _{GEN} =3Ω		40		ns
t _f	Turn-Off Fall Time			32.5		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =30A, dI/dt=100A/μs		35.3	42	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =30A, dI/dt=100A/μs		30.7		nC

A: The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The Power dissipation P_{DSM} is based on steady-state R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB or heatsink allows it.

B: The power dissipation P_D is based on T_{J(MAX)}=175°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

C: Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=175°C.

D: The R_{θJA} is the sum of the thermal impedance from junction to case R_{θJC} and case to ambient.

E: The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

G: The maximum current rating is limited by the package current capability.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

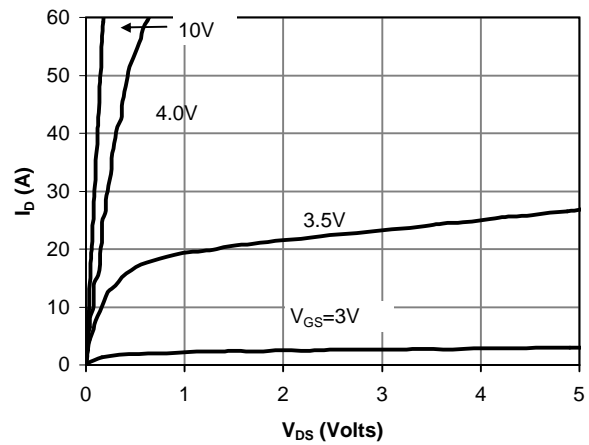


Fig 1: On-Region Characteristics

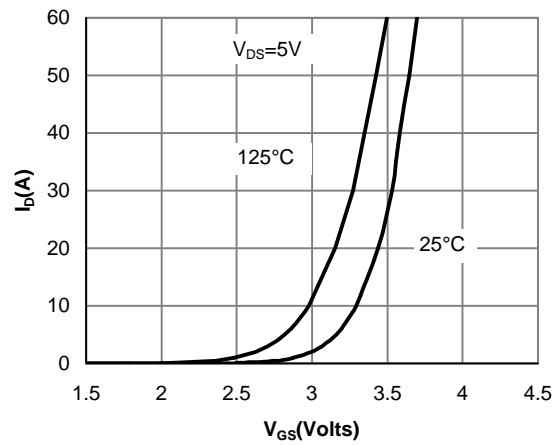


Figure 2: Transfer Characteristics

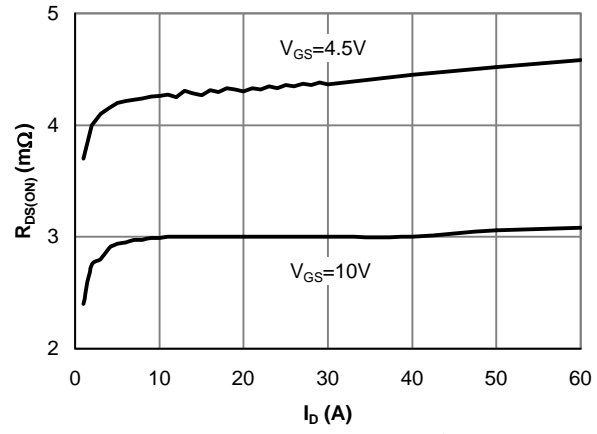


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

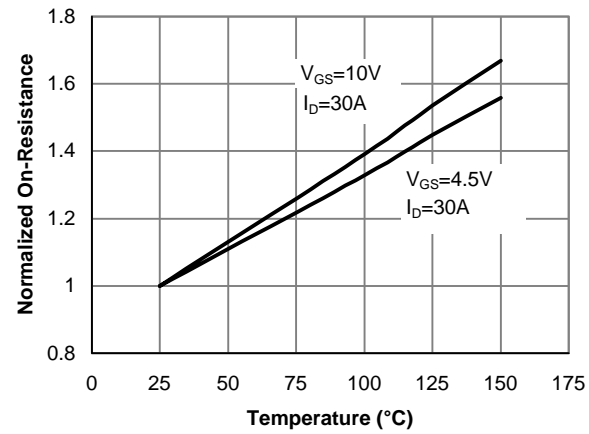


Figure 4: On-Resistance vs. Junction Temperature

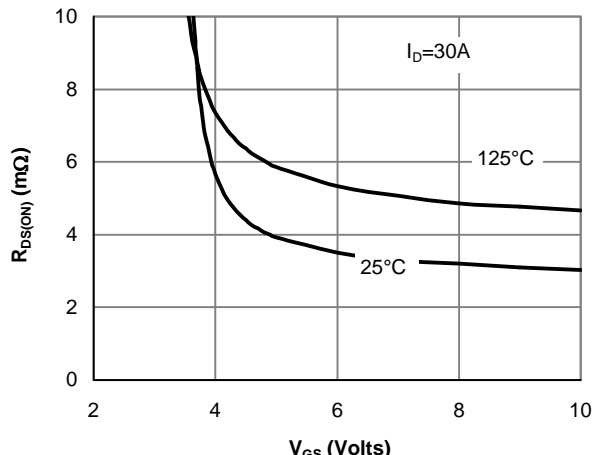


Figure 5: On-Resistance vs. Gate-Source Voltage

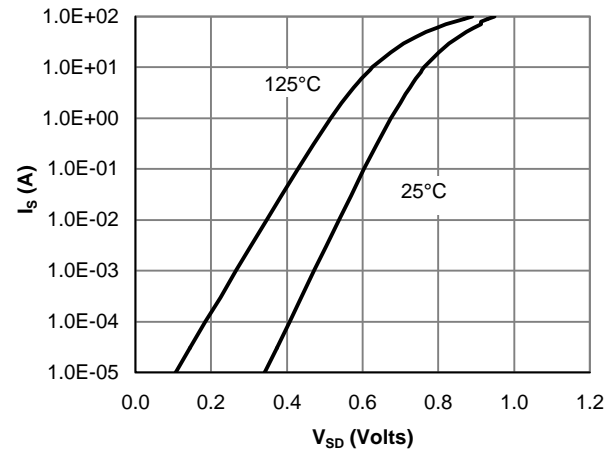


Figure 6: Body-Diode Characteristics

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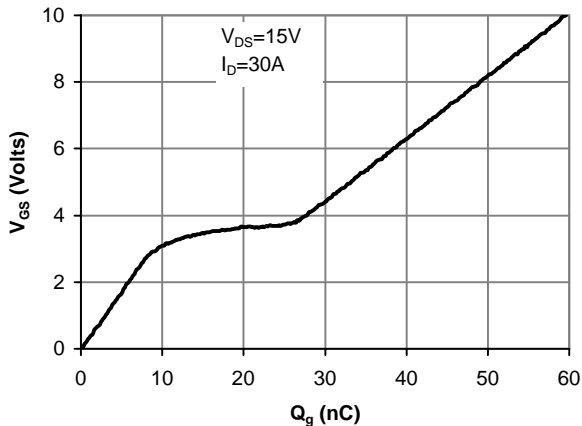


Figure 7: Gate-Charge Characteristics

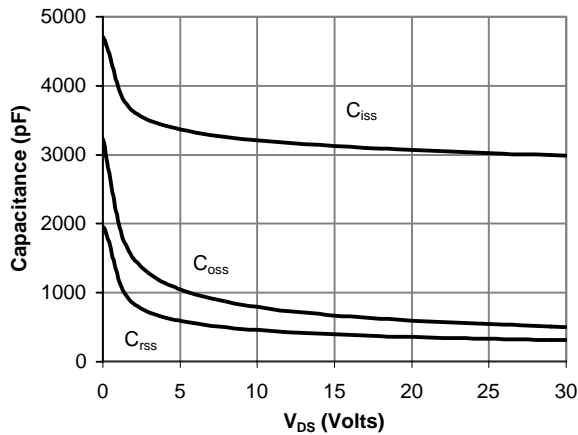


Figure 8: Capacitance Characteristics

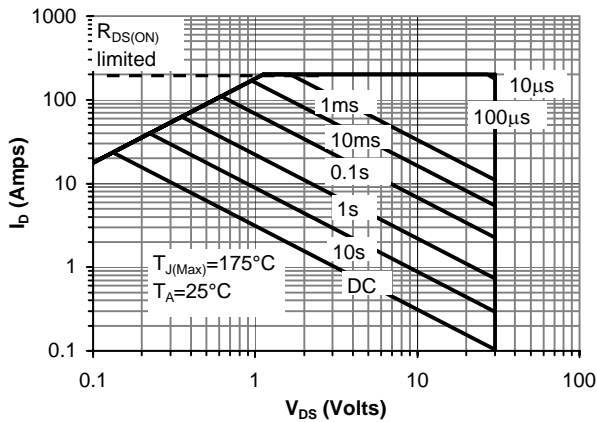


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

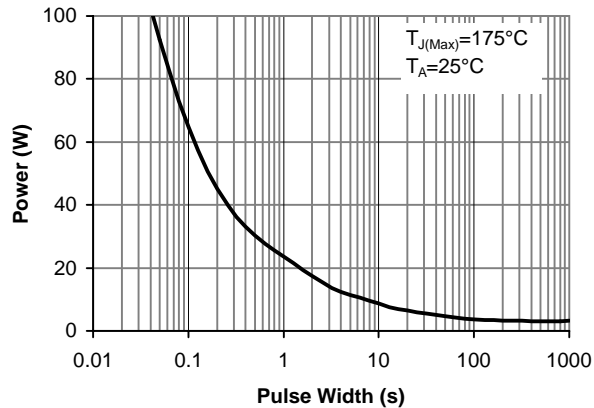


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

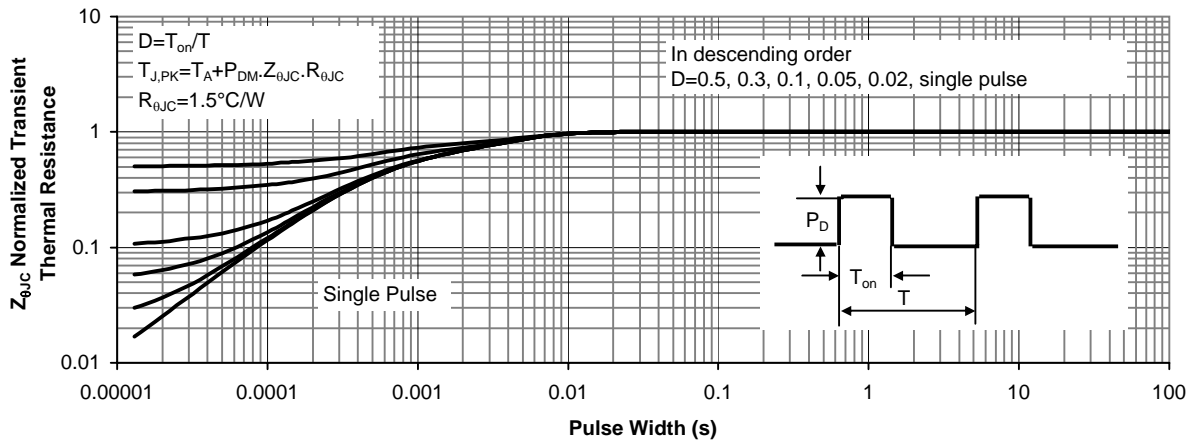
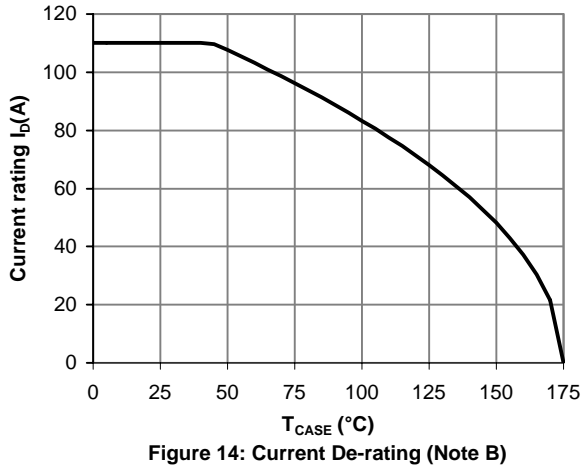
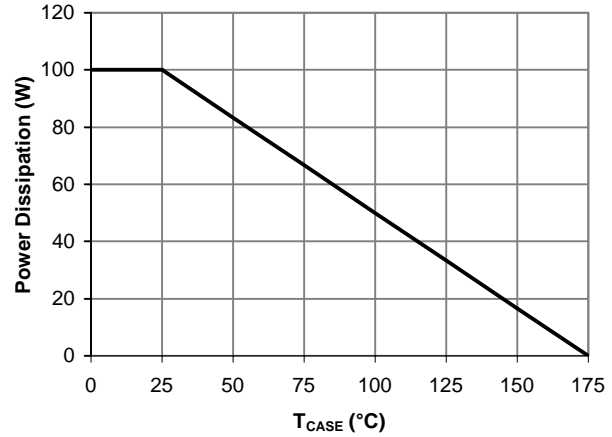
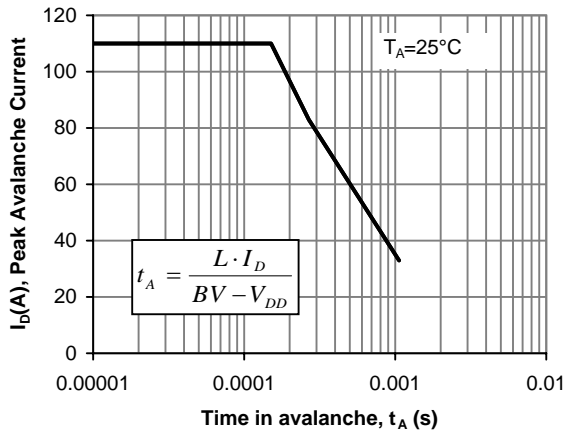


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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