

# AN1702FHK

### Gray-scale voltage generating IC for liquid crystal display

#### Overview

The AN1702FHK has been designed for LCD applications, and features built-in functions such as a gray-scale voltage generatior that supports 256 gradations, a reference voltage of 1.23 V, 16 output buffer amp circuits and a COM amplifier circuit.

#### ■ Features

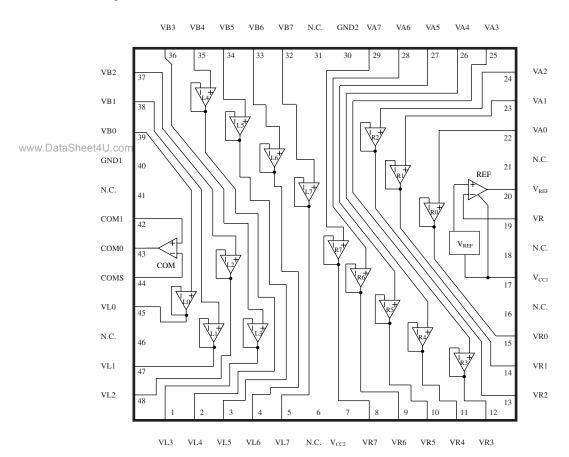
- Built-in 1.23 V (typical) high-precision reference voltage power source
- Wide buffer amp. dynamic range output:  $V_{CC} 0.2 \text{ V}$  (top stage) to GND + 0.15 V (bottom stage)
- Large ±100 mA (max.) COM amp. driving current

### ■ Applications

• Gray-scale power sources for LCDs

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### ■ Block Diagram



Publication date: December 2001 SDF00004BEB 1



### ■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	15.8	V
Supply current	I <sub>CC</sub>	<del>-</del>	mA
Power dissipation *2	$P_{\mathrm{D}}$	200	mW
Operating ambient temperature *1	$T_{opr}$	0 to +75	°C
Strage temperature *1	T <sub>stg</sub>	-55 to +125	°C
REF amplifier output source current	$I_{O_{SOURCE}}$	-5	mA
R0 amplifier output source current	$I_{O-R0}$	-15	mA
R0 amplifier output sink current	$I_{O_{+R0}}$	0.05	mA
R0, R1, R2, R3, R4, R5, L5, L4, L3, L2, L1 amplifier output source current	I <sub>O-R1-R5, L1-L5</sub>	-10	mA
R6, R7, L7, L6 amplifier output source current	I <sub>O-R6-L6</sub>	-15	mA
R0, R1, R2, R3, R4, R5, L5, L4, L3, L2, L1 amplifier output sink current	I <sub>O+R1-R5, L1-L5</sub>	10	mA
R6, R7, L7, L6 amplifier output sink current	$I_{O+R6-L6}$	15	mA
L0 amplifier output source current	$I_{O_{-L0}}$	- 0.05	mA
L0 amplifier output sink current	$I_{O_{+L0}}$	15	mA
COM amplifier output source current		-100	mA
COM amplifier output sink current	$I_{O_{+COM}}$	100	mA
REF amplifier maximum load capacitance		0.2	μF
R0, R1, R2, R3, R4, R5, R6, R7, www. Peris be to the Edge of the E	C <sub>OR/L</sub>	0.1	μF
COM amplifier maximum load capacitance	C <sub>OCOM</sub>	10	μF

Note) 1. Do not apply external currents or voltages to any pins not specifically mentioned.

For circuit currents, '+' denotes current flowing into the IC, and '-' denotes current flowing out of the IC.

### ■ Recommended Operating Range

Parameter		Symbol	Range	Unit
Supply voltage		V <sub>CC</sub>	7 to 15.5	V
Load capacitance	R0 to L0	CLOAD	0.01	μF
	COM		0.1 to 1	μF
	REF		0.1	μF

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<sup>2. \*1:</sup> Except for the power dissipation, operating ambient temperature and storage temperature, all ratings are for  $T_a = 25^{\circ}\text{C}$ . \*2:  $T_a = 75^{\circ}\text{C}$ . For the independent IC without a heat sink.

# ■ Electrical Characteristics at $T_a = 25$ °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Circuit current	I <sub>CC</sub>		_	_	10	mA
Reference voltage	V <sub>REF</sub>		1.19	1.23	1.27	V
Input bias current	$I_B$		_		500	nA
REF amplifier block						
Operating upper limit voltage	V <sub>H</sub>	$I_{OUT} = -2 \text{ mA}, C_{OUT} \ge 0.1 \mu\text{F}$	V <sub>CC</sub> – 0.2	_	_	V
Operating lower limit voltage	VL	COUT ≥ 0.1 μF	_	_	V <sub>REF</sub>	V
R0 amplifier block						
Output upper limit voltage 1	$V_{H_{R01}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> – 0.2	_	_	V
Output upper limit voltage 2	$V_{H_{R02}}$	$I_{OUT} = -15 \text{ mA}$	V <sub>CC</sub> – 0.23	_	_	V
Output lower limit voltage	$V_{L_{R0}}$	$I_{OUT} = 0.05 \text{ mA}$	_	_	V <sub>CC</sub> – 3.0	V
Offset voltage	V <sub>OFF<sub>R0</sub></sub>		_	_	10	mV
R1 amplifier block						
Output upper limit voltage	$V_{H_{R1}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> - 0.3	_	_	V
Output lower limit voltage	$V_{L_{R1}}$	$I_{OUT} = 10 \text{ mA}$	_	_	V <sub>CC</sub> /2	V
Offset voltage	V <sub>OFF<sub>R1</sub></sub>		_	_	10	mV
R2 amplifier block	•					
Output upper limit voltage	V <sub>HR2</sub>	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> – 0.7	_	_	V
Output lower limit voltage	$V_{L_{R2}}$	$I_{OUT} = 10 \text{ mA}$	_	_	V <sub>CC</sub> /2	V
DataSheet4U.com Offset voltage	V <sub>OFF<sub>R2</sub></sub>		_	_	10	mV
R3, R4, R5, R6, R7, L3, L4, L5,		lifier block				
Output upper limit voltage 1	$V_{H_{R3-R5}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> – 1.2	_	_	V
Output upper limit voltage 2	$V_{H_{R6-R7}}$	$I_{OUT} = -15 \text{ mA}$	V <sub>CC</sub> – 1.2	_	_	V
Output lower limit voltage 1	$V_{L_{R3-R5}}$	$I_{OUT} = 10 \text{ mA}$	_	_	2	V
Output lower limit voltage 2	$V_{L_{R6-R7}}$	$I_{OUT} = 15 \text{ mA}$	_	_	2	V
Output upper limit voltage 3	$V_{H_{L3\text{-}L5}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> – 2.0	_	_	V
Output upper limit voltage 4	$V_{H_{L6\text{-}L7}}$	$I_{OUT} = -15 \text{ mA}$	V <sub>CC</sub> – 2.0	_	_	V
Output lower limit voltage 3	V <sub>LL3-L5</sub>	$I_{OUT} = 10 \text{ mA}$	_	_	1.2	V
Output lower limit voltage 4	V <sub>LL6-L7</sub>	$I_{OUT} = 15 \text{ mA}$	_	_	1.2	V
Offset voltage	V <sub>OFF34567</sub>		_		10	mV



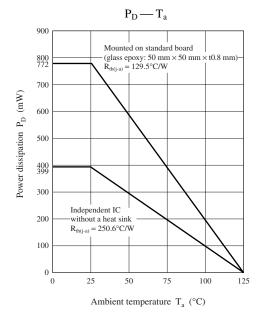
## ■ Electrical Characteristics at $T_a = 25$ °C (continued)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
L2 amplifier block							
Output upper limit voltage	$V_{H_{L2}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> / 2	_		V	
Output lower limit voltage	$V_{L_{L2}}$	$I_{OUT} = 10 \text{ mA}$	_	_	0.7	V	
Offset voltage	V <sub>OFFL2</sub>		_	_	10	mV	
L1 amplifier block							
Output upper limit voltage	$V_{H_{L1}}$	$I_{OUT} = -10 \text{ mA}$	V <sub>CC</sub> / 2	_	_	V	
Output lower limit voltage	$V_{L_{L1}}$	$I_{OUT} = 10 \text{ mA}$	_	_	0.25	V	
Offset voltage	V <sub>OFFL1</sub>		_	_	10	mV	
L0 amplifier block							
Output upper limit voltage	V <sub>HL0</sub>	$I_{OUT} = -0.05 \text{ mA}$	3	_	_	V	
Output lower limit voltage	$V_{L_{L0}}$	$I_{OUT} = 15 \text{ mA}$	_	_	0.15	V	
Offset voltage	V <sub>OFFL0</sub>		_	_	10	mV	
COM amplifier block							
Output upper limit voltage	V <sub>HCOM</sub>	$I_{OUT} = -100 \text{ mA}$	$V_{CC}$	_	_	V	
			-2.5				
Output lower limit voltage	$V_{L_{COM}}$	$I_{OUT} = 100 \text{ mA}$	_	_	2.5	V	
Offset voltage	V <sub>OFFCOM</sub>	$V_{IN} = 5 \text{ V}$	_	_	10	mV	

### ■ Technical Data

### $\bullet$ $P_D$ — $T_a$ curves of TQFP048-P-0707B

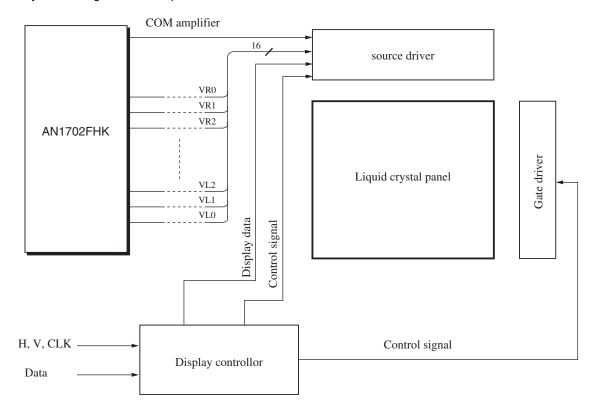
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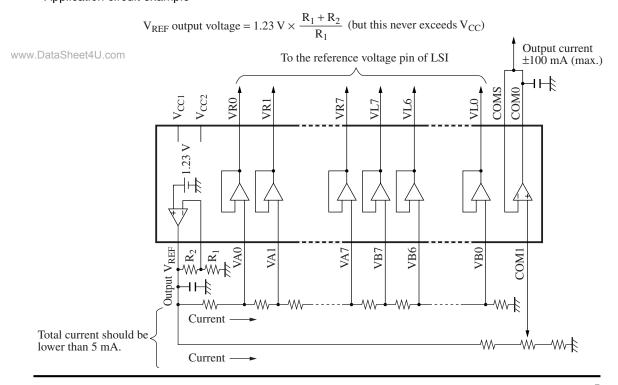
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### ■ Application Circuit Example

• System configuration example



#### • Application circuit example



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