

N-Channel 30-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

- DC/DC Conversion
- Power Routing
- Motor Drives

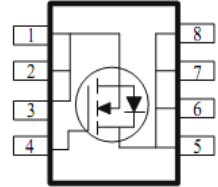
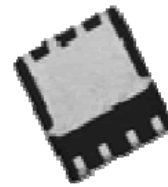
PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
30	20 @ $V_{GS} = 10V$	19
	32 @ $V_{GS} = 4.5V$	15

DFN3x3-8L



RoHS
COMPLIANT
HALOGEN
FREE



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	19	A
	$T_C = 70^\circ\text{C}$		15	
	$T_A = 25^\circ\text{C}$		11 ^a	
	$T_A = 70^\circ\text{C}$		8 ^a	
Pulsed Drain Current ^b		I_{DM}	40	
Continuous Source Current (Diode Conduction) ^a		I_S	4.5	
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	11	W
	$T_C = 70^\circ\text{C}$		7	
	$T_A = 25^\circ\text{C}$		3.5 ^a	
	$T_A = 70^\circ\text{C}$		2 ^a	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	$R_{\theta JA}$	35	$^\circ\text{C/W}$
	Steady State		81	
Maximum Junction-to-Case	Steady State	$R_{\theta JC}$	11	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

Electrical Characteristics

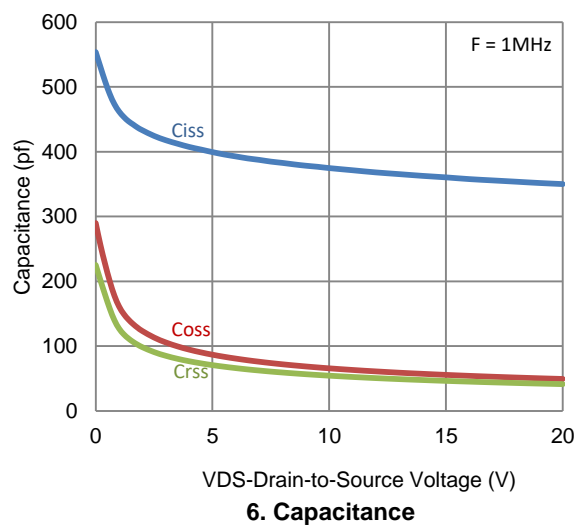
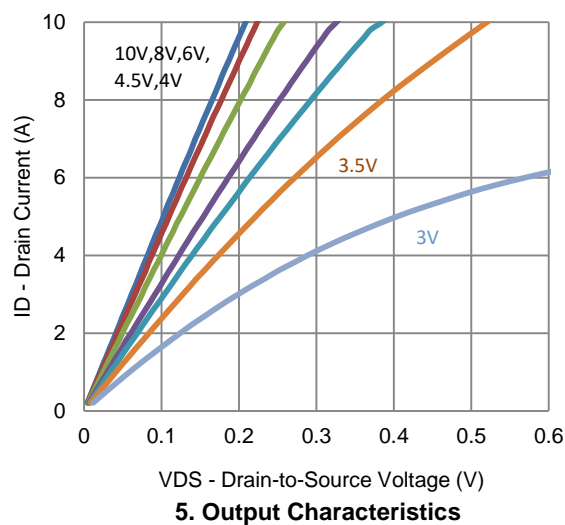
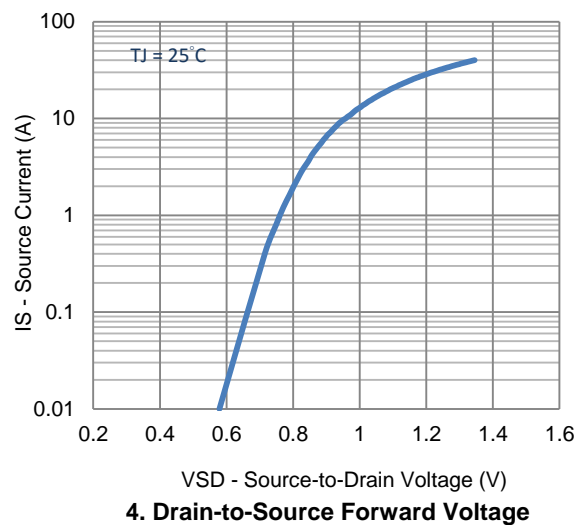
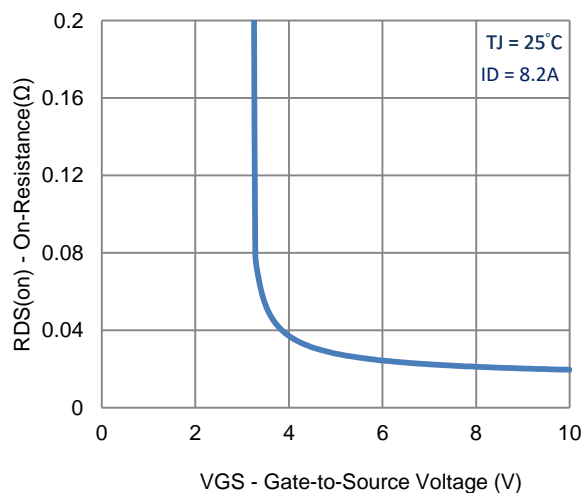
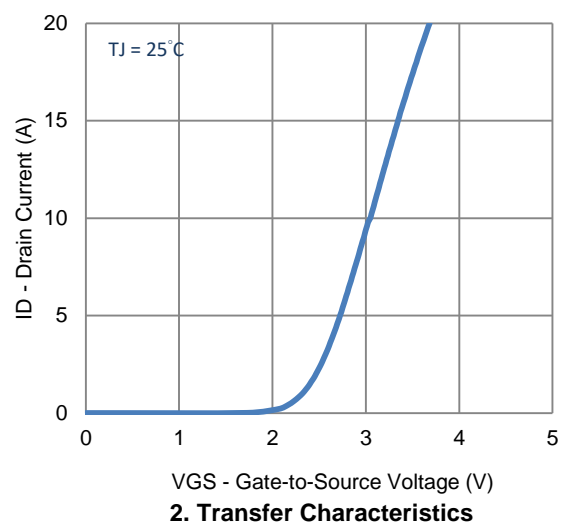
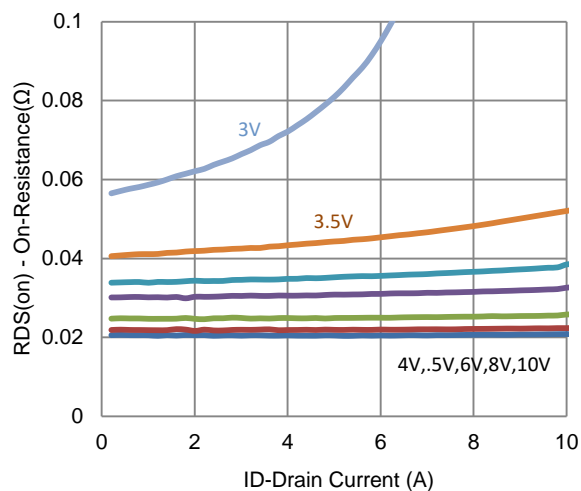
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24 V$, $V_{GS} = 0 V$			1	μA
		$V_{DS} = 24 V$, $V_{GS} = 0 V$, $T_J = 55^\circ C$			25	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 V$, $V_{GS} = 10 V$	20			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10 V$, $I_D = 8.2 A$			20	m Ω
		$V_{GS} = 4.5 V$, $I_D = 6.6 A$			32	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 V$, $I_D = 8.2 A$		12		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 2.3 A$, $V_{GS} = 0 V$		0.82		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 15 V$, $V_{GS} = 4.5 V$, $I_D = 8.2 A$		4.1		nC
Gate-Source Charge	Q_{gs}			1.1		
Gate-Drain Charge	Q_{gd}			2.0		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 15 V$, $R_L = 1.9 \Omega$, $I_D = 8.2 A$, $V_{GEN} = 10 V$, $R_{GEN} = 6 \Omega$		2		ns
Rise Time	t_r			4		
Turn-Off Delay Time	$t_{d(off)}$			16		
Fall Time	t_f			4		
Input Capacitance	C_{iss}	$V_{DS} = 15 V$, $V_{GS} = 0 V$, $f = 1 Mhz$		360		pF
Output Capacitance	C_{oss}			55		
Reverse Transfer Capacitance	C_{rss}			46		

Notes

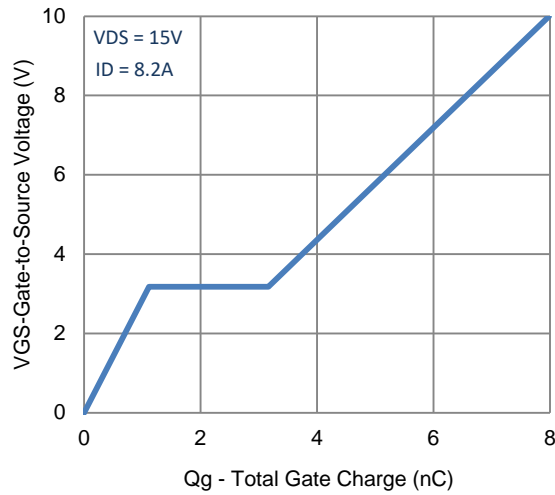
- a. Pulse test: PW \leq 300us duty cycle \leq 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

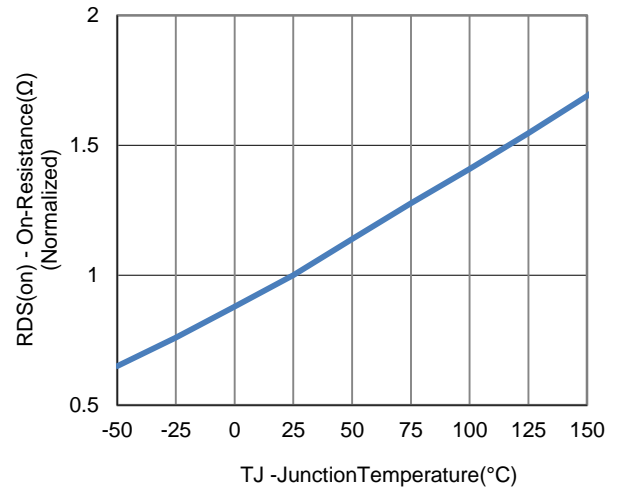
Typical Electrical Characteristics



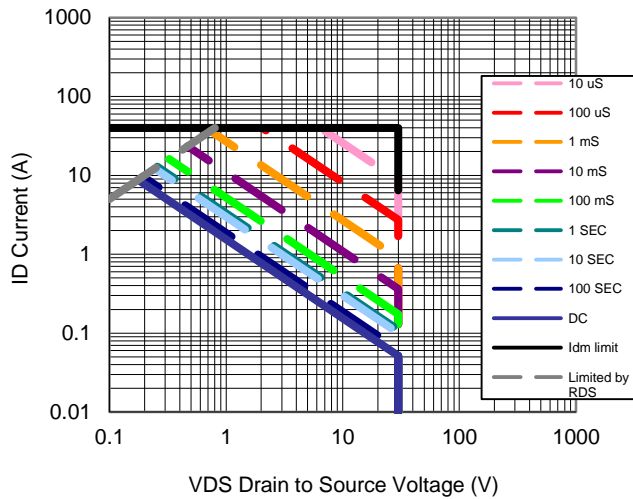
Typical Electrical Characteristics



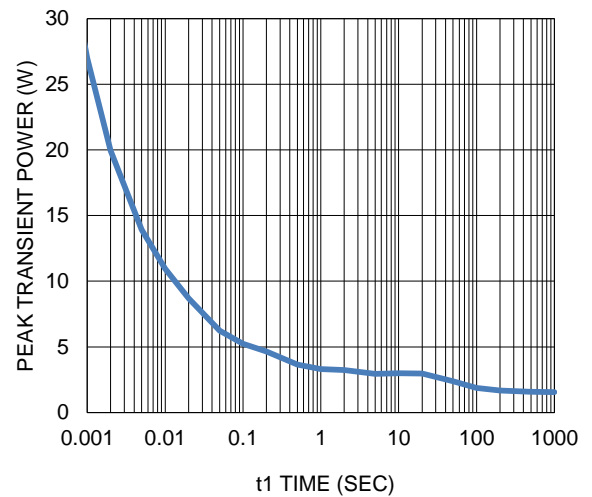
7. Gate Charge



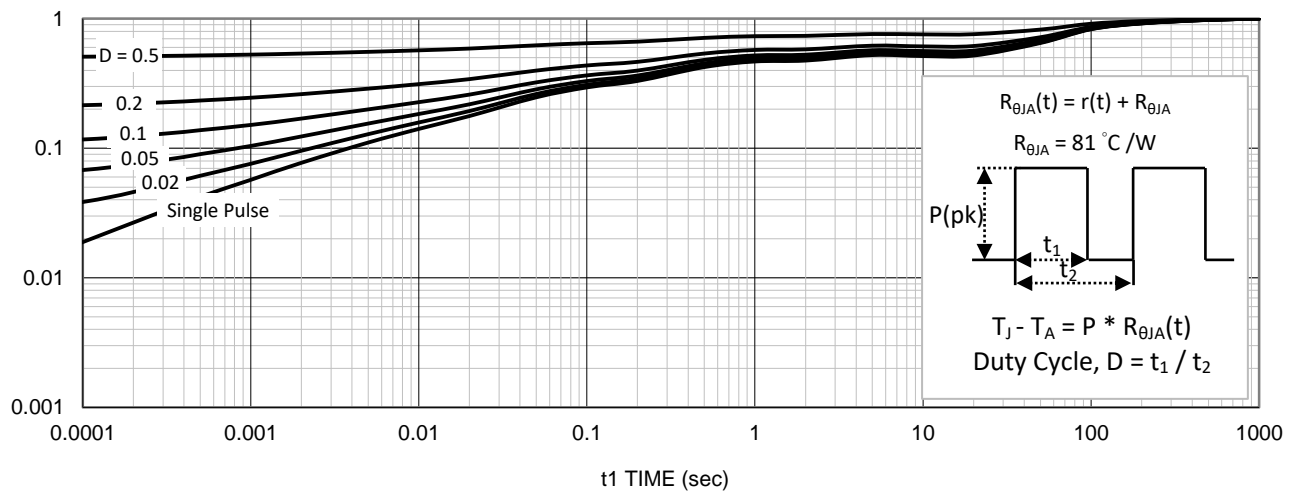
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

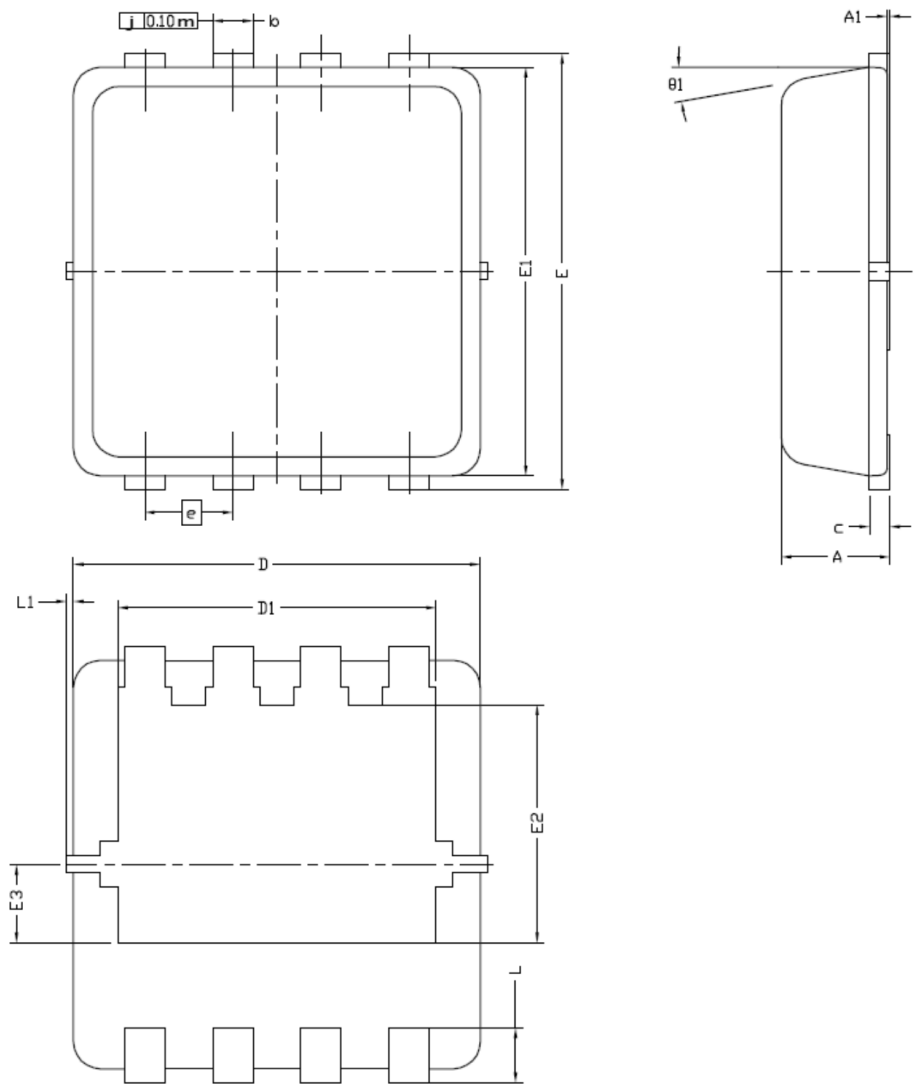


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



DIM.	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0,700	0,80	0,900	0,0276	0,0315	0,0354
A1	0,00	---	0,05	0,000	---	0,002
b	0,24	0,30	0,35	0,009	0,012	0,014
c	0,10	0,152	0,25	0,004	0,006	0,010
D	3,00 BSC			0,118 BSC		
D1	2,35 BSC			0,093 BSC		
E	3,20 BSC			0,126 BSC		
E1	3,00 BSC			0,118 BSC		
E2	1,75 BSC			0,069 BSC		
E3	0,575 BSC			0,023 BSC		
e	0,65 BSC			0,026 BSC		
L	0,30	0,40	0,50	0,0118	0,0157	0,0197
L1	0	---	0,100	0	---	0,004
θ1	0°	10°	12°	0°	10°	12°