N-Channel 60-V (D-S) MOSFET

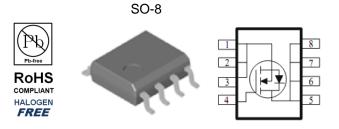
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- DC/DC Conversion Circuits
- Motor Drives

PRODUCT SUMMARY					
	Vps (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
ſ	60	14 @ V _{GS} = 10V	12		
		18 @ V _{GS} = 4.5V	11		



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage	Drain-Source Voltage			V	
Gate-Source Voltage		V _{GS}	±20	V	
Continuous Drain Current ^a	T _A =25°C		12		
Continuous Drain Current	T _A =70°C	I _D	10	А	
Pulsed Drain Current ^b		I _{DM}	48		
Continuous Source Current (Diode Conduction) ^a		I _S	4.1	А	
Power Dissipation ^a	T _A =25°C	P _D	3.1	W	
Power Dissipation ^a	T _A =70°C	'D	2.2	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	R_{\thetaJA}	40	°C/W	
	Steady State	INθJA	80		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

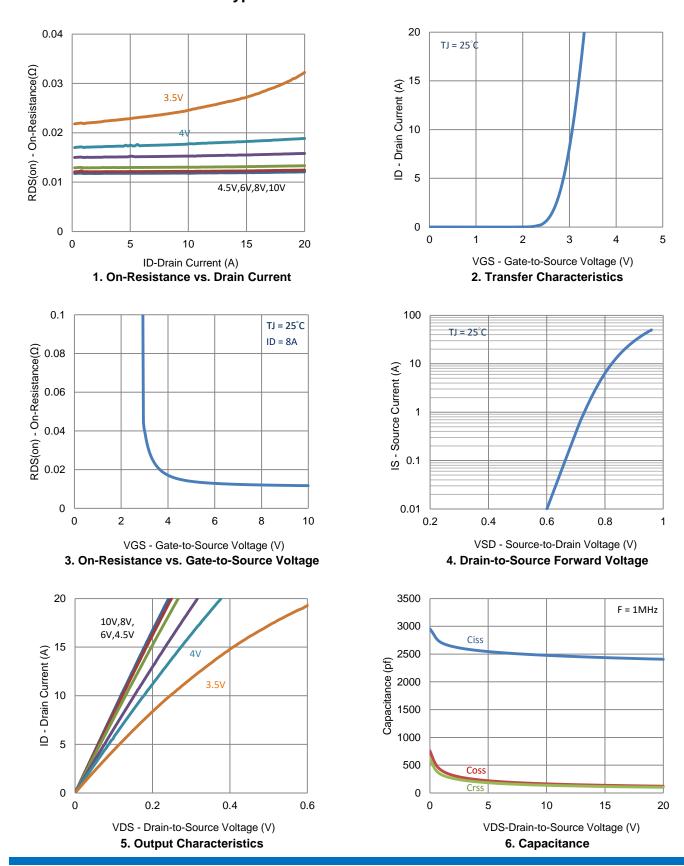
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	1	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA
	IDSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uД
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	18			А
	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 8 \text{ A}$			14	mΩ
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 6 \text{ A}$			18	11152
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 8 \text{ A}$		32		S
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 2.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.76		V
	Dynamic ^b					
Total Gate Charge	Q _g	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$		19		
Gate-Source Charge	Q _{gs}	$V_{DS} = 30 V, V_{GS} = 4.5 V,$ $I_{D} = 8 A$		7.3		nC
Gate-Drain Charge	Q_gd	ID = 0 A		6.3		
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 30 \text{ V}, \text{ R}_{1} = 3.8 \Omega,$		10		
Rise Time	t _r	$V_{\rm DS} = 30$ V, $N_{\rm L} = 3.0$ 22, $I_{\rm D} = 8$ A,		8		n 0
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		55		ns
Fall Time	t _f	$V_{\text{GEN}} = 10$ V, $V_{\text{GEN}} = 0.22$		16		
Input Capacitance	C _{iss}			2436		
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		138		pF
Reverse Transfer Capacitance	C _{rss}			116		

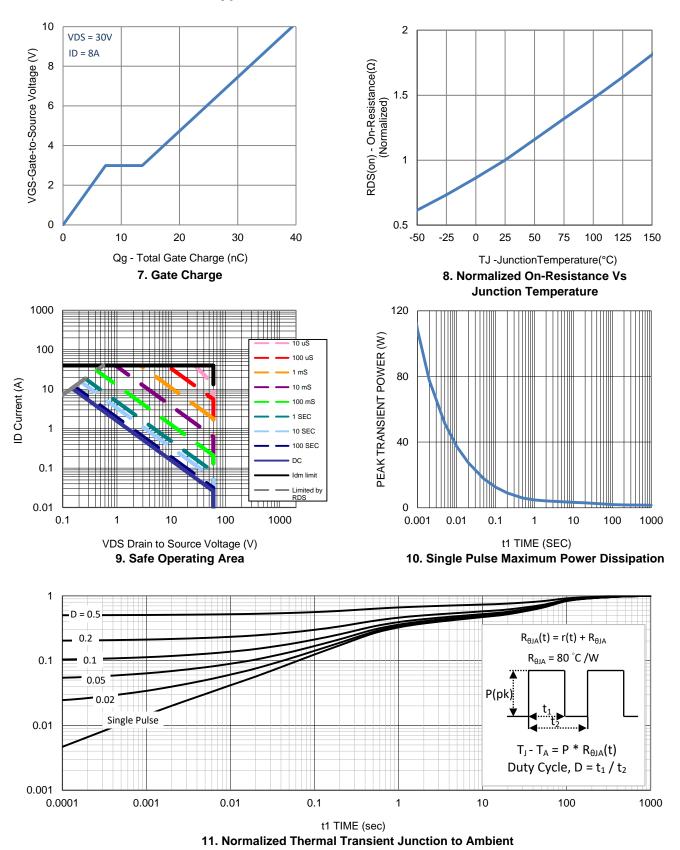
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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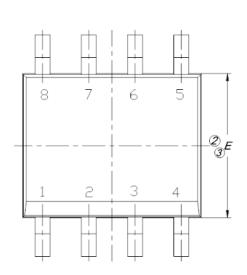
Typical Electrical Characteristics

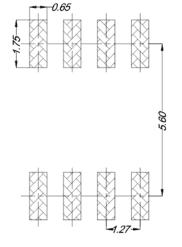


Typical Electrical Characteristics

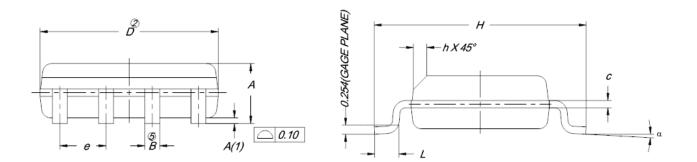
Package Information

Land Pattern (Only for Reference)





5.1.4	MILLIMETERS					
DIM.	MIN.	NOM.	MAX.			
А	1.35	1.55	1.75			
A(1)	0.10	0.18	0.25			
В	0.38	0.45	0.51			
С	0.19	0.22	0.25			
D	4.80	4.90	5.00			
E	3.80	3.90	4.00			
е		;				
н	5.80	6.00	6.20			
L	0.50	0.72	0.93			
α	0°	4°	8°			
h	0.25	0.38	0.50			



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.