

N-Channel 40-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

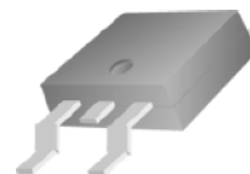
Typical Applications:

- Automotive Systems
- DC/DC Conversion Circuits
- Battery Powered Power Tools

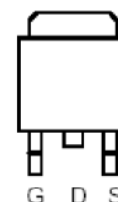
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
40	1.55 @ $V_{GS} = 10V$	400 ^a
	1.9 @ $V_{GS} = 4.5V$	



RoHS
COMPLIANT
HALOGEN
FREE



TO-263



Top View

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Units
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_C = 25^\circ\text{C}$	I_D	400	A
Pulsed Drain Current ^b		I_{DM}	1200	
Continuous Source Current (Diode Conduction) ^a	$T_C = 25^\circ\text{C}$	I_S	400	A
Power Dissipation ^a	$T_C = 25^\circ\text{C}$	P_D	300	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^c	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{\theta JC}$	0.5	

Notes

- Package Limited
- Pulse width limited by maximum junction temperature
- Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

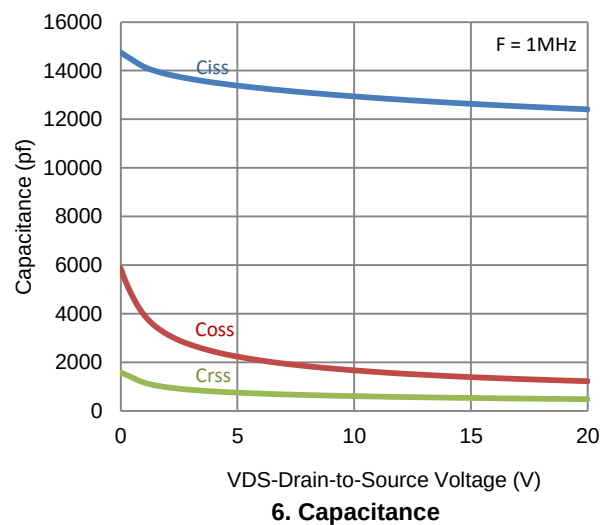
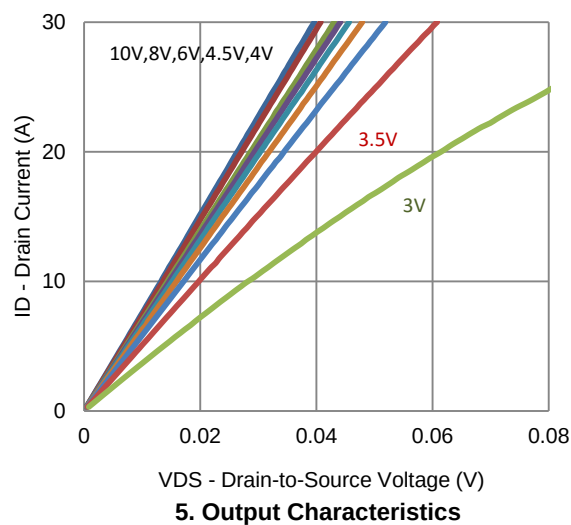
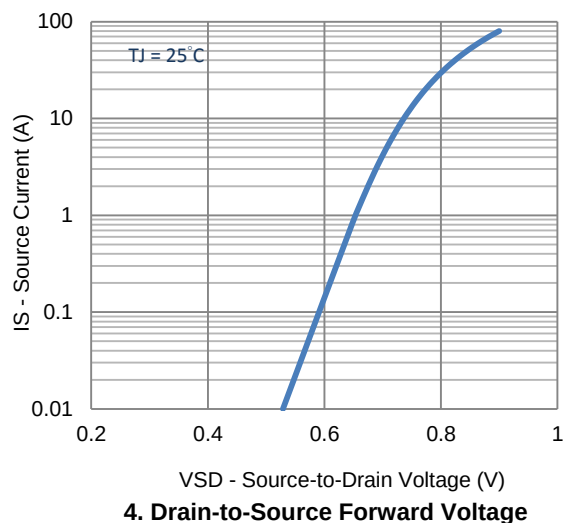
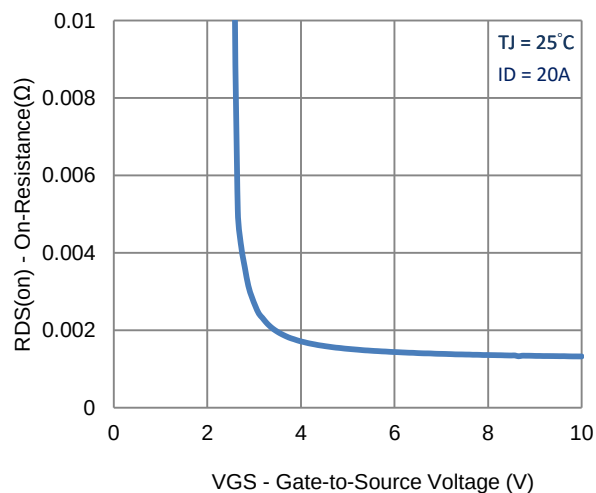
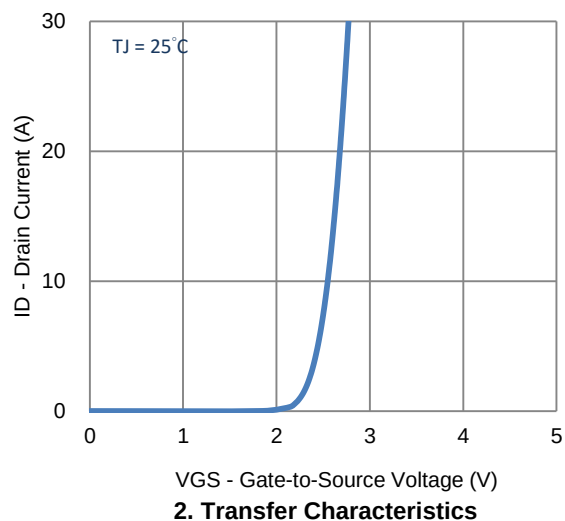
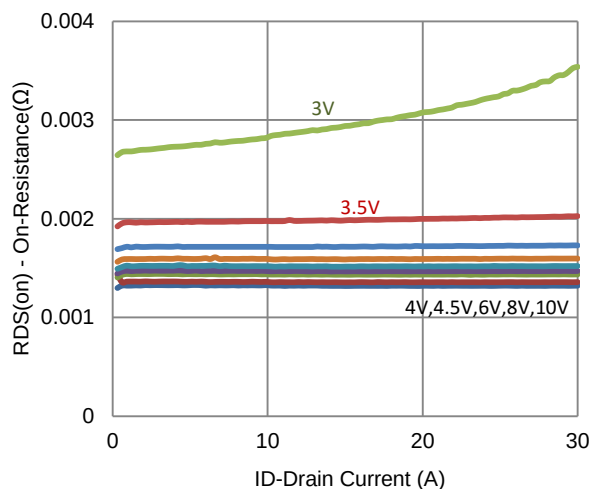
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 32 V, V_{GS} = 0 V$			1	uA
		$V_{DS} = 32 V, V_{GS} = 0 V, T_J = 55^\circ C$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	150			A
Drain-Source On-Resistance ^a	$r_{DS(on)}$	$V_{GS} = 10 V, I_D = 50 A$			1.55	mΩ
		$V_{GS} = 4.5 V, I_D = 40 A$			1.9	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 V, I_D = 30 A$		117		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 50 A, V_{GS} = 0 V$		0.85		V
Dynamic ^b						
Total Gate Charge	Q_g	$V_{DS} = 20 V, V_{GS} = 4.5 V,$ $I_D = 20 A$		118		nC
Gate-Source Charge	Q_{gs}			32		
Gate-Drain Charge	Q_{gd}			39		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 20 V, R_L = 10 \Omega,$ $I_D = 20 A,$ $V_{GEN} = 10 V, R_{GEN} = 6 \Omega$		34		ns
Rise Time	t_r			48		
Turn-Off Delay Time	$t_{d(off)}$			349		
Fall Time	t_f			132		
Input Capacitance	C_{iss}	$V_{DS} = 15 V, V_{GS} = 0 V, f = 1 Mhz$		12636		pF
Output Capacitance	C_{oss}			1390		
Reverse Transfer Capacitance	C_{rss}			533		

Notes

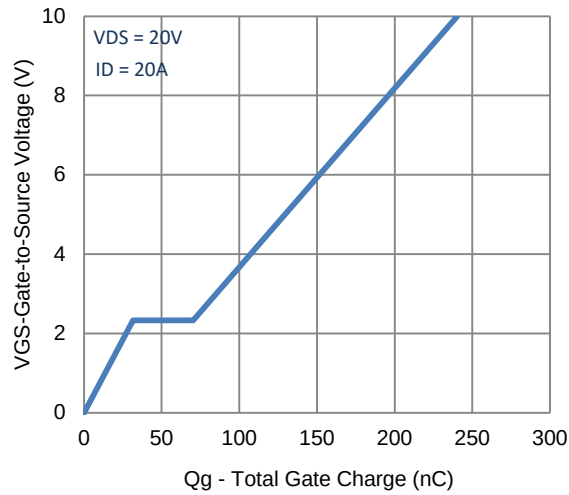
- a. Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

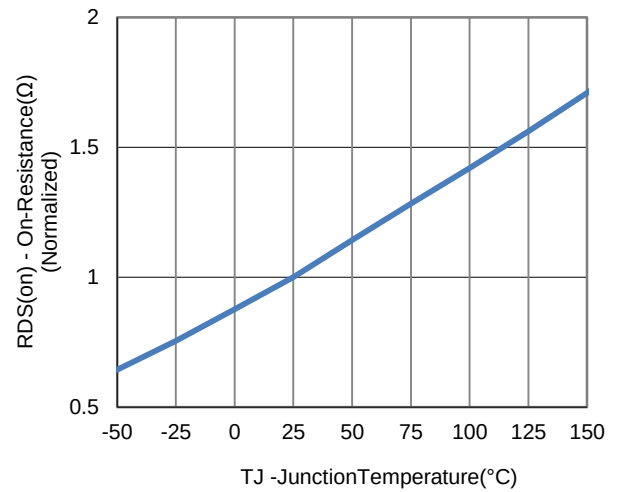
Typical Electrical Characteristics



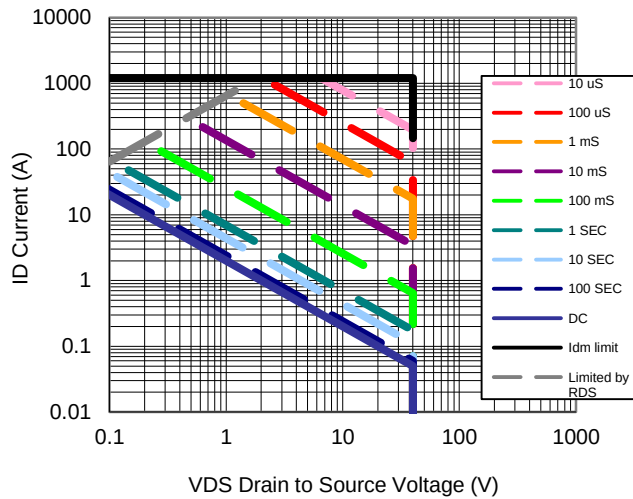
Typical Electrical Characteristics



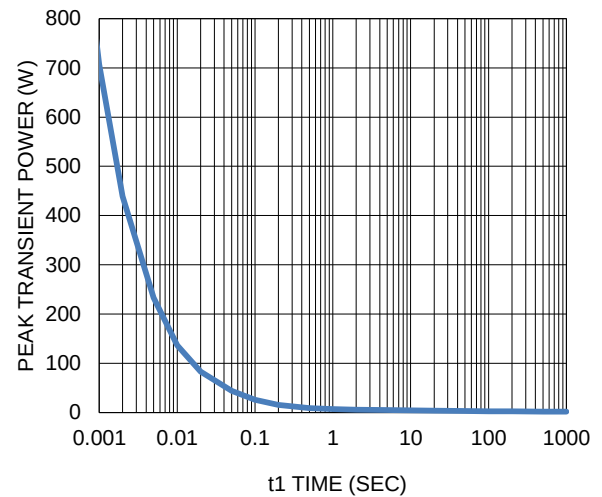
7. Gate Charge



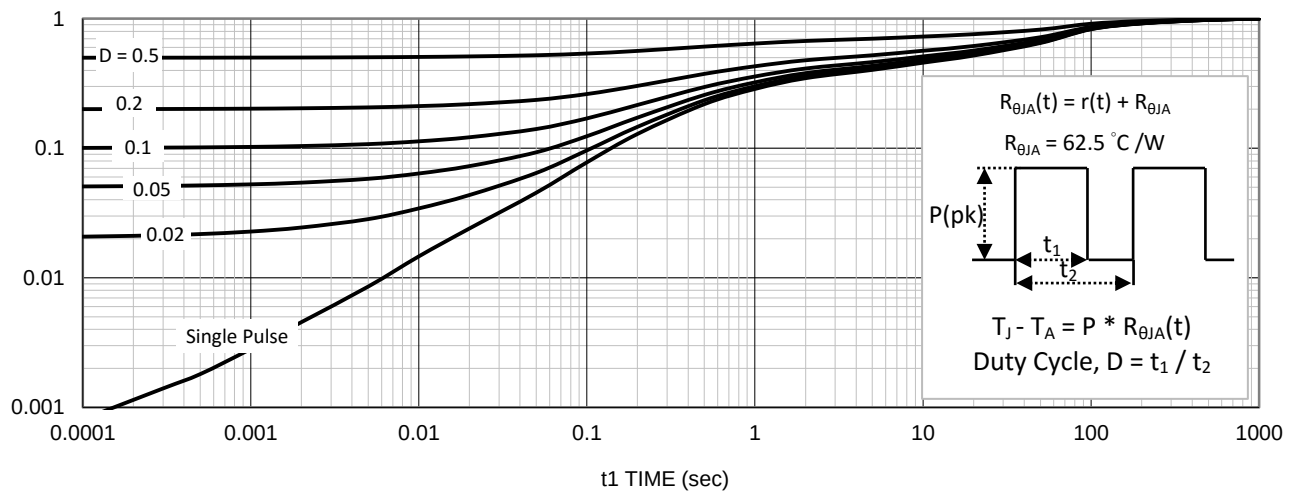
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

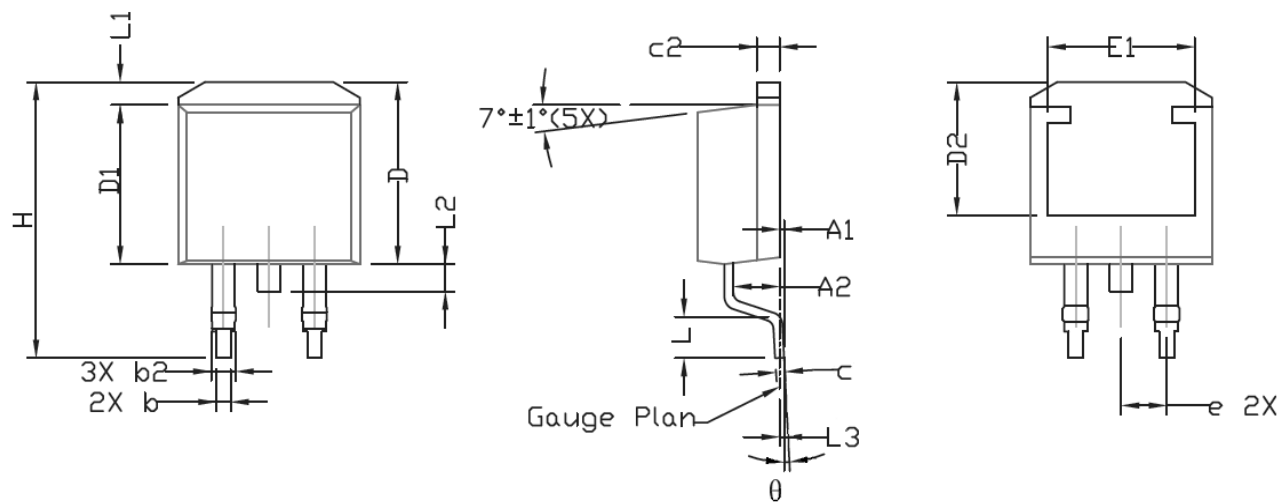


10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



SYMBOL	DIMENSIONAL REQMTS			INCHES REQMTS		
	MTN	NOM	MAX	MTN	NOM	MAX
A	4.30	4.57	4.72	0.169	0.180	0.186
A1	0	---	0.25	0	---	0.010
A2	2.47	2.57	2.67	0.097	0.101	0.105
b	0.69	0.813	0.94	0.027	0.032	0.037
b2	1.17	1.27	1.45	0.046	0.050	0.057
c	0.48	0.50	0.60	0.019	0.020	0.024
c2	1.17	1.27	1.37	0.046	0.050	0.054
D	9.80	10.05	10.30	0.386	0.396	0.406
D1	8.64	8.78	9.65	0.340	0.346	0.380
D2	7.12	7.37	7.62	0.280	0.290	0.300
E	9.70	10.15	10.54	0.382	0.400	0.415
E1	8.00	8.20	8.40	0.315	0.323	0.331
e	2.54 BSC			0.100 BSC		
H	14.99	15.24	15.49	0.590	0.600	0.610
L	1.78	2.29	2.79	0.070	0.090	0.110
L1	1.02	1.27	1.52	0.040	0.050	0.060
L2	---	---	1.75	---	---	0.069
L3	---	0.254	---	---	0.010	---
theta	0°	---	8°	0°	---	8°