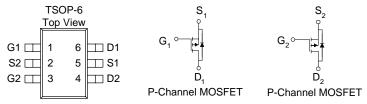
Analog Power AM3993P

P-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize High Cell Density process. Low $r_{DS(on)}$ assures minimal power loss and conserves energy, making this device ideal for use in power management circuitry. Typical applications are DC-DC converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} Provides Higher Efficiency and Extends Battery Life
- Miniature TSOP-6 Surface Mount Package Saves Board Space

PRODUCT SUMMARY					
$V_{DS}(V)$	$r_{DS(on)}$ (OHM)	$I_{D}(A)$			
-30	0.130	-2.5			
	$0.190 @ V_{GS} = -4.5V$	-1.9			



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Maximum	Units		
Drain-Source Voltage			-30	V		
Gate-Source Voltage	V_{GS}	±20	V			
Continuous Drain Current ^a	$T_A=25^{\circ}C$	I.	-2.5			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	ъ	-1.9	A		
Pulsed Drain Current ^b			-10			
Continuous Source Current (Diode Conduction) ^a			±1.6	A		
D a	$T_A=25^{\circ}C$	\mathbf{D}_{-}	1.15	W		
Power Dissipation ^a	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	Гр	0.7			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Тур	Max				
Mariana Innation to Analizata	t <= 10 sec	D	93	110	00/W		
Maximum Junction-to-Ambient ^a	Steady State	R_{thJA}	130	150	°C/W		

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Analog Power

SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)							
Domario 4 o m	Carrack of	Test Conditions	Limits			TT\$4	
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}$, $I_D = -250 \text{ uA}$	-1.00				
Gate-Body Leakage	Igss	$V_{DS} = 0 \text{ V}, V_{GS} = +/-20 \text{ V}$			±100	nA	
Zone Cota Valtana Dunin Communi	т	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$			-1] A	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			-10	uA	
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	-3			A	
A		$V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$			0.130	$+\Omega$ Γ	
Drain-Source On-Resistance ^A	fDS(on)	$V_{GS} = -4.5 \text{ V}, I_D = -1.9 \text{ A}$			0.190		
Forward Tranconductance ^A	gs	$V_{DS} = -5 \text{ V}, I_D = -2.5 \text{ A}$		3		S	
Diode Forward Voltage	V_{SD}	$I_S = -1.6 \text{ A}, V_{GS} = 0 \text{ V}$		-0.70		V	
Dynamic ^b							
Total Gate Charge	Qg			6.0			
Gate-Source Charge	Qgs	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V},$		0.80		nC	
Gate-Drain Charge	Qgd	$I_D = -2.5 \text{ A}$		1.30		1	
Input Capacitance	Ciss	P-Channel V _{DS} =-15V, V _{GS} =0V, f=1MHz		451		pF	
Output Capacitance	Coss			130			
Reverse Transfer Capacitance	Crss		1				
Turn-On Delay Time	td(on)	W SWB SOUR		6.5			
Rise Time	tr	$V_{DD} = -5 \text{ V}, \text{RL} = 5 \text{ OHM},$		20	ļ	ns	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -4.5 \text{ V}, R_G = 6 \text{ OHM}$		31			
Fall-Time	t_{f}			21			

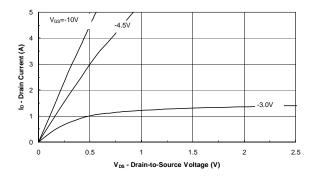
Notes

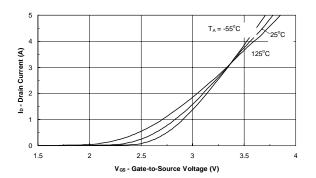
- a. Pulse test: $PW \le 300us duty cycle \le 2\%$.
- b. Guaranteed by design, not subject to production testing.

Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

2

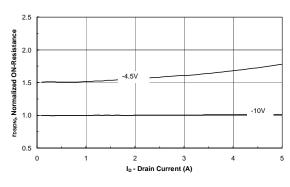
Typical Electrical Characteristics (P-Channel)

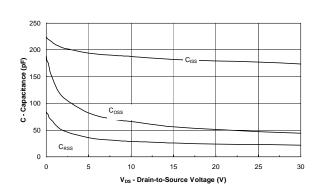




Output Characteristics

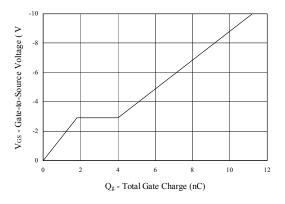


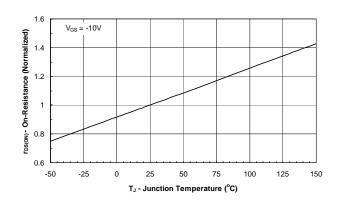




On-Resistance vs. Drain Current

Capacitance

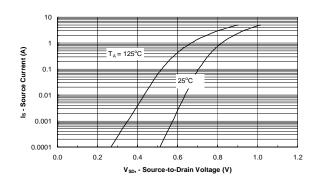


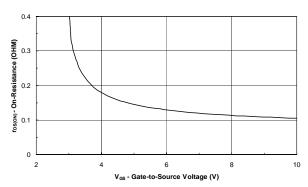


Gate Charge

On-Resistance vs. Junction Temperature

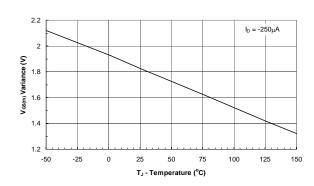
Typical Electrical Characteristics (P-Channel)

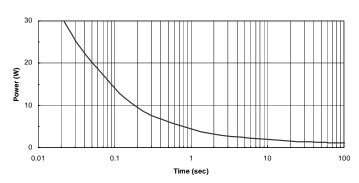


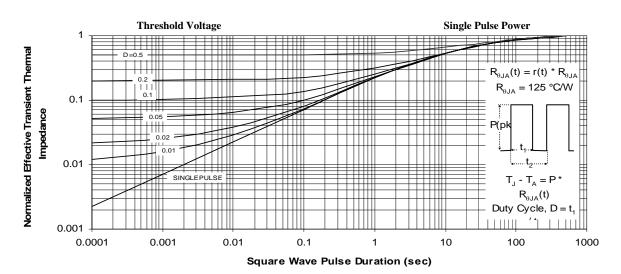


Source-Drain Diode Forward Voltage

On-Resistance vs.Gate-to Source Voltage





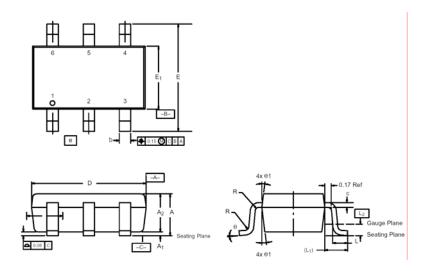


Normalized Thermal Transient Impedance, Junction-to-Ambient

Analog Power AM3993P

Package Information

TSOP-6: 6LEAD



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	_	0.043	
A ₁	0.01	_	0.10	0.0004	_	0.004	
A ₂	0.84	_	1.00	0.033	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	1.00 BSC			0.0394 BSC			
L	0.35	_	0.50	0.014	_	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	_	_	0.004	_	_	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			

5