N-Channel 150-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

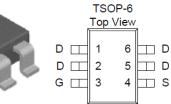
Typical Applications:

- White LED boost converters
- Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
VDS (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
150	1100 @ V _{GS} = 10V	1.1	
	1300 @ V _{GS} = 4.5V	1.0	



RoHS





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage			150	V	
Gate-Source Voltage	V _{GS}	±20	v		
Continuous Drain Current ^a	T _A =25°C	· I _D	1.1	A	
	T _A =70°C		0.9		
Pulsed Drain Current ^b	I _{DM}	10			
Continuous Source Current (Diode Conduction) ^a	I _S	1.7	А		
Power Dissinction ^a	T _A =25°C	P _D	2	W	
Power Dissipation ^a	T _A =70°C	'D	1.3		
Operating Junction and Storage Temperature Range			-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	R _{eja}	62.5	°C/W		
	Steady State	٩٢٩	110	C/VV		

Notes

- Surface Mounted on 1" x 1" FR4 Board. a.
- b. Pulse width limited by maximum junction temperature

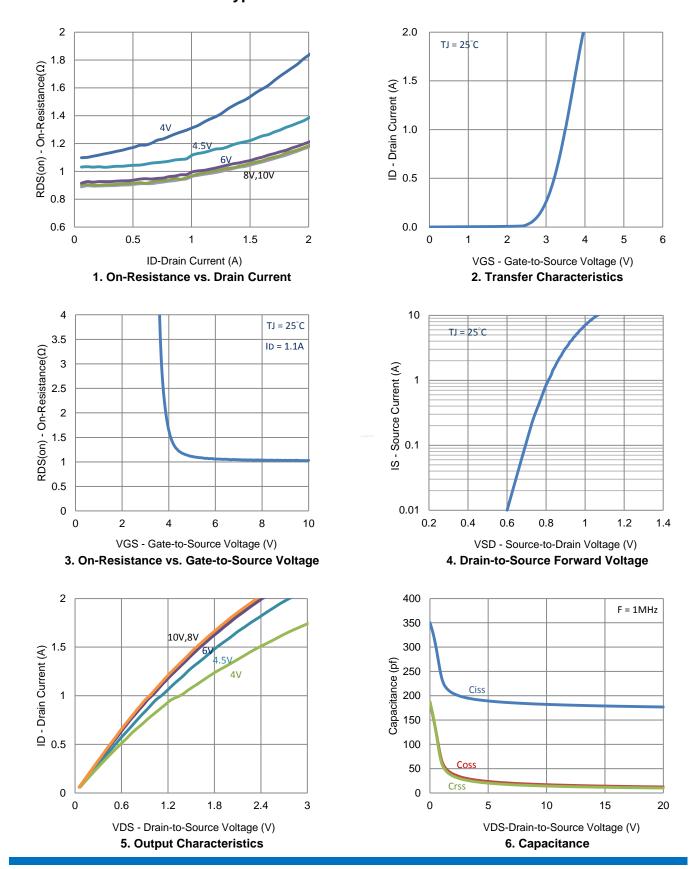
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 20 \text{ V}$			±10	uA	
Zero Gate Voltage Drain Current		$V_{DS} = 120 V, V_{GS} = 0 V$			1	uA	
	I _{DSS}	$V_{DS} = 120 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25		
On-State Drain Current	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	5			А	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 1.1 \text{ A}$			1100	mΩ	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 1 \text{ A}$			1300		
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 1.1 \text{ A}$		10		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 0.9 \text{ A}, V_{GS} = 0 \text{ V}$		0.82		V	
		Dynamic					
Total Gate Charge	Qg			3.2		nC	
Gate-Source Charge	Q_{gs} V _{DS} = 75 V, V _{GS} = 4.5 V, I _D = 1.1 A		1.0				
Gate-Drain Charge	Q_gd			1.9			
Turn-On Delay Time	t _{d(on)}	V - 75 V P - 68 O		4			
Rise Time	t _r	$V_{DS} = 75 \text{ V}, \text{ R}_{L} = 68 \Omega,$ $I_{D} = 1.1 \text{ A},$		5		ne	
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		20		ns	
Fall Time	t _f	$v_{\text{GEN}} = 10 v$, $R_{\text{GEN}} = 0.02$		7			
Input Capacitance	C _{iss}			179			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		14		pF	
Reverse Transfer Capacitance	C _{rss}	<u>1 </u>		12			

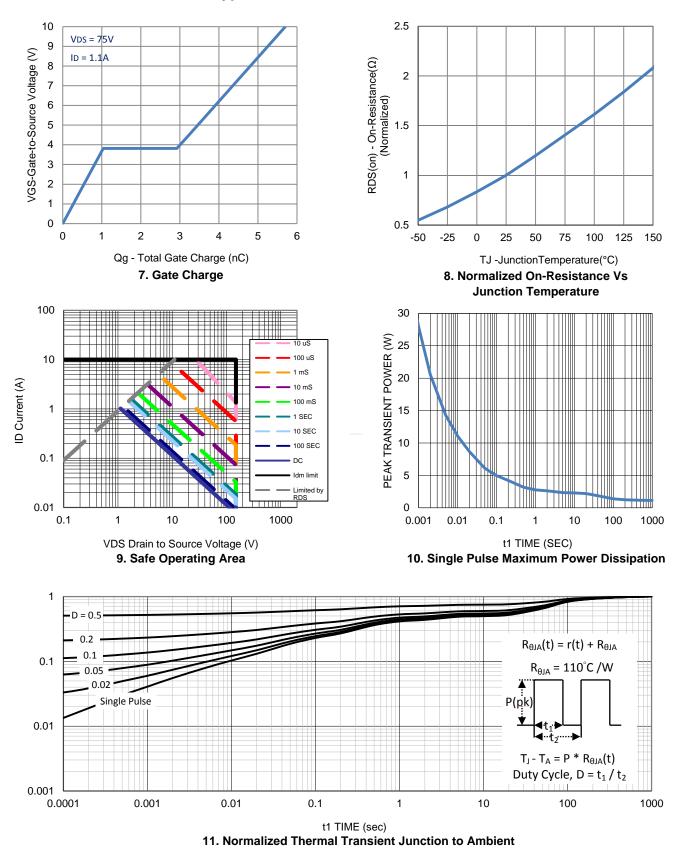
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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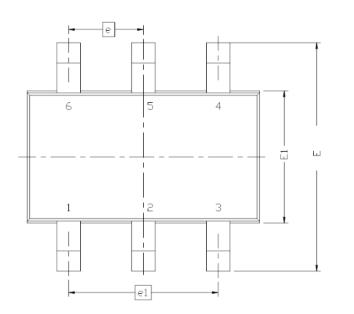
Typical Electrical Characteristics



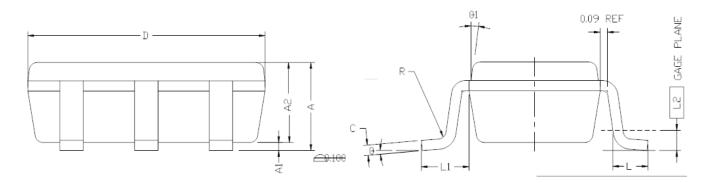
Typical Electrical Characteristics

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Package Information



DIM.	MILLIMETERS					
DIM	MIN	NDM	MAX			
Α	0.935		1.10			
A1	0.01		0.10			
A2	0.70		1.00			
b	0.25	0.40				
\subset	0.10	0.15	0.20			
D	2.95	3.05	3.10			
Ε	2.70	2.85	2.98			
E1	1.55	1.65	1.70			
e	0.95 BSC					
L	0.30		0.60			
L1	0.60REF					
L2	0.25BSC					
R	0.10					
θ	0?	4?	8?			
- 01	7? N⊡M					



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.