AM2D-N Series



2 watt dc-dc converters

- 7 PIN SIP PACKAGE
- LOW RIPPLE & NOISE
- HIGH EFFICIENCY UP TO 82%
- UL94-VO PACKAGE
- INPUT/OUTPUT ISOLATION : 1000 & 3000VDC
- OPERATING TEMPERATURE: -40°C ... +85°C
- PIN-COMPATIBLE WITH MULTIPLE MANUFACTURERS

GENERAL DESCRIPTION

Our AM2D-N series is a family of cost effective 2W single, dual output isolated DC-DC converters. These converters achieve low cost and ultra-miniature SIP7 pin size without compromising performance and reliability.

Forty eight models operate from input voltages of 5, 12 & 24VDC; producing output voltage levels of 5, 9, 12, 15, ± 5 , ± 9 , ± 12 , ± 15 VDC. Full SMD-design and a 100% production test of parameters ensures a high reliability in this product.

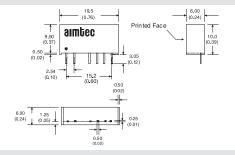
ELECTRICAL SPECIFICATIONS

Specifications typical at +25°C, nominal input voltage, rated output current unless otherwise specified

Input Specifications:		General Specifications:	
Voltage range	±10%	Efficiency	78% to 82%
Filter	Capacitor	Switching frequency	85KHz, typ. 100% load
Isolation Specifications:		Environmental Specifications	:
Rated voltage (60 sec)	1000VDC	Operating temperature (ambient)	-40°C +85°C
	3000VDC	Storage temperature	-55°C +125°C
Resistance	> 1000MOhm	Case temperature	+95°C, max.
Capacitance	60pF, typ.	Derating	None required
Output Specifications:		Humidity (non-condensing)	Up to 90%
Voltage accuracy	see tolerance	Cooling	Free-air Convection
	envelope graph	Dharical Crasifications:	
Voltage balance (dual output)	±1%	Physical Specifications:	10.50 60/7 05 10.00
Ripple & noise (at 20MHz BW)	75mVp-p, typ.	Dimensions	19.50x6.0/7.05x10.00mm
Short circuit protection	1 sec.		0.76x0.24x0.39inches
Line voltage regulation	±1.2% / 1.0% of Vin	Weight	2.5g
Load voltage regulation	±8%, load=10~100%	Case material	Plastic UL94-VO
Temperature coefficient	±0.03%/°C, max.		

MTBF: > 800,000 hrs (MIL-HDBK-217F, Ground Benign, t=+25°C) Specifications are subject to change without notification

OUTLINE DIMENSIONS & PIN CONNECTIONS



Pin	1000VDC		3000VDC	
	Single	Dual	Single	Dual
1	+V Input	+V Input	+V Input	+V Input
2	-V Input	-V Input	-V Input	-V Input
4	-V Output	-V Output	Omitted	Omitted
5	Omitted	Common	-V Output	-V Output
6	+V Output	+V Output	Omitted	Common
7	Omitted	Omitted	+V Output	+V Output

TO ORDER CALL 1-888-9-AIMTEC

AM2D-N Series

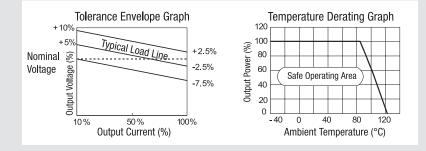
MODELS Single output

Models			Ourset Valtage	
Isolation 1000VDC	Isolation 3000VDC	Input Voltage	Ouput Voltage	Ouput Current max.
AM2D-0505S-N	AM2D-0505SH30-N	5V±10%	5VDC	400mA
AM2D-0509S-N	AM2D-0509SH30-N		9VDC	222mA
AM2D-0512S-N	AM2D-0512SH30-N	J V ±10%	12VDC	167mA
AM2D-0515S-N	AM2D-0515SH30-N		15VDC	134mA
AM2D-1205S-N	AM2D-1205SH30-N	12V±10%	5VDC	400mA
AM2D-1209S-N	AM2D-1209SH30-N		9VDC	222mA
AM2D-1212S-N	AM2D-1212SH30-N		12VDC	167mA
AM2D-1215S-N	AM2D-1215SH30-N		15VDC	134mA
AM2D-2405S-N	AM2D-2405SH30-N	24V±10%	5VDC	400mA
AM2D-2409S-N	AM2D-2409SH30-N		9VDC	222mA
AM2D-2412S-N	AM2D-2412SH30-N		12VDC	167mA
AM2D-2415S-N	AM2D-2415SH30-N		15VDC	134mA

Dual output

Models		Innut Valtage	Ounut Voltage	
Isolation 1000VDC	Isolation 3000VDC	Input Voltage	Ouput Voltage	Ouput Current max.
AM2D-0505D-N	AM2D-0505DH30-N		±5VDC	±200mA
AM2D-0509D-N	AM2D-0509DH30-N	5V±10%	±9VDC	±111mA
AM2D-0512D-N	AM2D-0512DH30-N	$5 v \pm 10 / 0$	±12VDC	±83mA
AM2D-0515D-N	AM2D-0515DH30-N		±15VDC	±67mA
AM2D-1205D-N	AM2D-1205DH30-N	12V±10%	±5VDC	±200mA
AM2D-1209D-N	AM2D-1209DH30-N		±9VDC	±111mA
AM2D-1212D-N	AM2D-1212DH30-N	$12 v \pm 10 / 0$	±12VDC	±83mA
AM2D-1215D-N	AM2D-1215DH30-N		±15VDC	±67mA
AM2D-2405D-N	AM2D-2405DH30-N		±5VDC	±200mA
AM2D-2409D-N	AM2D-2409DH30-N	24V±10%	±9VDC	±111mA
AM2D-2412D-N	AM2D-2412DH30-N	2 + v - 10/0	±12VDC	±83mA
AM2D-2415D-N	AM2D-2415DH30-N		±15VDC	±67mA

TYPICAL CHARACTERISTICS



AM2D-N Series

APPLICATION NOTE

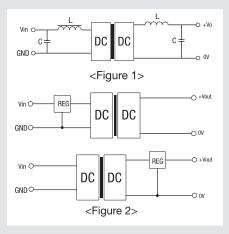
Filtering

In some circuits which are sensitive to noise and ripple, a filtering capacitor may be added to the DC/DC output end and input end to reduce the noise and ripple. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees the external capacitor table. To get an extremely low ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, which may produce a more significant filtering effect. It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference (see figure 1).

Requirement on output load

To ensure this module can operate efficiently and reliably, a minimum load is specified for this kind of DC/DC converter in addition to a maximum load (namely full load). During operation, make sure the specified range of input voltage is not exceeded, the minimum output load is not less than 10% of the full load, and that this product should never be operated under no load! If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

For single output



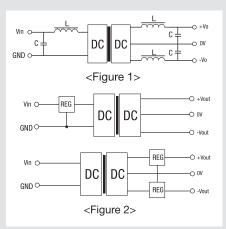
Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

Output Voltage Regulation and Over-voltage Protection Circuit

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (see Figure 2).

For dual output



External Capacitor Table for Single Output

Vin	External Capacitor	Vout	External Capacitor
5VDC	4.7uF	5VDC	10uF
12VDC	2.2uF	9VDC	4.7uF
24VDC	1uF	12VDC	2.2uF
-	-	15VDC	1uF

External Capacitor Table for Dual Output

Vin	External Capacitor	Vout	External Capacitor
5VDC	4.7uF	5VDC	4.7uF
12VDC	2.2uF	9VDC	2.2uF
24VDC	1uF	12VDC	1uF
-	-	15VDC	0.47uF